



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 175

IBM System x3250 M5
(Intel Xeon E3-1220 v3, 3.10 GHz)

SPECint_rate_base2006 = 168

CPU2006 license: 11

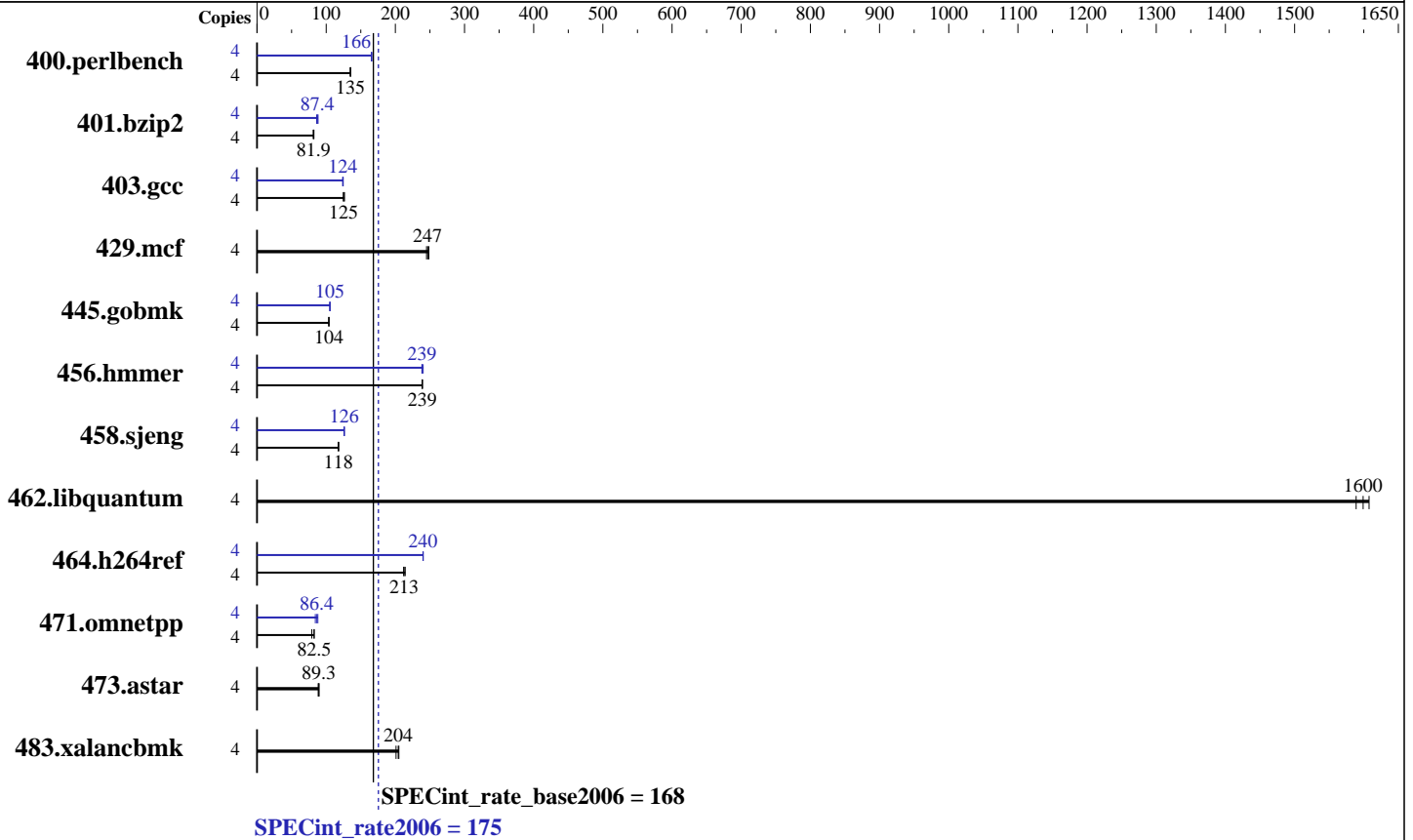
Test date: Nov-2013

Test sponsor: IBM Corporation

Hardware Availability: Dec-2013

Tested by: IBM Corporation

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E3-1220 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|------------|-------------|-------------|------------|-------------|--------|------------|-------------|-------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 289 | 135 | 290 | 135 | <u>289</u> | <u>135</u> | 4 | 235 | 166 | 237 | 165 | <u>236</u> | <u>166</u> |
| 401.bzip2 | 4 | 475 | 81.2 | <u>471</u> | <u>81.9</u> | 469 | 82.3 | 4 | 448 | 86.2 | <u>442</u> | <u>87.4</u> | 438 | 88.2 |
| 403.gcc | 4 | 255 | 126 | <u>257</u> | <u>125</u> | 258 | 125 | 4 | 258 | 125 | <u>259</u> | <u>124</u> | 260 | 124 |
| 429.mcf | 4 | <u>148</u> | <u>247</u> | 149 | 245 | 147 | 248 | 4 | <u>148</u> | <u>247</u> | 149 | 245 | 147 | 248 |
| 445.gobmk | 4 | 403 | 104 | 405 | 104 | <u>404</u> | <u>104</u> | 4 | 398 | 105 | 398 | 105 | <u>398</u> | <u>105</u> |
| 456.hammer | 4 | <u>156</u> | <u>239</u> | 156 | 239 | 156 | 239 | 4 | <u>156</u> | <u>239</u> | 157 | 238 | 155 | 240 |
| 458.sjeng | 4 | <u>410</u> | <u>118</u> | 410 | 118 | 412 | 117 | 4 | 384 | 126 | <u>384</u> | <u>126</u> | 383 | 126 |
| 462.libquantum | 4 | 52.2 | 1590 | <u>51.8</u> | <u>1600</u> | 51.6 | 1610 | 4 | 52.2 | 1590 | <u>51.8</u> | <u>1600</u> | 51.6 | 1610 |
| 464.h264ref | 4 | 413 | 214 | 418 | 212 | <u>416</u> | <u>213</u> | 4 | 369 | 240 | 369 | 240 | <u>369</u> | <u>240</u> |
| 471.omnetpp | 4 | 315 | 79.2 | <u>303</u> | <u>82.5</u> | 303 | 82.6 | 4 | <u>289</u> | <u>86.4</u> | 284 | 88.0 | 297 | 84.3 |
| 473.astar | 4 | 318 | 88.4 | 313 | 89.7 | <u>314</u> | <u>89.3</u> | 4 | 318 | 88.4 | 313 | 89.7 | <u>314</u> | <u>89.3</u> |
| 483.xalancbmk | 4 | <u>135</u> | <u>204</u> | 135 | 205 | 137 | 201 | 4 | <u>135</u> | <u>204</u> | 135 | 205 | 137 | 201 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS setting:
Operating Mode set to Custom
C-State enabled in BIOS
Sysinfo program /home/SPECcpu/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 # \$ e86d102572650a6e4d596a3cee98f191
running on x3250M5 Mon Nov 4 10:56:25 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1220 v3 @ 3.10GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with
Continued on next page



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Platform Notes (Continued)

```
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
  cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      16299396 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3250M5 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 4 10:55
```

```
SPEC is set to: /home/SPECcpu
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_x3250m5-lv_home
                ext4      852G  5.2G  804G   1% /home
```

```
Additional information from dmidecode:
BIOS IBM -[JUE109GUS-1.00]- 09/28/2013
Memory:
 2x 0000      1600 MHz
 2x Micron 18KSF1G72AZ-1G6E1 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)
"2x 0000 1600 MHz" memory information from dmidecode indicates unused DIMM slots.

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu/libs/32:/home/SPECcpu/libs/64:/home/SPECcpu/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Turbo Mode enabled in BIOS
Turbo Mode enabled in BIOS



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Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

Continued on next page



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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

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Peak Optimization Flags (Continued)

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.xml>

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For other inquiries, please contact webmaster@spec.org.

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