



# SPEC® CINT2006 Result

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## Huawei

**SPECint®2006 = 57.2**

### Huawei CH220 (Intel Xeon E5-2650 v2)

**SPECint\_base2006 = 52.4**

CPU2006 license: 3175

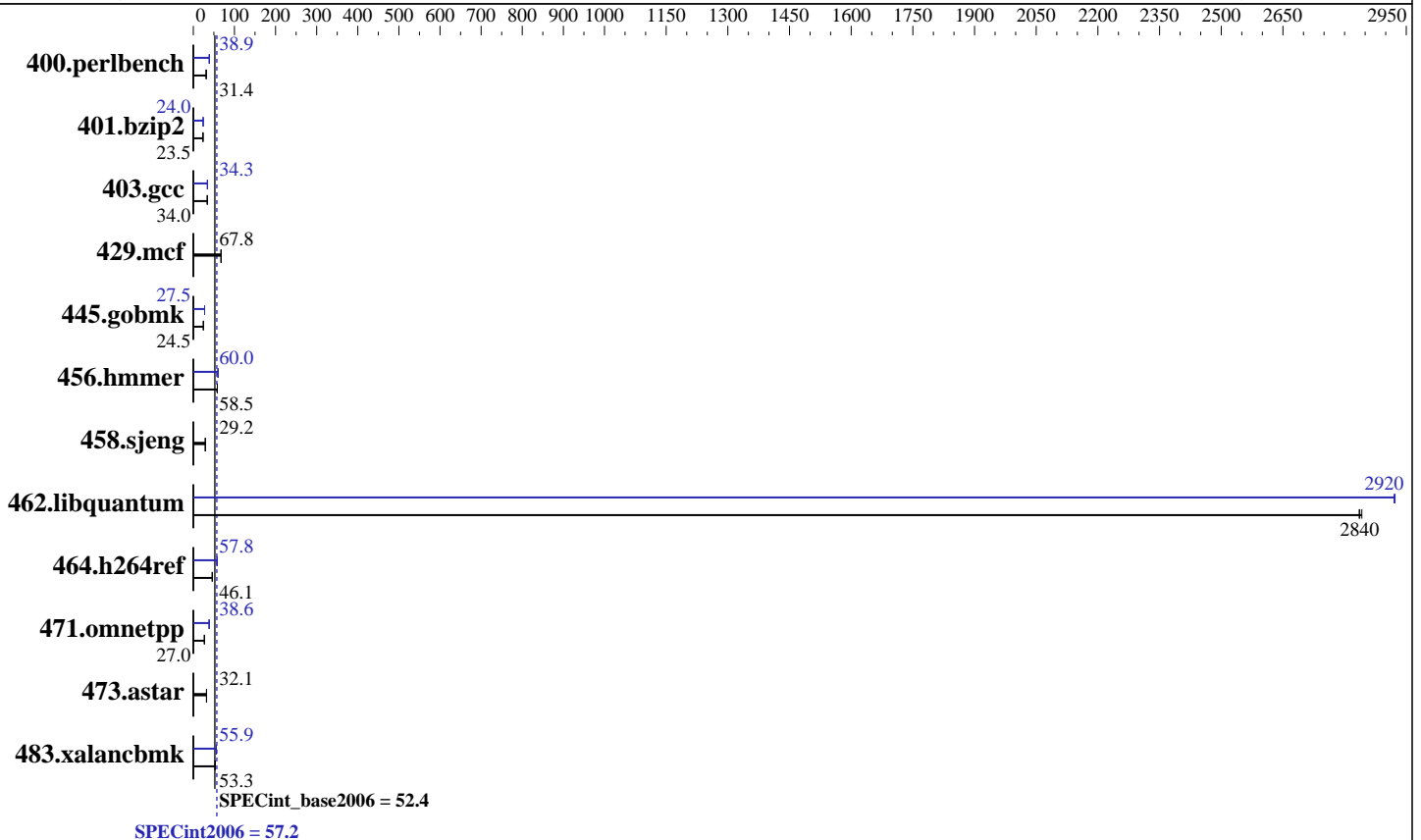
Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2650 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB SATA, 7200RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	312	31.3	312	31.4	<b>312</b>	<b>31.4</b>	251	38.9	252	38.8	<b>251</b>	<b>38.9</b>
401.bzip2	411	23.5	<b>411</b>	<b>23.5</b>	410	23.5	402	24.0	402	24.0	<b>402</b>	<b>24.0</b>
403.gcc	237	34.0	<b>237</b>	<b>34.0</b>	236	34.1	234	34.3	235	34.3	<b>235</b>	<b>34.3</b>
429.mcf	136	67.0	<b>135</b>	<b>67.8</b>	134	67.9	136	67.0	<b>135</b>	<b>67.8</b>	134	67.9
445.gobmk	<b>428</b>	<b>24.5</b>	427	24.6	429	24.5	382	27.5	<b>382</b>	<b>27.5</b>	382	27.5
456.hmmr	159	58.5	160	58.5	<b>160</b>	<b>58.5</b>	<b>156</b>	<b>60.0</b>	155	60.1	156	59.9
458.sjeng	414	29.2	414	29.2	<b>414</b>	<b>29.2</b>	414	29.2	414	29.2	<b>414</b>	<b>29.2</b>
462.libquantum	7.29	2840	7.31	2840	<b>7.30</b>	<b>2840</b>	<b>7.09</b>	<b>2920</b>	7.09	2920	7.09	2920
464.h264ref	481	46.0	<b>480</b>	<b>46.1</b>	479	46.2	383	57.8	383	57.7	<b>383</b>	<b>57.8</b>
471.omnetpp	231	27.0	<b>232</b>	<b>27.0</b>	232	26.9	<b>162</b>	<b>38.6</b>	163	38.4	162	38.6
473.astar	<b>219</b>	<b>32.1</b>	219	32.1	219	32.1	<b>219</b>	<b>32.1</b>	219	32.1	219	32.1
483.xalancbmk	130	53.0	<b>130</b>	<b>53.3</b>	129	53.3	<b>124</b>	<b>55.9</b>	123	55.9	124	55.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Set Hyper-Threading to Disabled  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Thu Aug 7 08:54:36 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v2 @ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
```

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## Platform Notes (Continued)

cache size : 20480 KB

From /proc/meminfo

MemTotal: 264480104 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/\*release\* /etc/\*version\*

redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:

Linux localhost.localdomain 2.6.32-431.el6.x86\_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Aug 6 08:45

SPEC is set to: /spec

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	ext4	265G	20G	232G	8%	/

Additional information from dmidecode:

Memory:

8x Hynix HMT42GR7AFR4C-RD 16 GB 1866 MHz 2 rank  
8x Samsung M393B2G70QH0-CMA 16 GB 1866 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,0,1"  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64"  
OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32  
445.gobmk: icc -m32

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## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch  
 -auto-p32

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## Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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