



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint®_rate2006 = 1050

Huawei CH222 V3 (Intel Xeon E5-2680 v3)

SPECint_rate_base2006 = 1020

CPU2006 license: 3175

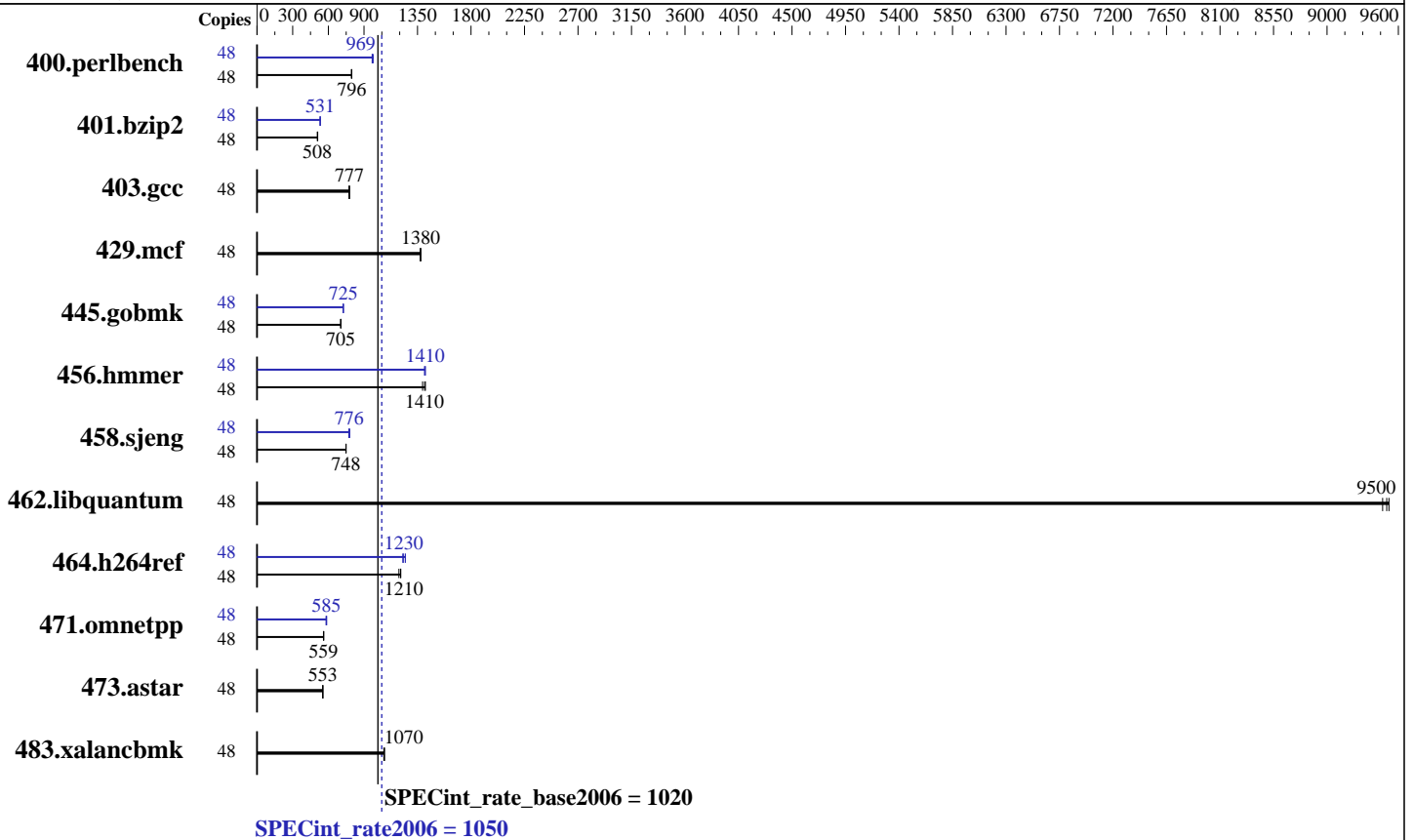
Test date: Aug-2014

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2680 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 256 GB SATA, SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei CH222 V3 (Intel Xeon E5-2680 v3)

SPECint_rate_base2006 = 1020

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Aug-2014
Hardware Availability: Sep-2014
Software Availability: Nov-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	591	794	588	797	<u>589</u>	<u>796</u>	48	<u>484</u>	<u>969</u>	479	978	484	969
401.bzip2	48	912	508	907	511	<u>911</u>	<u>508</u>	48	873	531	870	532	<u>872</u>	<u>531</u>
403.gcc	48	499	774	497	778	<u>498</u>	<u>777</u>	48	499	774	497	778	<u>498</u>	<u>777</u>
429.mcf	48	<u>318</u>	<u>1380</u>	319	1370	318	1380	48	<u>318</u>	<u>1380</u>	319	1370	318	1380
445.gobmk	48	713	706	716	703	<u>714</u>	<u>705</u>	48	<u>694</u>	<u>725</u>	695	725	693	727
456.hammer	48	322	1390	316	1420	<u>318</u>	<u>1410</u>	48	<u>317</u>	<u>1410</u>	316	1420	318	1410
458.sjeng	48	776	748	775	749	<u>776</u>	<u>748</u>	48	<u>749</u>	<u>776</u>	745	780	752	772
462.libquantum	48	104	9520	<u>105</u>	<u>9500</u>	105	9470	48	104	9520	<u>105</u>	<u>9500</u>	105	9470
464.h264ref	48	890	1190	878	1210	<u>880</u>	<u>1210</u>	48	851	1250	866	1230	<u>861</u>	<u>1230</u>
471.omnetpp	48	534	562	537	559	<u>536</u>	<u>559</u>	48	517	580	<u>513</u>	<u>585</u>	512	586
473.astar	48	608	554	<u>609</u>	<u>553</u>	610	552	48	608	554	<u>609</u>	<u>553</u>	610	552
483.xalancbmk	48	309	1070	<u>309</u>	<u>1070</u>	310	1070	48	309	1070	<u>309</u>	<u>1070</u>	310	1070

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to COD
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on xjt Fri Aug 22 11:52:41 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2680 v3 @ 2.50GHz
2 "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei CH222 V3 (Intel Xeon E5-2680 v3)

SPECint_rate_base2006 = 1020

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 12
siblings  : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 15360 KB
```

From /proc/meminfo

```
MemTotal:      264298316 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

/usr/bin/lsb_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux xjt 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Aug 22 11:51

SPEC is set to: /spec

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        ext4  222G   62G  149G  30% /
```

Additional information from dmidecode:

```
BIOS Insyde Corp. 8.09 07/14/2014
```

Memory:

```
8x NO DIMM NO DIMM      3 rank
8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 1 rank
8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei CH222 V3 (Intel Xeon E5-2680 v3)

SPECint_rate_base2006 = 1020

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Aug-2014
Hardware Availability: Sep-2014
Software Availability: Nov-2013

General Notes (Continued)

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei CH121 V3 and Huawei CH222 V3
are electronically equivalent.
The results have been measured on a Huawei CH121 V3 model

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei CH222 V3 (Intel Xeon E5-2680 v3)

SPECint_rate_base2006 = 1020

CPU2006 license: 3175

Test date: Aug-2014

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Nov-2013

Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei CH222 V3 (Intel Xeon E5-2680 v3)

SPECint_rate_base2006 = 1020

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013

Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Oct 22 12:21:20 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 22 October 2014.