



# SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

SPECint®2006 =

SPECint\_base2006

NC

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

400.perlbench |

401.bzip2 |

403.gcc |

429.mcf |

445.gobmk |

456.hmmer |

458.sjeng |

462.libquantum |

464.h264ref |

471.omnetpp |

473.astar |

483 |

CPU Name: Intel Xeon E5-2699 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
CPU MHz: 2300  
FPU: Integrated  
CPU(s) enabled: 18 cores, 1 chip, 18 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core

### Hardware

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
Compiler: Kernel 3.10.0-123.el7.x86\_64  
Auto Parallel: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
File System: Yes  
System State: xfs  
Run level 3 (multi-user)

### Software

Continued on next page

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

**SPECint2006 =**

**SPECint\_base2006**

**NC**

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

Secondary Cache:	256 KB I+D on chip per core	Bare Pointers:	32/64-bit
L3 Cache:	45 MB I+D on chip per chip	Link Pointers:	32/64-bit
Other Cache:	None	Other Software:	Microquill SmartHeap V10.0
Memory:	128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)		
Disk Subsystem:	1 x 400 GB SAS SSD, RAID 0		
Other Hardware:	None		

**Result Table**

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	NC	NC										
401.bzip2	NC	NC										
403.gcc	NC	NC										
429.mcf	NC	NC										
445.gobmk	NC	NC										
456.hammer	NC	NC										
458sjeng	NC	NC										
462.libquantum	NC	NC										
464.h264ref	NC	NC										
471.omnetpp	NC	NC										
473.astar	NC	NC										
483.xz	NC	NC										

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

**SPECint2006 =**

**SPECint\_base2006**

**NC**

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

### Platform Notes

#### BIOS Configuration:

HP Power Profile set to Custom  
HP Power Regulator set to HP Static High Performance Mode  
Minimum Processor Idle Power Package C-State set to No Package State  
Energy/Performance Bias set to Maximum Performance  
QPI Snoop Configuration set to Early Snoop  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh  
Intel Hyperthreading Option set to Disabled  
Sysinfo program /cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-21 13:51:00 +0000 \$ e3fbb8667b5a285932ceab81e28219e1  
running on W-b1460c\_gen9-VP2 1 Mon Feb 2 14:30:55 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz  
 1 "physical" "s" (chips)  
 18 "processor"  
cores, siblings Caution: counting these is hw and system dependent. The following counts from /proc/cpuinfo might not be reliable. Use with caution!  
 cores : 18  
 siblings : 18  
 physical 0: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
 cache size : 46080 KB

From /proc/meminfo  
MemTotal: 131733928 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*  
os-release:  
NAME="Red Hat Enterprise Linux Server"  
VERSION="7.0 (Maipo)"

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

**SPECint2006 =**

**SPECint\_base2006 = NC**

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

### Platform Notes (Continued)

```
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux W-bl460c_gen9-VP2.1 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT
2014 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 2 14:26
```

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   277G  137G  141G  50% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined, but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard."

```
BIOS HP 150 08/26/2014
```

```
Memory:
8x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz
8x UNKNOWN NOT AVAILABLE
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:

8x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

**SPECint2006 =**

**SPECint\_base2006**

**NC**

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

### General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/cpu2006/lib32:/cpu2006/lib64:/cpu2006/sh"

OMP\_NUM\_THREADS = "18"

Binaries compiled on a system with 1x Intel Xeon E5-4670K CPU + 16GB memory using RedHat EL 7.0

### Base Compiler Invocation

C benchmarks:

    icc -m64

C++ benchmarks:

    icpc -m64

### Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.tonto: -DSPEC\_CPU\_LP64  
450.numerical: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

**SPECint2006 =**

**SPECint\_base2006**

**NC**

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div parallel opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: cc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m64

471.craftpp: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

**SPECint2006 =**

**SPECint\_base2006 = 1000 NC**

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

## Peak Portability Flags (Continued)

```
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias
401.bzip2: basepeak = yes
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
          -f16-malloc-options=3 -auto-ilp32
429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
          -opt-prefetch -auto-p32
455.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias
456.hmmer: basepeak = yes
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll14
```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

**SPECint2006 =**

**SPECint\_base2006 =**

**NC**

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 1) -prof-use(pass 2)  
-opt-ra-regalloc-strategy=block -ansi-alias  
-Wl,-z,muldefs,L/sh -lstarheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca\_a=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant BL460c Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

**SPECint2006 =**

**SPECint\_base2006 = 10 NC**

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Jun-2014

**SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.**

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Nov 3 17:48:06 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 7 April 2015.