



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint®_rate2006 = 429

Huawei XH622 V3 (Intel Xeon E5-2623 v3)

SPECint_rate_base2006 = 411

CPU2006 license: 3175

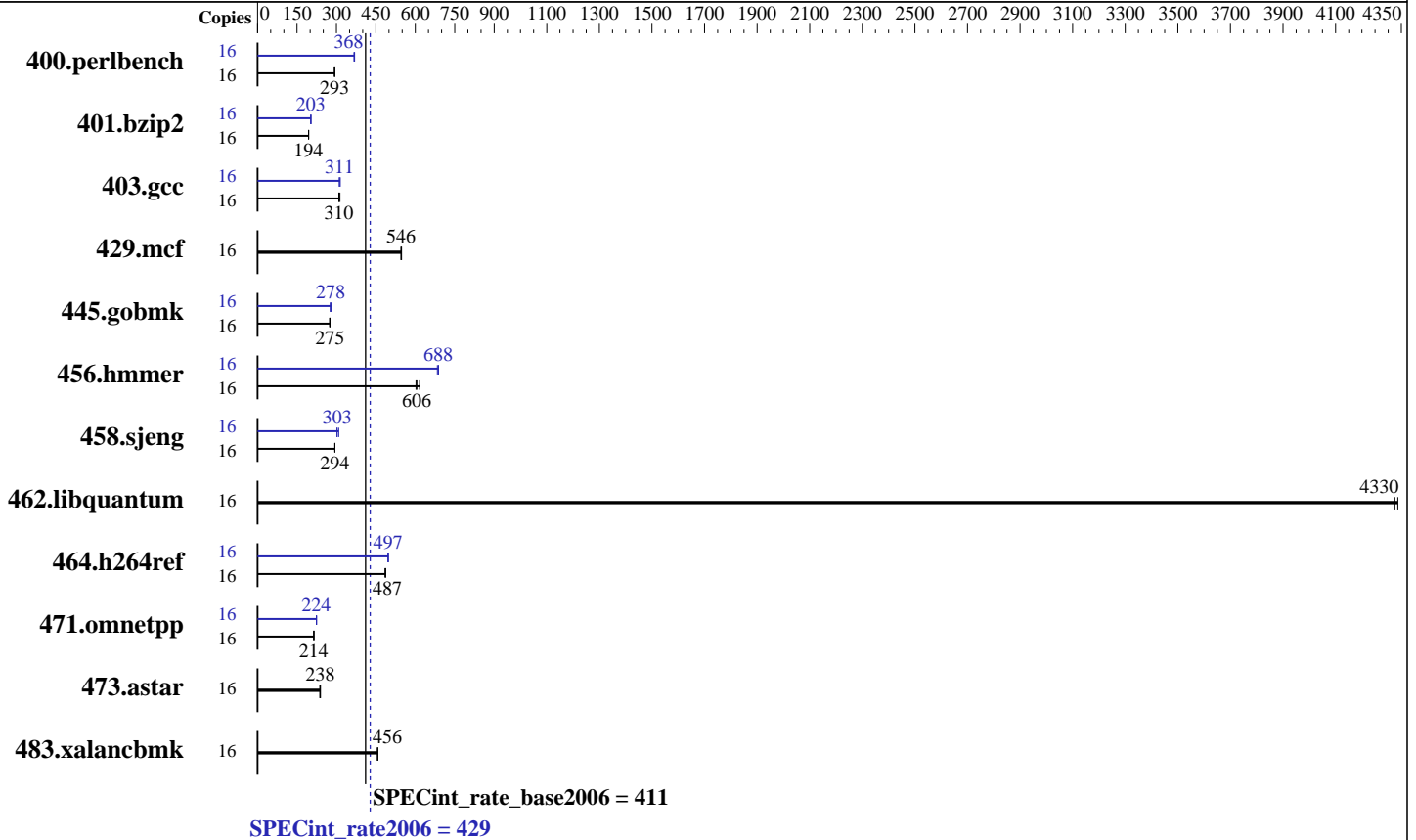
Test date: Mar-2015

Test sponsor: Huawei

Hardware Availability: Sep-2014

Tested by: Huawei

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2623 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 3.10.0-123.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 429

Huawei XH622 V3 (Intel Xeon E5-2623 v3)

SPECint_rate_base2006 = 411

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>533</u>	<u>293</u>	532	294	535	292	16	<u>425</u>	<u>368</u>	426	367	424	369
401.bzip2	16	794	194	<u>795</u>	<u>194</u>	797	194	16	761	203	762	203	<u>762</u>	<u>203</u>
403.gcc	16	416	310	<u>416</u>	<u>310</u>	411	313	16	414	311	<u>413</u>	<u>311</u>	410	314
429.mcf	16	267	546	266	548	<u>267</u>	<u>546</u>	16	267	546	266	548	<u>267</u>	<u>546</u>
445.gobmk	16	<u>610</u>	<u>275</u>	610	275	611	275	16	<u>604</u>	<u>278</u>	602	279	606	277
456.hammer	16	242	616	<u>246</u>	<u>606</u>	248	603	16	218	684	<u>217</u>	<u>688</u>	217	688
458.sjeng	16	658	294	<u>658</u>	<u>294</u>	659	294	16	627	309	<u>640</u>	<u>303</u>	640	302
462.libquantum	16	76.7	4320	76.4	4340	<u>76.7</u>	<u>4330</u>	16	76.7	4320	76.4	4340	<u>76.7</u>	<u>4330</u>
464.h264ref	16	727	487	<u>728</u>	<u>487</u>	730	485	16	712	497	714	496	<u>713</u>	<u>497</u>
471.omnetpp	16	<u>467</u>	<u>214</u>	466	215	469	213	16	<u>447</u>	<u>224</u>	447	224	445	225
473.astar	16	474	237	469	239	<u>472</u>	<u>238</u>	16	474	237	469	239	<u>472</u>	<u>238</u>
483.xalancbmk	16	<u>242</u>	<u>456</u>	242	456	241	457	16	<u>242</u>	<u>456</u>	242	456	241	457

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Set Snoop Mode to ES mode

Sysinfo program /spec15/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Fri Mar 27 05:41:16 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2623 v3 @ 3.00GHz

2 "physical id"s (chips)

16 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>

Page 2



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 429

Huawei XH622 V3 (Intel Xeon E5-2623 v3)

SPECint_rate_base2006 = 411

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Platform Notes (Continued)

```

caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
  physical 1: cores 0 1 2 3
  cache size : 10240 KB

```

```

From /proc/meminfo
MemTotal:      263579372 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 27 05:35

```

SPEC is set to: /spec15
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2        ext4      448G      242G  184G  57% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 1.20 10/25/2014

Memory:

```

8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz, configured at 1867 MHz
8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1867 MHz

```

(End of data from sysinfo program)



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 429

Huawei XH622 V3 (Intel Xeon E5-2623 v3)

SPECint_rate_base2006 = 411

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

The Huawei XH622 V3 and Huawei XH628 V3

are electronically equivalent.

The results have been measured on a Huawei XH628 V3 model.

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 429

Huawei XH622 V3 (Intel Xeon E5-2623 v3)

SPECint_rate_base2006 = 411

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 429

Huawei XH622 V3 (Intel Xeon E5-2623 v3)

SPECint_rate_base2006 = 411

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Peak Optimization Flags (Continued)

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 429

Huawei XH622 V3 (Intel Xeon E5-2623 v3)

SPECint_rate_base2006 = 411

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Apr 21 18:21:03 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 April 2015.