



SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL660c Gen9
(2.20 GHz, Intel Xeon E5-4669 v4)

SPECint®2006 = 64.9

SPECint_base2006 = 63.2

CPU2006 license: 3

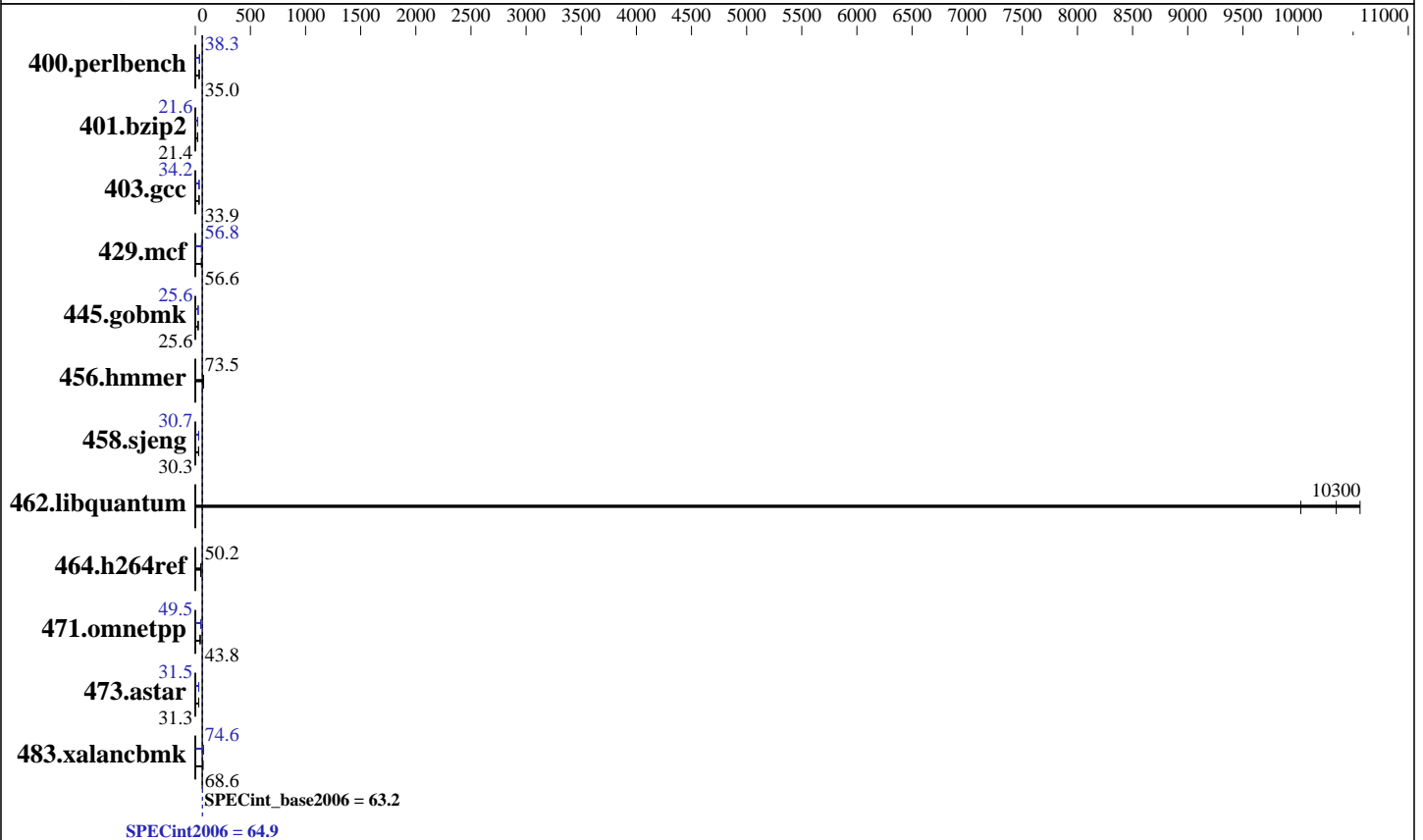
Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-4669 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 88 cores, 4 chips, 22 cores/chip
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 55 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)
Kernel 3.10.0-327.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL660c Gen9

(2.20 GHz, Intel Xeon E5-4669 v4)

SPECint2006 = 64.9

SPECint_base2006 = 63.2

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>279</u>	<u>35.0</u>	279	35.0	279	35.0	255	38.3	255	38.3	<u>255</u>	<u>38.3</u>
401.bzip2	452	21.4	<u>452</u>	<u>21.4</u>	454	21.3	446	21.6	<u>446</u>	<u>21.6</u>	446	21.6
403.gcc	<u>237</u>	<u>33.9</u>	237	33.9	238	33.9	235	34.2	235	34.3	<u>235</u>	<u>34.2</u>
429.mcf	<u>161</u>	<u>56.6</u>	162	56.5	160	56.9	161	56.5	160	57.1	<u>161</u>	<u>56.8</u>
445.gobmk	<u>409</u>	<u>25.6</u>	410	25.6	409	25.6	<u>410</u>	<u>25.6</u>	410	25.6	410	25.6
456.hammer	<u>127</u>	<u>73.5</u>	127	73.5	127	73.6	<u>127</u>	<u>73.5</u>	127	73.5	127	73.6
458.sjeng	399	30.3	399	30.3	<u>399</u>	<u>30.3</u>	394	30.7	394	30.7	<u>394</u>	<u>30.7</u>
462.libquantum	1.96	10600	2.07	10000	<u>2.00</u>	<u>10300</u>	1.96	10600	2.07	10000	<u>2.00</u>	<u>10300</u>
464.h264ref	<u>440</u>	<u>50.2</u>	441	50.2	440	50.3	<u>440</u>	<u>50.2</u>	441	50.2	440	50.3
471.omnetpp	143	43.6	<u>143</u>	<u>43.8</u>	140	44.5	126	49.6	126	49.5	<u>126</u>	<u>49.5</u>
473.astar	224	31.3	<u>224</u>	<u>31.3</u>	225	31.2	223	31.5	225	31.2	<u>223</u>	<u>31.5</u>
483.xalancbmk	100	68.7	<u>101</u>	<u>68.6</u>	101	68.4	<u>92.4</u>	<u>74.6</u>	92.6	74.6	92.3	74.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration:

Intel Hyperthreading Option set to Disabled

Power Profile set to Custom

Power Regulator set to Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package state

Collaborative Power Control set to Disabled

QPI Snoop Configuration set to Home Snoop

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Sysinfo program /home/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on BL660-Gen9-B Mon May 30 00:37:00 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>

Page 2



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL660c Gen9
(2.20 GHz, Intel Xeon E5-4669 v4)

SPECint2006 = 64.9

SPECint_base2006 = 63.2

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4669 v4 @ 2.20GHz
 4 "physical id"s (chips)
 88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 22
  siblings  : 22
  physical 0: cores 0 2 3 4 8 10 11 12 16 17 18 19 20 21 24 25 26 27 28
  physical 1: cores 0 2 3 4 8 10 11 12 16 17 18 19 20 21 24 25 26 27 28
  physical 2: cores 0 2 3 4 8 10 11 12 16 17 18 19 20 21 24 25 26 27 28
  physical 3: cores 0 2 3 4 8 10 11 12 16 17 18 19 20 21 24 25 26 27 28
cache size : 56320 KB

```

```

From /proc/meminfo
MemTotal:      528057312 kB
HugePages_Total:      0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux BL660-Gen9-B 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 May 30 00:31

```

SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda5       xfs   318G  16G  303G   5% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I38 05/05/2016

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL660c Gen9
(2.20 GHz, Intel Xeon E5-4669 v4)

SPECint2006 = 64.9

SPECint_base2006 = 63.2

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Platform Notes (Continued)

Memory:

16x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "88"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL660c Gen9
(2.20 GHz, Intel Xeon E5-4669 v4)

SPECint2006 = 64.9

SPECint_base2006 = 63.2

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

`445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

C++ benchmarks (except as noted below):

`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

`473.astar: icpc -m64`

Peak Portability Flags

`400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`

`401.bzip2: -DSPEC_CPU_LP64`

`403.gcc: -DSPEC_CPU_LP64`

`429.mcf: -DSPEC_CPU_LP64`

`445.gobmk: -D_FILE_OFFSET_BITS=64`

`456.hmmmer: -DSPEC_CPU_LP64`

`458.sjeng: -DSPEC_CPU_LP64`

`462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

`464.h264ref: -DSPEC_CPU_LP64`

`471.omnetpp: -D_FILE_OFFSET_BITS=64`

`473.astar: -DSPEC_CPU_LP64`

`483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL660c Gen9

(2.20 GHz, Intel Xeon E5-4669 v4)

SPECint2006 =

64.9

SPECint_base2006 =

63.2

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL660c Gen9
(2.20 GHz, Intel Xeon E5-4669 v4)

SPECint2006 = 64.9

SPECint_base2006 = 63.2

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jun 21 18:04:36 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 June 2016.