



# SPEC® CINT2006 Result

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## Huawei

SPECint®\_rate2006 = 1360

Huawei RH2288H V3 (Intel Xeon E5-2683 v4)

SPECint\_rate\_base2006 = 1300

CPU2006 license: 3175

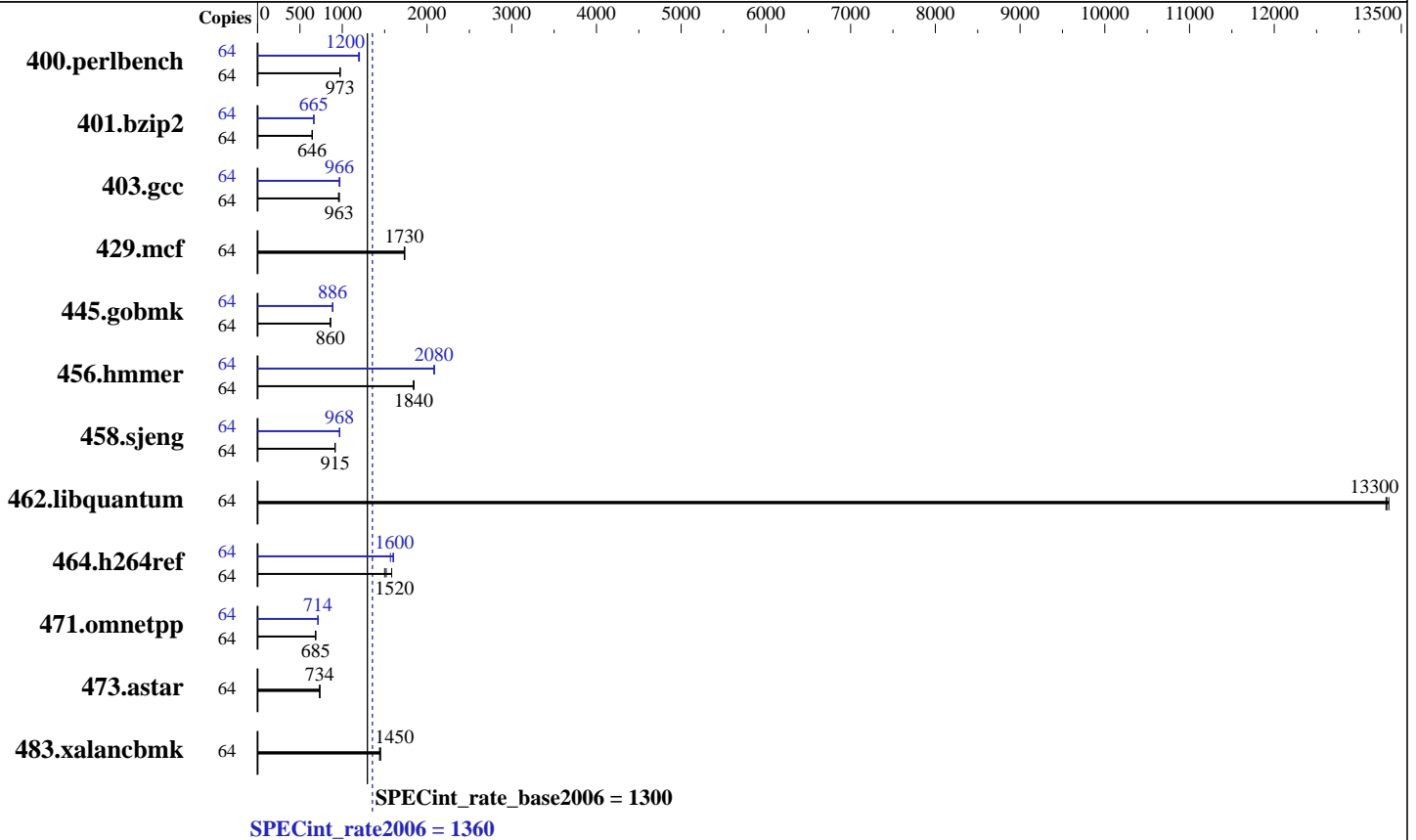
Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Nov-2015



### Hardware

CPU Name: Intel Xeon E5-2683 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 40 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	640	976	<b><u>642</u></b>	<b><u>973</u></b>	643	972	64	520	1200	<b><u>522</u></b>	<b><u>1200</u></b>	524	1190
401.bzip2	64	958	645	<b><u>956</u></b>	<b><u>646</u></b>	956	646	64	929	665	931	664	<b><u>929</u></b>	<b><u>665</u></b>
403.gcc	64	539	955	<b><u>535</u></b>	<b><u>963</u></b>	534	964	64	530	972	534	964	<b><u>533</u></b>	<b><u>966</u></b>
429.mcf	64	337	1730	336	1740	<b><u>337</u></b>	<b><u>1730</u></b>	64	337	1730	336	1740	<b><u>337</u></b>	<b><u>1730</u></b>
445.gobmk	64	779	862	781	859	<b><u>780</u></b>	<b><u>860</u></b>	64	758	886	756	888	<b><u>758</u></b>	<b><u>886</u></b>
456.hammer	64	325	1840	324	1840	<b><u>324</u></b>	<b><u>1840</u></b>	64	286	2090	<b><u>287</u></b>	<b><u>2080</u></b>	287	2080
458.sjeng	64	846	915	846	915	<b><u>846</u></b>	<b><u>915</u></b>	64	800	968	800	969	<b><u>800</u></b>	<b><u>968</u></b>
462.libquantum	64	99.3	13400	<b><u>99.5</u></b>	<b><u>13300</u></b>	99.6	13300	64	99.3	13400	<b><u>99.5</u></b>	<b><u>13300</u></b>	99.6	13300
464.h264ref	64	<b><u>933</u></b>	<b><u>1520</u></b>	944	1500	895	1580	64	<b><u>886</u></b>	<b><u>1600</u></b>	903	1570	882	1610
471.omnetpp	64	584	685	583	686	<b><u>584</u></b>	<b><u>685</u></b>	64	562	712	560	715	<b><u>560</u></b>	<b><u>714</u></b>
473.astar	64	613	733	<b><u>612</u></b>	<b><u>734</u></b>	611	735	64	613	733	<b><u>612</u></b>	<b><u>734</u></b>	611	735
483.xalancbmk	64	307	1440	<b><u>305</u></b>	<b><u>1450</u></b>	304	1450	64	307	1440	<b><u>305</u></b>	<b><u>1450</u></b>	304	1450

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set Snoop Mode to COD mode  
Set Patrol Scrub to Disable  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec16/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Wed Nov 2 08:50:37 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2683 v4 @ 2.10GHz  
2 "physical id"s (chips)  
64 "processors"

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 16
siblings  : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 20480 KB
```

From /proc/meminfo

```
MemTotal:      527788144 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

uname -a:

```
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 2 08:35

SPEC is set to: /spec16

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   911G  153G  758G  17% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.32 09/14/2016

Memory:

```
8x NO DIMM NO DIMM
16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmarthheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalanbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

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## Peak Optimization Flags (Continued)

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
-opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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