



SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

SPECint®_rate2006 = 1050

Huawei XH321 V3 (Intel Xeon E5-2650 v4)

SPECint_rate_base2006 = 1000

CPU2006 license: 3175

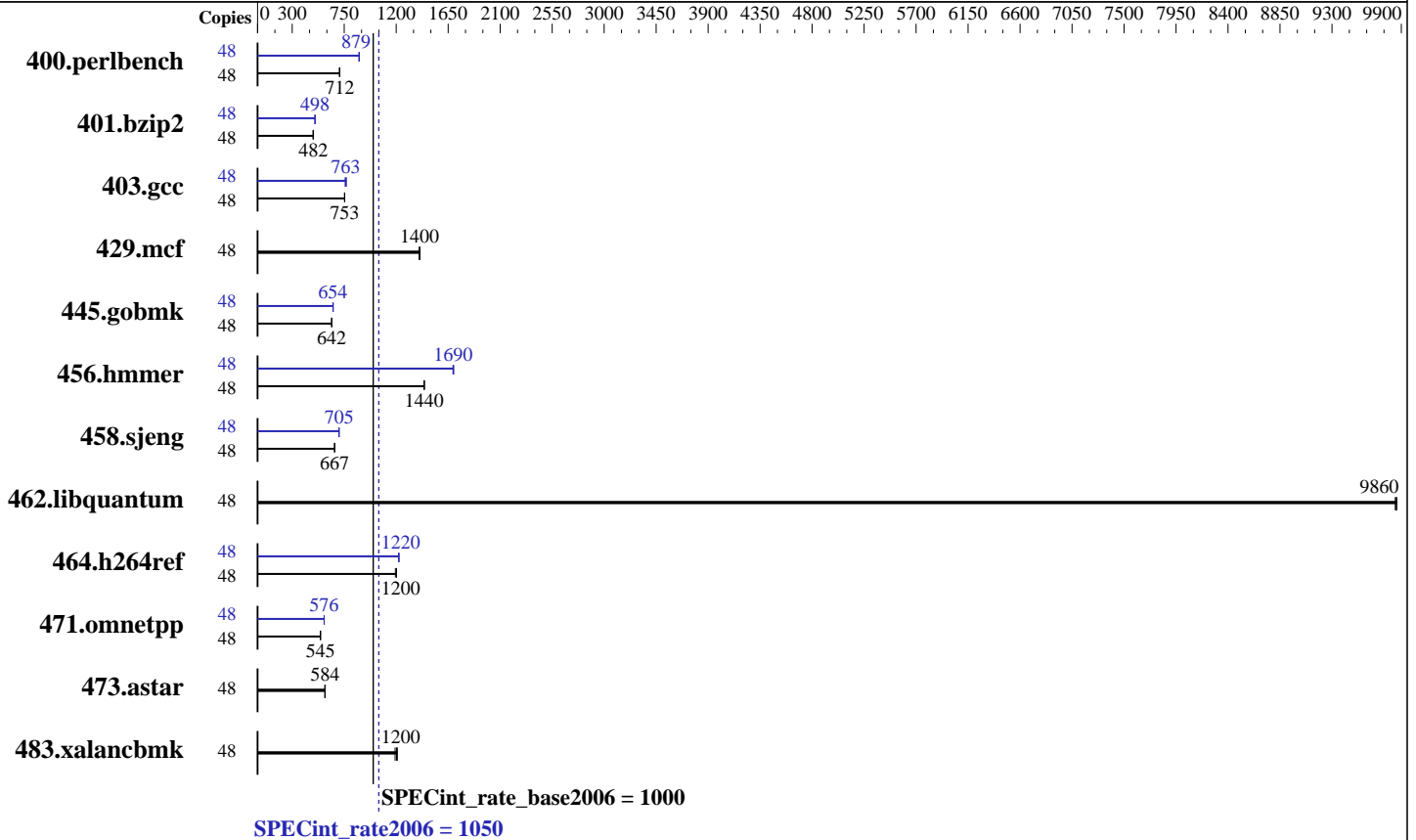
Test date: Dec-2016

Test sponsor: Huawei

Hardware Availability: Nov-2016

Tested by: Huawei

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E5-2650 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x 600 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64) 3.12.49-11-default
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei XH321 V3 (Intel Xeon E5-2650 v4)

SPECint_rate_base2006 = 1000

CPU2006 license: 3175

Test date: Dec-2016

Test sponsor: Huawei

Hardware Availability: Nov-2016

Tested by: Huawei

Software Availability: Nov-2015

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|-------------|------------|------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 48 | 659 | 712 | 663 | 708 | 659 | 712 | 48 | 533 | 880 | 533 | 879 | 534 | 879 |
| 401.bzip2 | 48 | 961 | 482 | 960 | 482 | 962 | 481 | 48 | 932 | 497 | 930 | 498 | 930 | 498 |
| 403.gcc | 48 | 513 | 753 | 512 | 755 | 513 | 753 | 48 | 503 | 769 | 506 | 763 | 510 | 758 |
| 429.mcf | 48 | 313 | 1400 | 312 | 1410 | 312 | 1400 | 48 | 313 | 1400 | 312 | 1410 | 312 | 1400 |
| 445.gobmk | 48 | 785 | 642 | 784 | 642 | 784 | 642 | 48 | 770 | 654 | 770 | 654 | 771 | 653 |
| 456.hammer | 48 | 311 | 1440 | 310 | 1440 | 310 | 1440 | 48 | 264 | 1700 | 265 | 1690 | 264 | 1690 |
| 458.sjeng | 48 | 870 | 668 | 871 | 667 | 871 | 667 | 48 | 824 | 705 | 824 | 705 | 824 | 705 |
| 462.libquantum | 48 | 101 | 9860 | 101 | 9860 | 101 | 9850 | 48 | 101 | 9860 | 101 | 9860 | 101 | 9850 |
| 464.h264ref | 48 | 891 | 1190 | 883 | 1200 | 883 | 1200 | 48 | 868 | 1220 | 869 | 1220 | 869 | 1220 |
| 471.omnetpp | 48 | 551 | 544 | 551 | 545 | 551 | 545 | 48 | 520 | 577 | 520 | 576 | 521 | 576 |
| 473.astar | 48 | 576 | 585 | 577 | 584 | 578 | 583 | 48 | 576 | 585 | 577 | 584 | 578 | 583 |
| 483.xalancbmk | 48 | 274 | 1210 | 278 | 1190 | 275 | 1200 | 48 | 274 | 1210 | 278 | 1190 | 275 | 1200 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode
Set Patrol Scrub to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on linux-v5zw Wed Dec 28 22:41:52 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
2 "physical id"s (chips)
48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei XH321 V3 (Intel Xeon E5-2650 v4)

SPECint_rate_base2006 = 1000

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Dec-2016

Hardware Availability: Nov-2016

Software Availability: Nov-2015

Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 12
siblings  : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 15360 KB
```

From /proc/meminfo

```
MemTotal:      264073264 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux linux-v5zw 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Dec 28 22:41 last=5

SPEC is set to: /spec16

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  542G  11G  530G   3% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

```
8x Hynix HMA84GR7MFR4N-UH 32 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM
```

(End of data from sysinfo program)



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei XH321 V3 (Intel Xeon E5-2650 v4)

SPECint_rate_base2006 = 1000

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Dec-2016

Hardware Availability: Nov-2016

Software Availability: Nov-2015

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmarthheap



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei XH321 V3 (Intel Xeon E5-2650 v4)

SPECint_rate_base2006 = 1000

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Dec-2016
Hardware Availability: Nov-2016
Software Availability: Nov-2015

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalanbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei XH321 V3 (Intel Xeon E5-2650 v4)

SPECint_rate_base2006 = 1000

CPU2006 license: 3175

Test date: Dec-2016

Test sponsor: Huawei

Hardware Availability: Nov-2016

Tested by: Huawei

Software Availability: Nov-2015

Peak Optimization Flags (Continued)

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
-opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

SPECint_rate2006 = 1050

Huawei XH321 V3 (Intel Xeon E5-2650 v4)

SPECint_rate_base2006 = 1000

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Dec-2016

Hardware Availability: Nov-2016

Software Availability: Nov-2015

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Jan 25 10:53:59 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 25 January 2017.