



SPEC® CINT2006 Result

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Inspur Corporation

SPECint®2006 = 73.0

Inspur NF5180M4 (Intel Xeon E5-2698 v4)

SPECint_base2006 = 70.1

CPU2006 license: 3358

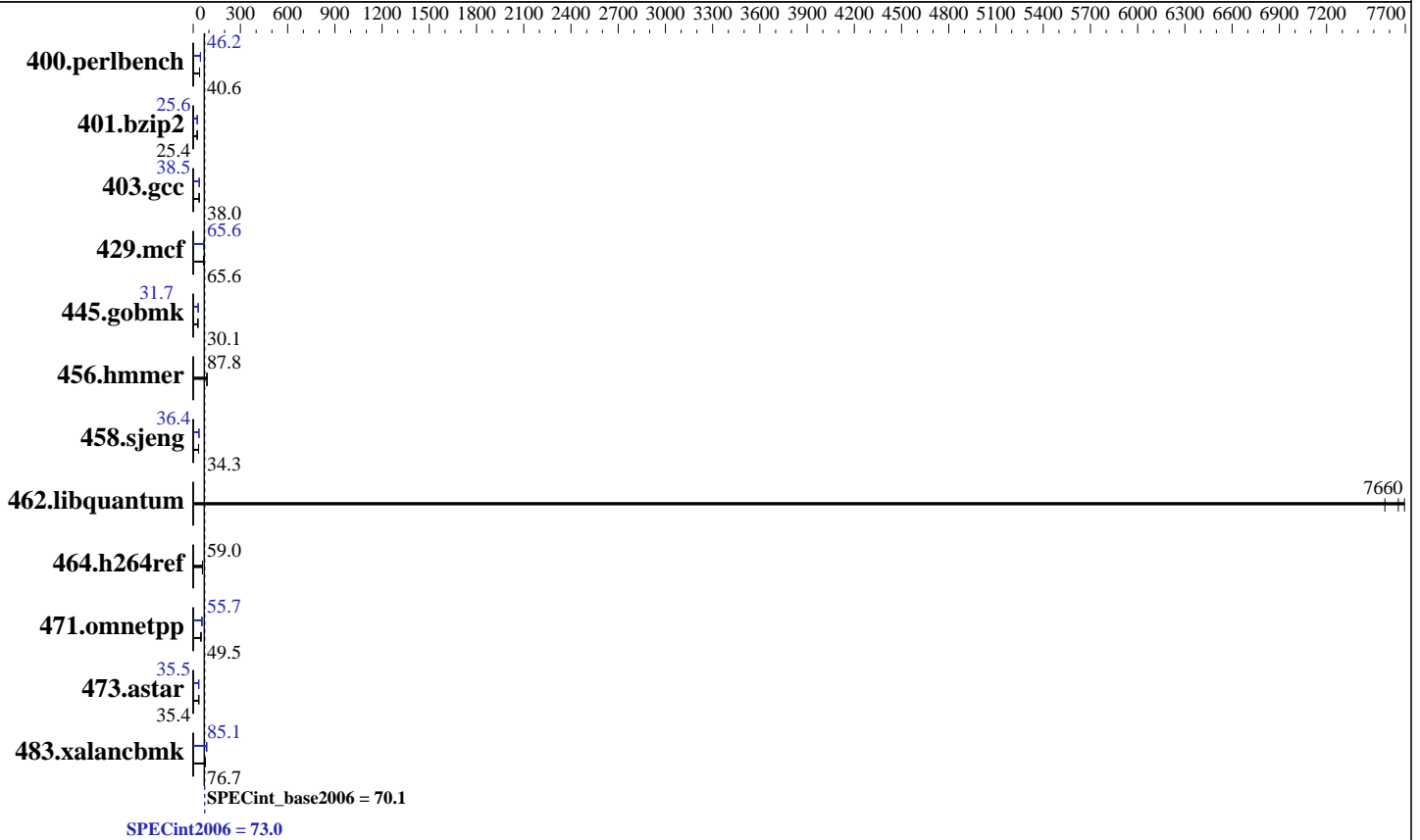
Test date: Jun-2017

Test sponsor: Inspur Corporation

Hardware Availability: Mar-2016

Tested by: Inspur Corporation

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon E5-2698 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 50 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem: 1 x 450 GB SATA SSD
Other Hardware: None

Software

Operating System: Inspur K-UX release 3.0.5 (Inspur) 3.10.4-K_UX.x86_64
Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 5 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|-------------|-------------|------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 241 | 40.5 | 240 | 40.8 | <u>241</u> | <u>40.6</u> | 211 | 46.2 | 212 | 46.0 | <u>212</u> | <u>46.2</u> |
| 401.bzip2 | 380 | 25.4 | 383 | 25.2 | <u>381</u> | <u>25.4</u> | 376 | 25.6 | 377 | 25.6 | <u>377</u> | <u>25.6</u> |
| 403.gcc | 211 | 38.1 | 212 | 37.9 | <u>212</u> | <u>38.0</u> | 209 | 38.5 | <u>209</u> | <u>38.5</u> | 208 | 38.7 |
| 429.mcf | 140 | 65.1 | <u>139</u> | <u>65.6</u> | 136 | 67.0 | 138 | 65.9 | 139 | 65.5 | <u>139</u> | <u>65.6</u> |
| 445.gobmk | 348 | 30.2 | 349 | 30.1 | <u>349</u> | <u>30.1</u> | <u>331</u> | <u>31.7</u> | 331 | 31.7 | 331 | 31.7 |
| 456.hammer | <u>106</u> | <u>87.8</u> | 107 | 87.6 | 106 | 87.9 | <u>106</u> | <u>87.8</u> | 107 | 87.6 | 106 | 87.9 |
| 458.sjeng | 352 | 34.4 | 355 | 34.1 | <u>353</u> | <u>34.3</u> | 334 | 36.3 | <u>333</u> | <u>36.4</u> | 333 | 36.4 |
| 462.libquantum | 2.74 | 7570 | 2.69 | 7700 | <u>2.71</u> | <u>7660</u> | 2.74 | 7570 | 2.69 | 7700 | <u>2.71</u> | <u>7660</u> |
| 464.h264ref | <u>375</u> | <u>59.0</u> | 376 | 58.8 | 374 | 59.1 | <u>375</u> | <u>59.0</u> | 376 | 58.8 | 374 | 59.1 |
| 471.omnetpp | 124 | 50.5 | 129 | 48.4 | <u>126</u> | <u>49.5</u> | 112 | 55.8 | <u>112</u> | <u>55.7</u> | 112 | 55.7 |
| 473.astar | 199 | 35.3 | <u>198</u> | <u>35.4</u> | 197 | 35.6 | 200 | 35.2 | <u>198</u> | <u>35.5</u> | 198 | 35.5 |
| 483.xalancbmk | <u>89.9</u> | <u>76.7</u> | 90.8 | 76.0 | 89.1 | 77.5 | 81.3 | 84.9 | <u>81.1</u> | <u>85.1</u> | 79.7 | 86.6 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS and OS configuration:

SCALING_GOVERNOR set to Performance

Hardware Prefetch set to Disable

VT Support set to Disable

C1E Support set to Disable

Hyper-Threading set to Disable

Sysinfo program /home/CPU2006/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on localhost.localdomain Sat Jun 10 11:22:31 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2698 v4 @ 2.20GHz

2 "physical id"s (chips)

40 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The

Continued on next page

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Page 2



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Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```

cpu cores : 20
siblings  : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
cache size : 51200 KB

```

From /proc/meminfo

```

MemTotal:      264029356 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

inspur-release: Inspur K-UX release 3.0.5 (Inspur)
os-release:
NAME="Inspur K-UX"
VERSION="3 (Inspur)"
ID="k-ux"
VERSION_ID="3"
PRETTY_NAME="Inspur K-UX 3 (Inspur)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:k-ux:k-ux:3"
HOME_URL="http://www.inspur.com/"
system-release: Inspur K-UX release 3.0.5 (Inspur)
system-release-cpe: cpe:/o:k-ux:k-ux:3

```

uname -a:

```

Linux localhost.localdomain 3.10.4-K_UX.x86_64 #1 SMP Fri Sep 30 11:06:29 GMT
2016 x86_64 x86_64 x86_64 GNU/Linux

```

SPEC is set to: /home/CPU2006

```

Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/ik-home xfs      393G  7.3G 386G   2% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 4.1.11 09/07/2016

Memory:

```

8x NO DIMM NO DIMM
16x Samsung M393A2K43BB1-CNC 16 GB 2 rank 2400 MHz, configured at 2133 MHz

```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/CPU2006/lib/ia32:/home/CPU2006/lib/intel64:/home/CPU2006/sh10.2"

OMP_NUM_THREADS = "40"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -auto-ilp32 -qopt-prefetch

Continued on next page



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Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2)

456.hmmcr: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-ra-region-strategy=block
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Inspur-Platform-Settings-V1.0-HSW.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Inspur-Platform-Settings-V1.0-HSW.xml>



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