



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8580)

**SPECrate®2017\_int\_base = 1040**

**SPECrate®2017\_int\_peak = 1070**

CPU2017 License: 3

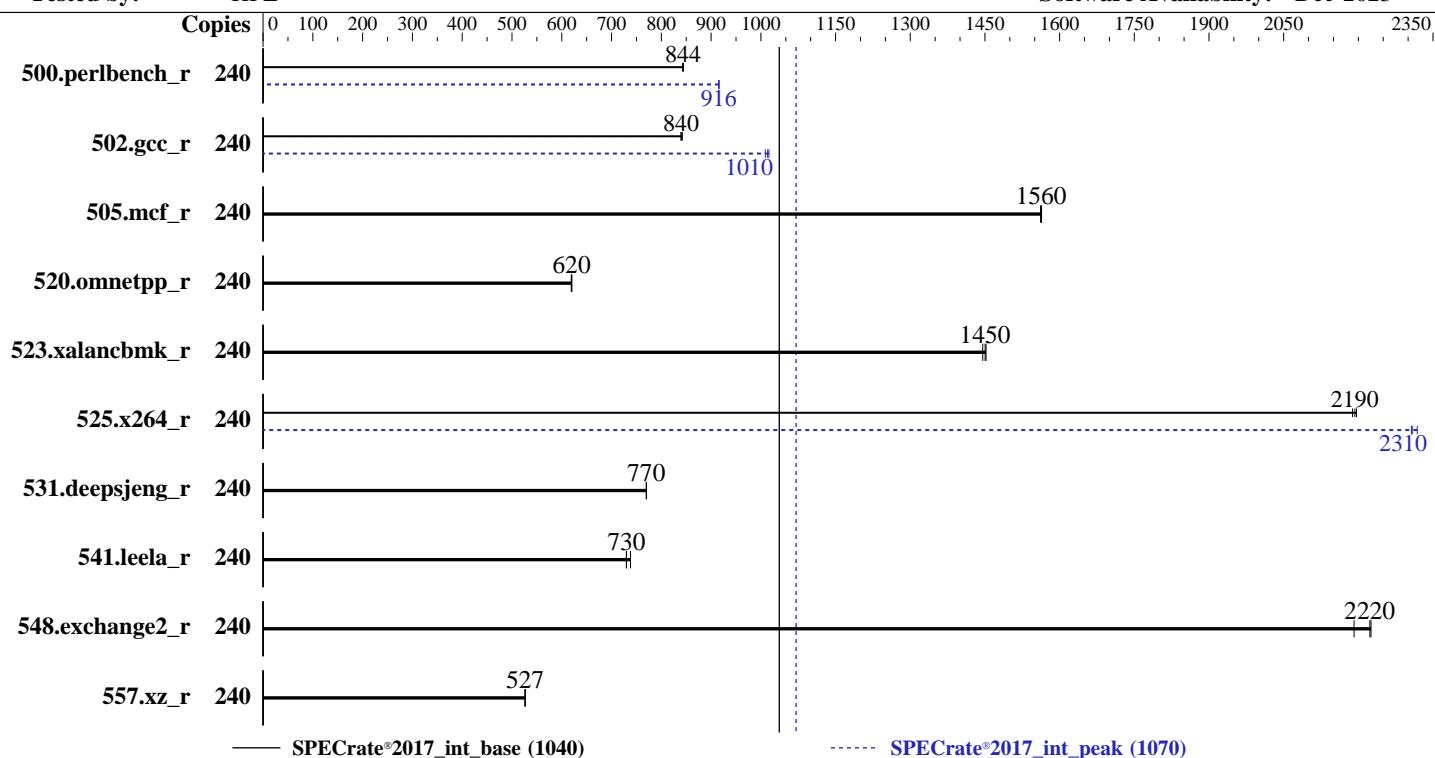
**Test Date:** Jan-2024

**Test Sponsor:** HPE

**Hardware Availability:** Feb-2024

**Tested by:** HPE

**Software Availability:** Dec-2023



— SPECrate®2017\_int\_base (1040)

----- SPECrate®2017\_int\_peak (1070)

## Hardware

CPU Name: Intel Xeon Platinum 8580  
 Max MHz: 4000  
 Nominal: 2000  
 Enabled: 120 cores, 2 chips, 2 threads/core  
 Orderable: 1, 2 chip(s)  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 300 MB I+D on chip per chip  
 Other: None  
 Memory: 512 GB (16 x 32 GB 2Rx8 PC5-5600B-R)  
 Storage: 1 x 1.6 TB NVMe SSD  
 Other: None

## Software

OS: SUSE Linux Enterprise Server 15 SP5  
 Compiler: Kernel 5.14.21-150500.53-default  
 C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: HPE BIOS Version v2.12 12/19/2023 released Dec-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	240	454	842	453	844	<b>453</b>	<b>844</b>	240	417	916	418	915	<b>417</b>	<b>916</b>		
502.gcc_r	240	404	842	<b>405</b>	<b>840</b>	405	839	240	<b>335</b>	<b>1010</b>	337	1010	334	1020		
505.mcf_r	240	<b>248</b>	<b>1560</b>	248	1560	248	1560	240	<b>248</b>	<b>1560</b>	248	1560	248	1560		
520.omnetpp_r	240	509	619	<b>508</b>	<b>620</b>	508	620	240	509	619	<b>508</b>	<b>620</b>	508	620		
523.xalancbmk_r	240	175	1450	<b>175</b>	<b>1450</b>	175	1450	240	175	1450	<b>175</b>	<b>1450</b>	175	1450		
525.x264_r	240	192	2190	<b>192</b>	<b>2190</b>	191	2200	240	182	2310	<b>182</b>	<b>2310</b>	181	2320		
531.deepsjeng_r	240	<b>357</b>	<b>770</b>	357	770	357	770	240	<b>357</b>	<b>770</b>	357	770	357	770		
541.leela_r	240	545	730	<b>544</b>	<b>730</b>	539	738	240	545	730	<b>544</b>	<b>730</b>	539	738		
548.exchange2_r	240	283	2230	287	2190	<b>283</b>	<b>2220</b>	240	283	2230	287	2190	<b>283</b>	<b>2220</b>		
557.xz_r	240	<b>492</b>	<b>527</b>	493	526	492	527	240	<b>492</b>	<b>527</b>	493	526	492	527		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.  
For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
tuned service was stopped using "systemctl stop tuned"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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## General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

The system ROM used for this result contains Intel microcode version 0x21000200 for the Intel Xeon Platinum 8580 processor.

BIOS Configuration:

Workload Profile set to General Throughput Compute

Memory Patrol Scrubbing set to Disabled

Intel UPI Link Enablement set to Single Link

Last Level Cache (LLC) Dead Line Allocation set to Disabled

Enhanced Processor Performance Profile set to Aggressive

Thermal Configuration set to Maximum Cooling

Sub-NUMA Clustering (SNC) set to Enable SNC2(2-clusters)

Workload Profile set to Custom

DCU Stream Prefetcher set to Disabled

Adjacent Sector Prefetch set to Disabled

Intel UPI Link Power Management set to Enabled

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Mon Jan 22 02:33:08 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

1. uname -a  
2. w  
3. Username  
4. ulimit -a  
5. sysinfo process ancestry  
6. /proc/cpuinfo  
7. lscpu  
8. numactl --hardware  
9. /proc/meminfo  
10. who -r  
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)  
12. Services, from systemctl list-unit-files  
13. Linux kernel boot-time arguments, from /proc/cmdline  
14. cpupower frequency-info  
15. tuned-adm active  
16. sysctl  
17. /sys/kernel/mm/transparent\_hugepage  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
19. OS release  
20. Disk information  
21. /sys/devices/virtual/dmi/id  
22. dmidecode  
23. BIOS
- 

1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)

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## Platform Notes (Continued)

x86\_64 x86\_64 x86\_64 GNU/Linux

```
-----  
2. w  
 02:33:08 up 1 min, 0 users, load average: 0.21, 0.12, 0.05  
USER      TTY      FROM           LOGIN@     IDLE     JCPU     PCPU WHAT  
  
-----  
3. Username  
From environment variable $USER: root  
  
-----  
4. ulimit -a  
core file size          (blocks, -c) unlimited  
data seg size            (kbytes, -d) unlimited  
scheduling priority      (-e) 0  
file size                (blocks, -f) unlimited  
pending signals           (-i) 2062533  
max locked memory        (kbytes, -l) 64  
max memory size          (kbytes, -m) unlimited  
open files               (-n) 1024  
pipe size                (512 bytes, -p) 8  
POSIX message queues     (bytes, -q) 819200  
real-time priority        (-r) 0  
stack size                (kbytes, -s) unlimited  
cpu time                 (seconds, -t) unlimited  
max user processes        (-u) 2062533  
virtual memory             (kbytes, -v) unlimited  
file locks                (-x) unlimited  
  
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 29  
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups  
sshd: root@notty  
bash -c cd $SPEC/ && $SPEC/intrate.sh  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=240 -c  
    ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=120 --define physicalfirst  
    --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=240 --configfile  
    ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=120 --define physicalfirst  
    --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower  
    --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile  
    $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /home/cpu2017  
  
-----  
6. /proc/cpuinfo  
model name      : INTEL(R) XEON(R) PLATINUM 8580  
vendor_id       : GenuineIntel  
cpu family     : 6  
model          : 207  
stepping        : 2  
microcode       : 0x21000200  
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_pbrsb  
cpu cores       : 60  
siblings         : 120  
2 physical ids (chips)  
240 processors (hardware threads)
```

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## Platform Notes (Continued)

```
physical id 0: core ids 0-59
physical id 1: core ids 0-59
physical id 0: apicids 0-119
physical id 1: apicids 128-247
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 240
On-line CPU(s) list: 0-239
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) PLATINUM 8580
CPU family: 6
Model: 207
Thread(s) per core: 2
Core(s) per socket: 60
Socket(s): 2
Stepping: 2
BogoMIPS: 4000.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
       nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor
       ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
       sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
       invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
       vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
       bmi2 erms invpcid rtm cqmq rdt_a avx512f avx512dq rdseed adx smap
       avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
       xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total
       cqmq_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
       avx512vbmi umip pkru ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
       avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid bus_lock_detect
       cldemote movdiri movdir64b enqcnd fsrm md_clear serialize tsxldtrk pconfig
       arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization: VT-x
L1d cache: 5.6 MiB (120 instances)
L1i cache: 3.8 MiB (120 instances)
L2 cache: 240 MiB (120 instances)
L3 cache: 600 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-29,120-149
NUMA node1 CPU(s): 30-59,150-179
NUMA node2 CPU(s): 60-89,180-209
NUMA node3 CPU(s): 90-119,210-239
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
```

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## Platform Notes (Continued)

Vulnerability Spectre v1:

Mitigation: usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2:

Mitigation: Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	5.6M	12	Data	1	64	1	64
L1i	32K	3.8M	8	Instruction	1	64	1	64
L2	2M	240M	16	Unified	2	2048	1	64
L3	300M	600M	20	Unified	3	245760	1	64

-----

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-29,120-149

node 0 size: 128702 MB

node 0 free: 128068 MB

node 1 cpus: 30-59,150-179

node 1 size: 128974 MB

node 1 free: 128326 MB

node 2 cpus: 60-89,180-209

node 2 size: 129008 MB

node 2 free: 127874 MB

node 3 cpus: 90-119,210-239

node 3 size: 128970 MB

node 3 free: 128404 MB

node distances:

node 0 1 2 3

0: 10 20 30 30

1: 20 10 30 30

2: 30 30 10 20

3: 30 30 20 10

-----

9. /proc/meminfo

MemTotal: 528032144 kB

-----

10. who -r

run-level 3 Jan 22 02:30

-----

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

Default Target Status

multi-user running

-----

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled apparmor auditd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor nvmefc-boot-connections postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny

enabled-runtime systemd-remount-fs

disabled blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load nvmf-autoconnect rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned

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## Platform Notes (Continued)

indirect                wickeddd

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
    BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
    root=UUID=8cfea0f2-51cb-41d3-956f-66e764c9dda6  
    splash=silent  
    resume=/dev/disk/by-uuid/f2fdb83d-34e7-4b60-be82-95ecb061302c  
    mitigations=auto  
    quiet  
    security=apparmor

-----  
14. cpupower frequency-info  
    analyzing CPU 0:  
        Unable to determine current policy  
        boost state support:  
            Supported: yes  
            Active: yes

-----  
15. tuned-adm active  
    It seems that tuned daemon is not running, preset profile is not activated.  
    Preset profile: throughput-performance

-----  
16. sysctl  
    kernel.numa\_balancing                    1  
    kernel.randomize\_va\_space                2  
    vm.compaction\_proactiveness             20  
    vm.dirty\_background\_bytes                0  
    vm.dirty\_background\_ratio                10  
    vm.dirty\_bytes                            0  
    vm.dirty\_expire\_centisecs              3000  
    vm.dirty\_ratio                            20  
    vm.dirty\_writeback\_centisecs            500  
    vm.dirtytime\_expire\_seconds            43200  
    vm.extfrag\_threshold                    500  
    vm.min\_unmapped\_ratio                    1  
    vm.nr\_hugepages                         0  
    vm.nr\_hugepages\_mempolicy              0  
    vm.nr\_overcommit\_hugepages            0  
    vm.swappiness                            60  
    vm.watermark\_boost\_factor             15000  
    vm.watermark\_scale\_factor             10  
    vm.zone\_reclaim\_mode                    0

-----  
17. /sys/kernel/mm/transparent\_hugepage  
    defrag                                    always defer defer+madvise [madvise] never  
    enabled                                    [always] madvise never  
    hpage\_pmd\_size                         2097152  
    shmem\_enabled                            always within\_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
    alloc\_sleep\_millisecs                60000  
    defrag                                    1  
    max\_ptes\_none                         511  
    max\_ptes\_shared                        256

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## Platform Notes (Continued)

```
max_ptes_swap      64
pages_to_scan     4096
scan_sleep_millisecs 10000
```

```
-----  
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5
```

```
-----  
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p3  xfs  946G  285G  662G  31% /home
```

```
-----  
21. /sys/devices/virtual/dmi/id
Vendor:          HPE
Product:         ProLiant DL380a Gen11
Product Family: ProLiant
Serial:          CNX22602MZ
```

```
-----  
22. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  16x Hynix HMCG88AGBRA193N 32 GB 2 rank 5600
```

```
-----  
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:          HPE
BIOS Version:         2.12
BIOS Date:            12/19/2023
BIOS Revision:        2.12
Firmware Revision:   1.56
```

## Compiler Version Notes

```
=====| 502.gcc_r(peak)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

```
=====| 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

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## Compiler Version Notes (Continued)

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 548.exchange2\_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502.gcc\_r: -DSPEC\_LP64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

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# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8580)

**SPECrate®2017\_int\_base = 1040**

**SPECrate®2017\_int\_peak = 1070**

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

**Test Date:** Jan-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

## Base Portability Flags (Continued)

541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

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SPECrate®2017\_int\_base = 1040

SPECrate®2017\_int\_peak = 1070

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Peak Portability Flags (Continued)

502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: -w -std=c11 -m64 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-fno-strict-overflow  
-L/home/specdev/new\_compilers/ic2023.2.3/compiler/lib/intel64\_lin  
-lqkmalloc

502.gcc\_r: -m32  
-L/home/specdev/new\_compilers/ic2023.2.3/compiler/lib/ia32\_lin  
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf\_r: basepeak = yes

525.x264\_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-alias  
-L/home/specdev/new\_compilers/ic2023.2.3/compiler/lib/intel64\_lin  
-lqkmalloc

557.xz\_r: basepeak = yes

C++ benchmarks:

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# SPEC CPU®2017 Integer Rate Result

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ProLiant DL380a Gen11  
(2.00 GHz, Intel Xeon Platinum 8580)

SPECrate®2017\_int\_base = 1040

SPECrate®2017\_int\_peak = 1070

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Peak Optimization Flags (Continued)

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml>

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