



# SPEC CPU®2017 Floating Point Speed Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11  
(2.60 GHz, Intel Xeon Silver 4509Y)

**SPECspeed®2017\_fp\_base = 172**

**SPECspeed®2017\_fp\_peak = 172**

CPU2017 License: 3

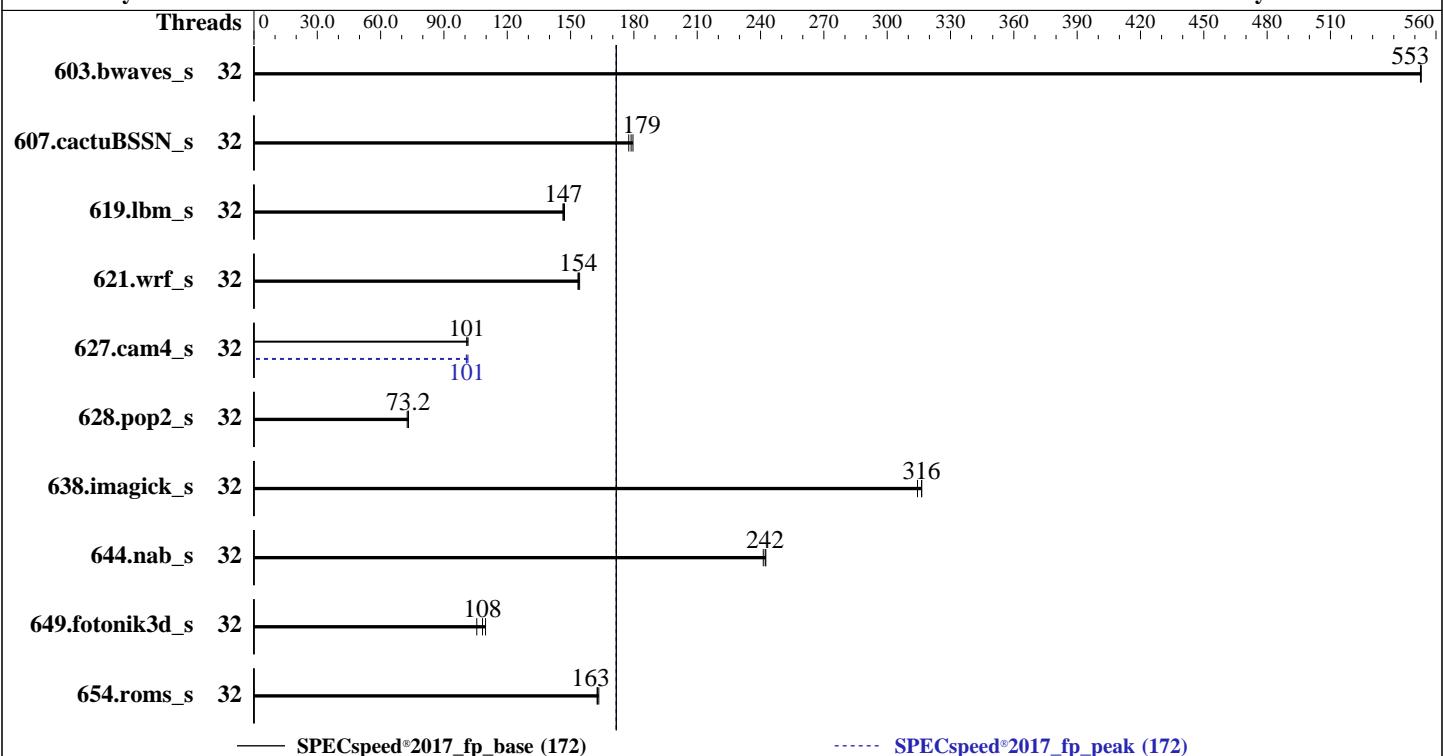
**Test Date:** Mar-2024

**Test Sponsor:** HPE

**Hardware Availability:** Feb-2024

**Tested by:** HPE

**Software Availability:** Dec-2023



## Hardware

CPU Name: Intel Xeon Silver 4509Y  
Max MHz: 4100  
Nominal: 2600  
Enabled: 16 cores, 2 chips, 2 threads/core  
Orderable: 1, 2 chip(s)  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 22.5 MB I+D on chip per chip  
Other: None  
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-5600B-R, running at 4400)  
Storage: 1 x 3.2 TB NVMe SSD  
Other: CPU Cooling: Air

## Software

OS: SUSE Linux Enterprise Server 15 SP5  
Compiler: Kernel 5.14.21-150500.53-default  
C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: HPE BIOS Version v2.12 12/13/2023 released Dec-2023  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	107	553	<b>107</b>	<b>553</b>	107	553	32	107	553	<b>107</b>	<b>553</b>	107	553
607.cactuBSSN_s	32	93.9	178	<b>93.3</b>	<b>179</b>	92.8	180	32	93.9	178	<b>93.3</b>	<b>179</b>	92.8	180
619.lbm_s	32	35.6	147	35.8	146	<b>35.7</b>	<b>147</b>	32	35.6	147	35.8	146	<b>35.7</b>	<b>147</b>
621.wrf_s	32	86.1	154	<b>86.0</b>	<b>154</b>	85.7	154	32	86.1	154	<b>86.0</b>	<b>154</b>	85.7	154
627.cam4_s	32	87.9	101	<b>87.8</b>	<b>101</b>	87.4	101	32	<b>87.9</b>	<b>101</b>	87.4	101	88.1	101
628.pop2_s	32	164	72.6	162	73.2	<b>162</b>	<b>73.2</b>	32	164	72.6	162	73.2	<b>162</b>	<b>73.2</b>
638.imagick_s	32	<b>45.6</b>	<b>316</b>	45.9	314	45.6	316	32	<b>45.6</b>	<b>316</b>	45.9	314	45.6	316
644.nab_s	32	72.1	242	<b>72.1</b>	<b>242</b>	72.4	241	32	72.1	242	<b>72.1</b>	<b>242</b>	72.4	241
649.fotonik3d_s	32	86.4	106	<b>84.2</b>	<b>108</b>	83.1	110	32	86.4	106	<b>84.2</b>	<b>108</b>	83.1	110
654.roms_s	32	<b>96.7</b>	<b>163</b>	96.4	163	96.9	163	32	<b>96.7</b>	<b>163</b>	96.4	163	96.9	163
<b>SPECspeed®2017_fp_base = 172</b>							<b>SPECspeed®2017_fp_peak = 172</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3 > /proc/sys/vm/drop\_caches

runcpu command invoked through numactl i.e.:

numactl --interleave=all runcpu <etc>

tuned service was set to Throughput-Performance using "tuned-adm profile throughput-performance"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"

MALLOC\_CONF = "retain:true"

OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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## Platform Notes

### BIOS Configuration:

Workload Profile set to General Peak Frequency Compute  
Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
Enhanced Processor Performance Profile set to Aggressive  
Dead Block Predictor set to Enabled  
Last Level Cache (LLC) Prefetch set to Enabled  
Last Level Cache (LLC) Dead Line Allocation set to Disabled  
Workload Profile set to Custom  
Intel DMI Link Frequency set to Gen2 Speed  
Adjacent Sector Prefetch set to Disabled  
Minimum Processor Idle Power Package C-State set to No Package State

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Thu Mar 7 05:40:09 2024

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
05:40:09 up 1 min, 0 users, load average: 0.09, 0.04, 0.01  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username  
From environment variable \$USER: root

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## Platform Notes (Continued)

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 2062845
max locked memory        (kbytes, -l) 64
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority        (-r) 0
stack size                (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 2062845
virtual memory             (kbytes, -v) unlimited
file locks               (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/fpspeedTP.sh
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2023.2.3-lin-core-avx512-speed-20231121.cfg --define cores=32 --tune base,peak -o all --define
    drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2023.2.3-lin-core-avx512-speed-20231121.cfg --define cores=32 --tune base,peak --output_format all
  --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv
  --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0
  --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) SILVER 4509Y
vendor_id       : GenuineIntel
cpu family      : 6
model          : 143
stepping        : 8
microcode       : 0x2b000571
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_brsb
cpu cores       : 8
siblings         : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0-15
physical id 1: apicids 64-79
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
7. lscpu
```

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## Platform Notes (Continued)

From lscpu from util-linux 2.37.4:

```

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) SILVER 4509Y
CPU family: 6
Model: 143
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
Stepping: 8
BogoMIPS: 5200.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
       nonstop_tsc cpuid aperf fmpf perf tsc_known_freq pni pclmulqdq dtes64 monitor
       ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1
       sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
       lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
       invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
       vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
       bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
       avx512fma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
       xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
       cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
       arat pln pts hfi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
       vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid
       bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
       tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lll arch_capabilities
Virtualization: VT-x
L1d cache: 768 KiB (16 instances)
L1i cache: 512 KiB (16 instances)
L2 cache: 32 MiB (16 instances)
L3 cache: 45 MiB (2 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	22.5M	45M	15	Unified	3	24576	1	64

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## Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.  
available: 2 nodes (0-1)  
node 0 cpus: 0-7,16-23  
node 0 size: 257702 MB  
node 0 free: 256648 MB  
node 1 cpus: 8-15,24-31  
node 1 size: 258031 MB  
node 1 free: 257054 MB  
node distances:  
node 0 1  
0: 10 20  
1: 20 10

9. /proc/meminfo

MemTotal: 528112008 kB

10. who -r

run-level 3 Mar 7 05:39

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

Default Target Status  
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor auditd cron getty@ irqbalance issue-generator kbdsettings nvmefc-boot-connections postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load nvmf-autoconnect rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
root=UUID=a806a23c-58ee-4027-a60d-04b4da1323d6  
splash=silent  
resume=/dev/disk/by-uuid/ebbabfc7-d0cb-4aac-aeed-3d2c035d5d16  
mitigations=auto  
quiet  
security=apparmor

14. cpupower frequency-info

analyzing CPU 0:  
Unable to determine current policy  
boost state support:  
Supported: yes  
Active: yes

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## Platform Notes (Continued)

-----  
15. tuned-adm active

Current active profile: throughput-performance

-----  
16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	10
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

-----  
17. /sys/kernel/mm/transparent\_hugepage

defrag	always defer defer+madvise [madvise] never
enabled	[always] madvise never
hppte_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

-----  
19. OS release

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP5

-----  
20. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme0n1p3	xfs	2.5T	124G	2.3T	5%	/home

-----  
21. /sys/devices/virtual/dmi/id

Vendor:	HPE
Product:	ProLiant DL380 Gen11
Product Family:	ProLiant
Serial:	CNX21000G3

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## Platform Notes (Continued)

22. dmidecode

Additional information from dmidecode 3.4 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Hynix HMCG88AGBRA193N 32 GB 2 rank 5600, configured at 4400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE  
BIOS Version: 2.12  
BIOS Date: 12/13/2023  
BIOS Revision: 2.12  
Firmware Revision: 1.52

## Compiler Version Notes

=====

C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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## Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
```

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# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11

(2.60 GHz, Intel Xeon Silver 4509Y)

**SPECspeed®2017\_fp\_base = 172**

**SPECspeed®2017\_fp\_peak = 172**

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

**Test Date:** Mar-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp  
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP -Wno-implicit-int  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

627.cam4\_s: -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -futto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC\_OPENMP  
-Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml>

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