



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

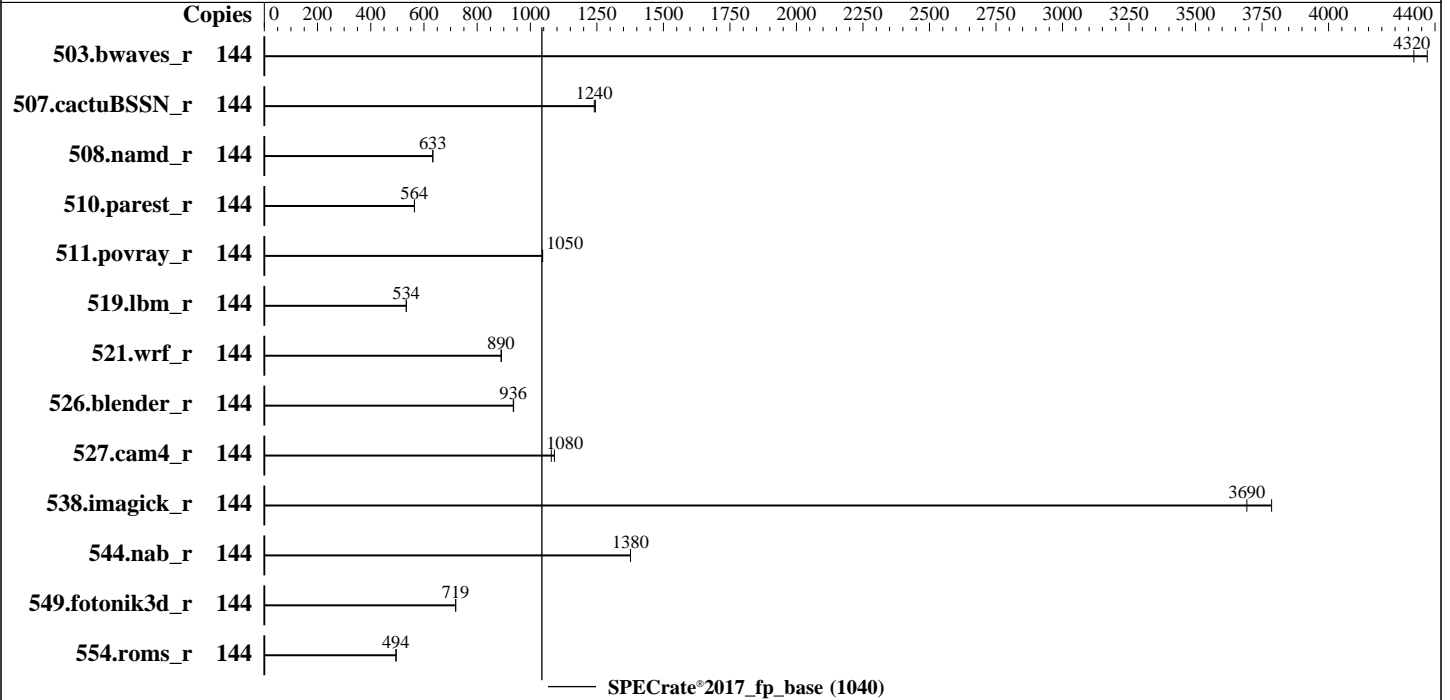
Test Date: Oct-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2025

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024



## Hardware

CPU Name: Intel Xeon 6960P  
 Max MHz: 3900  
 Nominal: 2700  
 Enabled: 72 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 64 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 432 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (12 x 64 GB 4Rx8 PC5-12800B-R, running at 8800)  
 Storage: 1 x 3.84 TB NVME SSD  
 Other: CPU Cooling: Air

## Software

OS: Red Hat Enterprise Linux 9.4 (Plow)  
 5.14.0-427.13.1.el9\_4.x86\_64  
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 7.10.01P91 released Sep-2024 BIOS  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Date: Oct-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2025

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	144	330	4370	<b><u>334</u></b>	<b><u>4320</u></b>									
507.cactuBSSN_r	144	146	1240	<b><u>147</u></b>	<b><u>1240</u></b>									
508.namd_r	144	<b><u>216</u></b>	<b><u>633</u></b>	216	633									
510.parest_r	144	<b><u>668</u></b>	<b><u>564</u></b>	668	564									
511.povray_r	144	<b><u>322</u></b>	<b><u>1050</u></b>	322	1050									
519.lbm_r	144	284	534	<b><u>284</u></b>	<b><u>534</u></b>									
521.wrf_r	144	<b><u>363</u></b>	<b><u>890</u></b>	362	891									
526.blender_r	144	<b><u>234</u></b>	<b><u>936</u></b>	234	937									
527.cam4_r	144	231	1090	<b><u>233</u></b>	<b><u>1080</u></b>									
538.imagick_r	144	94.6	3790	<b><u>97.0</u></b>	<b><u>3690</u></b>									
544.nab_r	144	<b><u>176</u></b>	<b><u>1380</u></b>	176	1380									
549.fotonik3d_r	144	780	719	<b><u>780</u></b>	<b><u>719</u></b>									
554.roms_r	144	<b><u>463</u></b>	<b><u>494</u></b>	462	496									

SPECrate®2017\_fp\_base = 1040

SPECrate®2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Jun-2025

Software Availability: Apr-2024

## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:

LLC dead line alloc = Disabled

Power Performance Tuning = BIOS Controls EPB

ENERGY\_PERF\_BIAS\_CFG mode = Performance

Sysinfo program /home/speccpu/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost.localdomain Mon Oct 7 20:50:07 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.el9\_4)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent\_hugepage
19. /sys/kernel/mm/transparent\_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

-----

1. uname -a  
Linux localhost.localdomain 5.14.0-427.13.1.el9\_4.x86\_64 #1 SMP PREEMPT\_DYNAMIC Wed Apr 10 10:29:16 EDT 2024 x86\_64 x86\_64 x86\_64 GNU/Linux

-----

2. w  
20:50:07 up 1:45, 2 users, load average: 10.63, 85.22, 120.40  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Jun-2025

Software Availability: Apr-2024

## Platform Notes (Continued)

```

root      tty1      19:05      1:42m    0.00s    0.00s  -bash
root      pts/0      19:07      23.00s   0.80s    0.00s  sh intrate.sh

```

### 3. Username

From environment variable \$USER: root

### 4. ulimit -a

```

real-time non-blocking time (microseconds, -R) unlimited
core file size              (blocks, -c) 0
data seg size                (kbytes, -d) unlimited
scheduling priority         (-e) 0
file size                    (blocks, -f) unlimited
pending signals              (-i) 3092056
max locked memory            (kbytes, -l) 8192
max memory size              (kbytes, -m) unlimited
open files                   (-n) 1024
pipe size                    (512 bytes, -p) 8
POSIX message queues         (bytes, -q) 819200
real-time priority          (-r) 0
stack size                   (kbytes, -s) unlimited
cpu time                     (seconds, -t) unlimited
max user processes           (-u) 3092056
virtual memory                (kbytes, -v) unlimited
file locks                   (-x) unlimited

```

### 5. sysinfo process ancestry

```

/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
sh intrate.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 -c
ic2024.1-lin-sierraforest-rate-20240308.cfg --define smt-on --define peakfpcopies=72 --define
physicalfirst --define invoke_with_interleave --define drop_caches --reportable --tune base -o all fprate
-n 2
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 --configfile
ic2024.1-lin-sierraforest-rate-20240308.cfg --define smt-on --define peakfpcopies=72 --define
physicalfirst --define invoke_with_interleave --define drop_caches --reportable --tune base
--output_format all --iterations 2 --nopower --runmode rate --tune base --size refrate fprate --nopreenv
--note-preenv --logfile $SPEC/tmp/CPU2017.075/templogs/preenv.fprate.075.0.log --lognum 075.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/speccpu

```

### 6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) 6960P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 173
stepping        : 1
microcode       : 0x810002e0
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores       : 72
siblings        : 144
1 physical ids (chips)

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Date: Oct-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2025

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024

## Platform Notes (Continued)

144 processors (hardware threads)  
 physical id 0: core ids 0-23,64-87,128-151  
 physical id 0: apicids 0-47,128-175,256-303  
 Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:               52 bits physical, 57 bits virtual
Byte Order:                  Little Endian
CPU(s):                      144
On-line CPU(s) list:        0-143
Vendor ID:                   GenuineIntel
BIOS Vendor ID:             Intel(R) Corporation
Model name:                  Intel(R) Xeon(R) 6960P
BIOS Model name:            Intel(R) Xeon(R) 6960P
CPU family:                  6
Model:                      173
Thread(s) per core:         2
Core(s) per socket:         72
Socket(s):                   1
Stepping:                   1
CPU(s) scaling MHz:         97%
CPU max MHz:                 3900.0000
CPU min MHz:                 800.0000
BogoMIPS:                   5400.00
Flags:                       fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                             clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                             rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                             xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq
                             dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
                             pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
                             avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2
                             cdp_l3 cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                             flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
                             erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                             clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt
                             xsaves xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                             split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                             hwp hwp_act_window hwp_epp hwp_pkg_req hfi vnmi avx512vbmi umip pku
                             ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
                             avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote
                             movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                             arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d
                             arch_capabilities
Virtualization:              VT-x
L1d cache:                   3.4 MiB (72 instances)
L1i cache:                   4.5 MiB (72 instances)
L2 cache:                    144 MiB (72 instances)
L3 cache:                    432 MiB (1 instance)
NUMA node(s):                3
NUMA node0 CPU(s):          0-23,72-95
NUMA node1 CPU(s):          24-47,96-119
NUMA node2 CPU(s):          48-71,120-143
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Date: Oct-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2025

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024

## Platform Notes (Continued)

Vulnerability Lltf: Not affected  
 Vulnerability Mds: Not affected  
 Vulnerability Meltdown: Not affected  
 Vulnerability Mmio stale data: Not affected  
 Vulnerability Retbleed: Not affected  
 Vulnerability Spec rstack overflow: Not affected  
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl  
 Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
 Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS Not affected  
 Vulnerability Srbds: Not affected  
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3.4M	12	Data	1	64	1	64
L1i	64K	4.5M	16	Instruction	1	64	1	64
L2	2M	144M	16	Unified	2	2048	1	64
L3	432M	432M	16	Unified	3	442368	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 3 nodes (0-2)  
 node 0 cpus: 0-23,72-95  
 node 0 size: 257041 MB  
 node 0 free: 244310 MB  
 node 1 cpus: 24-47,96-119  
 node 1 size: 257994 MB  
 node 1 free: 247439 MB  
 node 2 cpus: 48-71,120-143  
 node 2 size: 258020 MB  
 node 2 free: 247638 MB  
 node distances:  
 node 0 1 2  
 0: 10 12 12  
 1: 12 10 12  
 2: 12 12 10

9. /proc/meminfo

MemTotal: 791610144 kB

10. who -r

run-level 3 Oct 7 19:04

11. Systemd service manager version: systemd 252 (252-32.e19\_4)

Default Target Status  
 multi-user degraded

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* dnf-makecache.service	loaded	failed	failed	dnf makecache

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
-------	------------

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Date: Oct-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2025

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024

## Platform Notes (Continued)

```

enabled      NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth
             chronyd crond dbus-broker firewalld getty@ insights-client-boot irqbalance iscsi-onboot
             iscsi-starter kdump libstoragemgmt low-memory-monitor lvm2-monitor mcelog mdmonitor
             microcode multipathd nis-domainname nvme-fc-boot-connections rhsmcertd rsyslog rtkit-daemon
             selinux-autorelabel-mark smartd sshd sssd systemd-boot-update systemd-network-generator
             tuned udisks2 upower
enabled-runtime  systemd-remount-fs
disabled      arp-ethers blk-availability canberra-system-bootup canberra-system-shutdown
             canberra-system-shutdown-reboot chrony-wait chronyd-restricted console-getty cpupower
             debug-shell dnf-system-upgrade iprdump iprint iprupdate ipsec iscsi-init iscsid iscsiuiio
             kpatch kvm_stat ledmon man-db-restart-cache-update nftables nvme-f-autoconnect pesign psacct
             rdisc rhcd rhsm rhsm-facts rpmdb-rebuild selinux-check-proper-disable serial-getty@
             sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysex
indirect      iscsi sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
             systemd-sysupdate-reboot

```

```

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet

```

```

-----
15. cpupower frequency-info
analyzing CPU 12:
  current policy: frequency should be within 800 MHz and 3.90 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes

```

```

-----
16. tuned-adm active
Current active profile: throughput-performance

```

```

-----
17. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 40
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   10

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Jun-2025

Software Availability: Apr-2024

## Platform Notes (Continued)

```

vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

-----
18. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000

```

```

-----
20. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.4 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.4 (Plow)
system-release  Red Hat Enterprise Linux release 9.4 (Plow)

```

```

-----
21. Disk information
SPEC is set to: /home/speccpu
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs      3.5T      345G  3.1T  10% /home

```

```

-----
22. /sys/devices/virtual/dmi/id
Vendor:          New H3C Technologies Co., Ltd.
Product:         H3C UniServer R3900 G7
Serial:          210235A526H249000010

```

```

-----
23. dmidecode
Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  12x Hynix HMC98BDJHA381N 64 GB 4 rank 12800, configured at 8800

```

```

-----
24. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      American Megatrends International, LLC.
BIOS Version:     7.10.01P91
BIOS Date:        09/25/2024
BIOS Revision:    5.35
Firmware Revision: 1.16

```





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Jun-2025

Software Availability: Apr-2024

## Compiler Version Notes

=====  
C | 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
C++ | 508.namd\_r(base) 510.parest\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
C++, C | 511.povray\_r(base) 526.blender\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
=====

## Base Compiler Invocation

C benchmarks:  
icx

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Date: Oct-2024

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Jun-2025

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Apr-2024

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsierraforest -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsierraforest -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_fp\_base = 1040

H3C UniServer R3900 G7 (Intel Xeon 6960P)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Jun-2025

Software Availability: Apr-2024

## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsierraforest -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsierraforest -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsierraforest -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsierraforest -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-SPR-RevE.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevE.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-SPR-RevE.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevE.xml)

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-10-07 08:50:07-0400.

Report generated on 2024-11-25 17:28:03 by CPU2017 PDF formatter v6716.

Originally published on 2024-10-23.