



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488

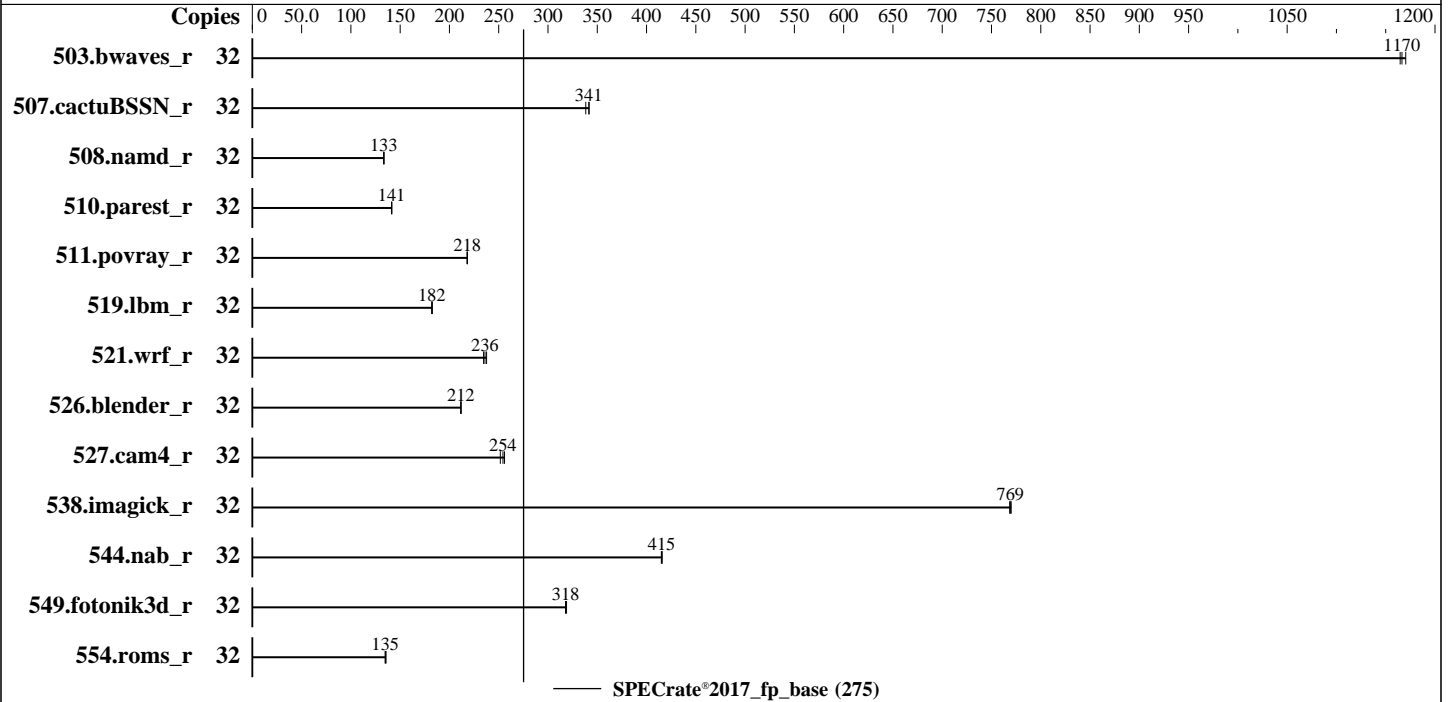
Test Sponsor: xFusion

Tested by: xFusion

Test Date: Oct-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024



Hardware

CPU Name: Intel Xeon Silver 4509Y
 Max MHz: 4100
 Nominal: 2600
 Enabled: 16 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 22.5 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)
 Storage: 1 x 960 GB SATA SSD
 Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.2 (Plow)
 5.14.0-284.11.1.el9_2.x86_64
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: Version 01.01.03.16 Released Aug-2024
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	32	274	1170	276	1160	<u>275</u>	<u>1170</u>							
507.cactuBSSN_r	32	<u>119</u>	<u>341</u>	119	342	120	338							
508.namd_r	32	<u>228</u>	<u>133</u>	228	133	228	133							
510.parest_r	32	<u>592</u>	<u>141</u>	592	141	592	141							
511.povray_r	32	343	218	<u>343</u>	<u>218</u>	342	218							
519.lbm_r	32	185	183	<u>185</u>	<u>182</u>	185	182							
521.wrf_r	32	302	238	305	235	<u>304</u>	<u>236</u>							
526.blender_r	32	<u>230</u>	<u>212</u>	230	212	230	212							
527.cam4_r	32	222	252	219	256	<u>220</u>	<u>254</u>							
538.imagick_r	32	104	768	103	770	<u>103</u>	<u>769</u>							
544.nab_r	32	129	416	130	415	<u>130</u>	<u>415</u>							
549.fotonik3d_r	32	<u>392</u>	<u>318</u>	391	319	392	318							
554.roms_r	32	<u>377</u>	<u>135</u>	375	136	377	135							

SPECrate®2017_fp_base = 275

SPECrate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/spec2017/lib/intel64:/home/spec2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:
Performance Profile Set to Performance
SNC Set to Enable SNC2 (2-clusters)
LLC Prefetch Set to Disabled

Sysinfo program /home/spec2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Fri Oct 18 01:30:57 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-13.e19_2)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

1. uname -a
Linux localhost.localdomain 5.14.0-284.11.1.e19_2.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 12 10:45:03 EDT 2023 x86_64 x86_64 x86_64 GNU/Linux

2. w
01:30:57 up 3:37, 2 users, load average: 21.60, 29.40, 30.86
USER TTY LOGIN@ IDLE JCPU PCPU WHAT

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Platform Notes (Continued)

```
root      tty1      22:58    2:30m  1.15s  0.08s  -bash
root      pts/0     21:57    2:33m  0.04s  0.04s  -bash
```

3. Username
From environment variable \$USER: root

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 4124632
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 4124632
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/spec2017

6. /proc/cpuinfo
model name : INTEL(R) XEON(R) SILVER 4509Y
vendor_id : GenuineIntel
cpu family : 6
model : 143
stepping : 8
microcode : 0x2b000590
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores : 8
siblings : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0-15

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Platform Notes (Continued)

physical id 1: apicids 64-79

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 32
On-line CPU(s) list:   0-31
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:             INTEL(R) XEON(R) SILVER 4509Y
BIOS Model name:       INTEL(R) XEON(R) SILVER 4509Y
CPU family:             6
Model:                  143
Thread(s) per core:    2
Core(s) per socket:    8
Socket(s):              2
Stepping:               8
CPU max MHz:            4100.0000
CPU min MHz:            800.0000
BogoMIPS:               5200.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 ds_cpl
                        vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
                        flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms
                        invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                        clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
                        xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect
                        avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi avx512vbmi umip
                        pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
                        avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote
                        movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr
                        ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d arch_capabilities
Virtualization:         VT-x
L1d cache:              768 KiB (16 instances)
L1i cache:              512 KiB (16 instances)
L2 cache:               32 MiB (16 instances)
L3 cache:               45 MiB (2 instances)
NUMA node(s):          4
NUMA node0 CPU(s):     0-3,16-19
NUMA node1 CPU(s):     4-7,20-23
NUMA node2 CPU(s):     8-11,24-27
NUMA node3 CPU(s):     12-15,28-31
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:     Not affected
Vulnerability Mds:      Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected

```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Platform Notes (Continued)

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBR SB-eIBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	22.5M	45M	15	Unified	3	24576	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-3,16-19
node 0 size: 257104 MB
node 0 free: 256258 MB
node 1 cpus: 4-7,20-23
node 1 size: 258045 MB
node 1 free: 257311 MB
node 2 cpus: 8-11,24-27
node 2 size: 258045 MB
node 2 free: 257240 MB
node 3 cpus: 12-15,28-31
node 3 size: 258001 MB
node 3 free: 257101 MB
node distances:
node  0  1  2  3
 0:  10  12  21  21
 1:  12  10  21  21
 2:  21  21  10  12
 3:  21  21  12  10

```

9. /proc/meminfo

MemTotal: 1055946892 kB

10. who -r

run-level 3 Oct 17 21:54

11. Systemd service manager version: systemd 252 (252-13.e19_2)

Default Target Status
multi-user degraded

12. Failed units, from systemctl list-units --state=failed

```

UNIT          LOAD    ACTIVE SUB    DESCRIPTION
* sep5.service loaded failed failed systemd script to load sep5 driver at boot time

```

13. Services, from systemctl list-unit-files

```

STATE          UNIT FILES
enabled        NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
                dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor

```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Platform Notes (Continued)

```

lvm2-monitor mdmonitor microcode nis-domainname nvme-fc-boot-connections rhsmcertd rsyslog
rtkit-daemon selinux-autorelabel-mark sep5 sshd sssd sysstat systemd-boot-update
systemd-network-generator tuned udisks2 upower
enabled-runtime systemd-remount-fs
disabled blk-availability canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell
dnf-system-upgrade kvm_stat man-db-restart-cache-update nftables nvme-fc-autoconnect pesign
rdisc rhcd rhsm rhsm-facts rpmdb-rebuild selinux-check-proper-disable serial-getty@
ssh-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
systemd-sysupdate-reboot

```

```

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-284.11.1.el9_2.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap

```

```

-----
15. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 4.10 GHz and 4.10 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

```

```

-----
16. tuned-adm active
Current active profile: throughput-performance

```

```

-----
17. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 40
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 10
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

```

```

-----
18. /sys/kernel/mm/transparent_hugepage

```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Platform Notes (Continued)

```
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

```
-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

```
-----
20. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.2 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.2 (Plow)
system-release  Red Hat Enterprise Linux release 9.2 (Plow)
```

```
-----
21. Disk information
SPEC is set to: /home/spec2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   819G  46G  774G   6% /home
```

```
-----
22. /sys/devices/virtual/dmi/id
Vendor:          XFUSION
Product:         2288H V7
Product Family:  Eagle Stream
```

```
-----
23. dmidecode
Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  16x Micron MTC40F2046S1RC56BD1 64 GB 2 rank 5600, configured at 4400
```

```
-----
24. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      XFUSION
BIOS Version:     01.01.03.16
BIOS Date:        08/09/2024
BIOS Revision:    3.16
```

Compiler Version Notes

```
=====
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
=====
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Compiler Version Notes (Continued)

=====
C++ | 508.namd_r(base) 510.parest_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
C++, C | 511.povray_r(base) 526.blender_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
C++, C, Fortran | 507.cactuBSSN_r(base)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

SPECrate®2017_fp_base = 275

FusionServer 2288H V7 (Intel Xeon Silver 4509Y)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Oct-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>
<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-EMR-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>
<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-EMR-V1.1.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-10-18 01:30:56-0400.
Report generated on 2024-11-06 12:20:41 by CPU2017 PDF formatter v6716.
Originally published on 2024-11-05.