



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R7725 (AMD EPYC 9255 24-Core Processor)

SPECrate®2017\_fp\_base = 1020

SPECrate®2017\_fp\_peak = 1040

CPU2017 License: 6573

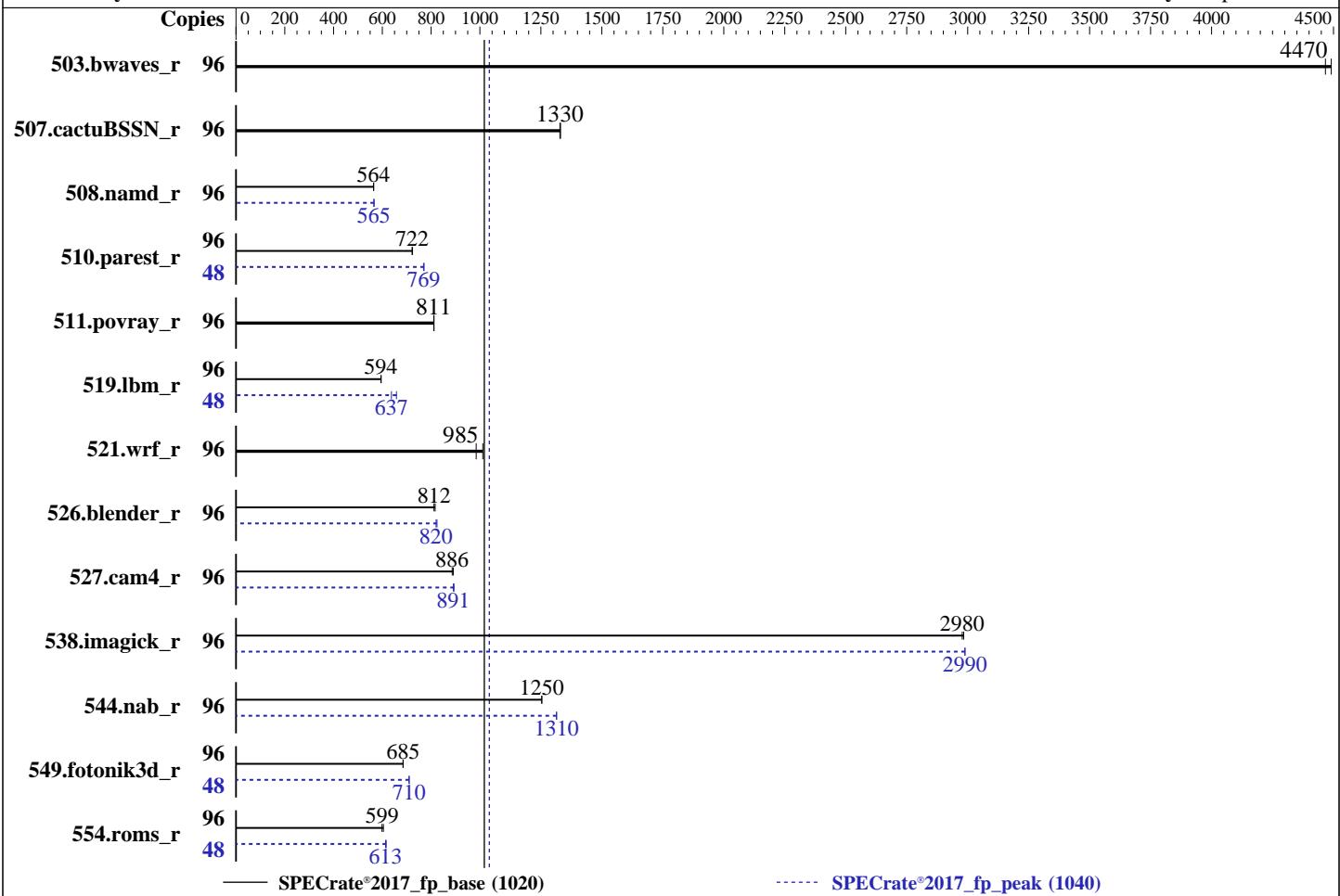
Test Date: May-2025

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2025

Tested by: Dell Inc.

Software Availability: Apr-2025



— SPECrate®2017\_fp\_base (1020)

- - - - - SPECrate®2017\_fp\_peak (1040)

## Hardware

CPU Name: AMD EPYC 9255  
 Max MHz: 4300  
 Nominal: 3200  
 Enabled: 48 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 128 MB I+D on chip per chip, 32 MB shared / 6 cores  
 Other: None  
 Memory: 768 GB (24 x 32 GB 2Rx8 PC5-6400B-R)  
 Storage: 70 GB on tmpfs  
 Other: CPU Cooling: Air

## Software

OS: Ubuntu 24.04.2 LTS  
 Compiler: 6.8.0-59-generic  
 C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 1.1.3 released Feb-2025  
 File System: tmpfs  
 System State: Run level 5 (graphical multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	<b>216</b>	<b>4470</b>	214	4490			96	<b>216</b>	<b>4470</b>	214	4490				
507.cactusBSSN_r	96	91.3	1330	<b>91.5</b>	<b>1330</b>			96	91.3	1330	<b>91.5</b>	<b>1330</b>				
508.namd_r	96	162	565	<b>162</b>	<b>564</b>			96	<b>161</b>	<b>565</b>	161	566				
510.parest_r	96	347	724	<b>348</b>	<b>722</b>			48	163	771	<b>163</b>	<b>769</b>				
511.povray_r	96	276	812	<b>276</b>	<b>811</b>			96	276	812	<b>276</b>	<b>811</b>				
519.lbm_r	96	<b>170</b>	<b>594</b>	170	595			48	<b>79.4</b>	<b>637</b>	76.9	658				
521.wrf_r	96	<b>218</b>	<b>985</b>	213	1010			96	<b>218</b>	<b>985</b>	213	1010				
526.blender_r	96	179	817	<b>180</b>	<b>812</b>			96	177	824	<b>178</b>	<b>820</b>				
527.cam4_r	96	188	891	<b>189</b>	<b>886</b>			96	188	894	<b>188</b>	<b>891</b>				
538.imagick_r	96	80.0	2980	<b>80.2</b>	<b>2980</b>			96	<b>79.9</b>	<b>2990</b>	79.9	2990				
544.nab_r	96	<b>129</b>	<b>1250</b>	129	1250			96	123	1310	<b>123</b>	<b>1310</b>				
549.fotonik3d_r	96	546	685	<b>546</b>	<b>685</b>			48	263	710	<b>264</b>	<b>710</b>				
554.roms_r	96	252	604	<b>255</b>	<b>599</b>			48	124	616	<b>124</b>	<b>613</b>				

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
 'numactl' was used to bind copies to the cores.  
 See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
 To free node-local memory and avoid remote memory usage,  
 'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
 To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
 To disable address space layout randomization (ASLR) to reduce run-to-run  
 variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

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## Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3-2/amd_rate_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu20  
    17-1.1.9-aocc500-znerv5_A1.3-2/amd_rate_aocc500_znver5_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

Benchmark run from a 70 GB ramdisk created with the cmd: "mount -t tmpfs -o size=70G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS Settings:

Virtualization Technology	:	Disabled
NUMA Nodes Per Socket	:	4
System Profile	:	Custom
C-States	:	Disabled
Memory Patrol Scrub	:	Disabled
PCI ASPM L1 Link Power Management	:	Disabled
Periodic Directory Rinse Tuning	:	Blended
Determinism Control	:	Manual
Determinism Slider	:	Power Determinism
Optimizer Mode	:	Enabled
Adaptive Allocation	:	Enabled
Dram Refresh Delay	:	Performance
DIMM Self Healing -		
on Uncorrectable Memory Error	:	Disabled

```
Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3-2/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on 1234567-R7725 Thu May 15 05:11:00 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo

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## Platform Notes (Continued)

```
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.5)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
-----
-----
1. uname -a
Linux 1234567-R7725 6.8.0-59-generic #61-Ubuntu SMP PREEMPT_DYNAMIC Fri Apr 11 23:16:11 UTC 2025 x86_64 x86_64 GNU/Linux
-----
2. w
05:11:00 up 2:38, 2 users, load average: 63.44, 88.02, 92.59
USER      TTY      FROM          LOGIN@    IDLE   JCPU   PCPU WHAT
root      tty1      -          02:32    2:37m  1.63s  0.50s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
root      192.168.138.129 02:34    209days  0.00s  0.10s sshd: root@notty
-----
3. Username
From environment variable $USER: root
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)        unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            3092837
nofiles            1024
vmmemory(kbytes)   unlimited
locks              unlimited
rtprio             0
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash ./DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-VERS=6.2 --output_format html,pdf,txt
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-L3NUMA=1 --define DL-BIOS-NP=4 --define DL-VERS=6.2 --output_format html,pdf,txt fprate
```

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## Platform Notes (Continued)

```
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define  
DL-BIOS-L3NUMA=1 --define DL-BIOS-NPS=4 --define DL-VERS=6.2 --output_format html,pdf,txt --nopower  
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile  
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3-2
```

```
-----  
6. /proc/cpuinfo  
model name      : AMD EPYC 9255 24-Core Processor  
vendor_id       : AuthenticAMD  
cpu family     : 26  
model          : 2  
stepping        : 1  
microcode       : 0xb00211e  
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass  
TLB size        : 192 4K pages  
cpu cores       : 24  
siblings         : 48  
2 physical ids (chips)  
96 processors (hardware threads)  
physical id 0: core ids 0-5,8-13,16-21,24-29  
physical id 1: core ids 0-5,8-13,16-21,24-29  
physical id 0: apicids 0-11,16-27,32-43,48-59  
physical id 1: apicids 64-75,80-91,96-107,112-123
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----  
7. lscpu
```

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	96
On-line CPU(s) list:	0-95
Vendor ID:	AuthenticAMD
BIOS Vendor ID:	AMD
Model name:	AMD EPYC 9255 24-Core Processor
BIOS Model name:	AMD EPYC 9255 24-Core Processor
BIOS CPU family:	107
CPU family:	26
Model:	2
Thread(s) per core:	2
Core(s) per socket:	24
Socket(s):	2
Stepping:	1
Frequency boost:	enabled
CPU(s) scaling MHz:	40%
CPU max MHz:	4315.6250
CPU min MHz:	1500.0000
BogoMIPS:	6391.34
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch

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## Platform Notes (Continued)

```
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bml1 avx2
smep bmi2 invpcid cqmq rdt_a avx512f avx512dq rdseed adx snap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbvl xsaves cqmq_llc cqmq_occur_llc cqmq_mbm_total
cqmq_mbm_local user_shstk avx_vnni avx512_bf16 clzero iperf
xsaverptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv
svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmlload vgif x2avic v_spec_ctrl
vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfnii vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca avx512_vp2intersect
flush_lld debug_swap
```

L1d cache: 2.3 MiB (48 instances)  
L1i cache: 1.5 MiB (48 instances)  
L2 cache: 48 MiB (48 instances)  
L3 cache: 256 MiB (8 instances)

NUMA node(s): 8  
NUMA node0 CPU(s): 0-5,48-53  
NUMA node1 CPU(s): 6-11,54-59  
NUMA node2 CPU(s): 12-17,60-65  
NUMA node3 CPU(s): 18-23,66-71  
NUMA node4 CPU(s): 24-29,72-77  
NUMA node5 CPU(s): 30-35,78-83  
NUMA node6 CPU(s): 36-41,84-89  
NUMA node7 CPU(s): 42-47,90-95

Vulnerability Gather data sampling: Not affected  
Vulnerability Itlb multihit: Not affected  
Vulnerability Llft: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Reg file data sampling: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec rstack overflow: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl  
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP  
always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:  

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	1M	48M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-5,48-53

node 0 size: 96038 MB

node 0 free: 95362 MB

node 1 cpus: 6-11,54-59

node 1 size: 96762 MB

node 1 free: 96065 MB

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## Platform Notes (Continued)

```
node 2 cpus: 12-17,60-65
node 2 size: 96762 MB
node 2 free: 96116 MB
node 3 cpus: 18-23,66-71
node 3 size: 96746 MB
node 3 free: 96035 MB
node 4 cpus: 24-29,72-77
node 4 size: 96762 MB
node 4 free: 95961 MB
node 5 cpus: 30-35,78-83
node 5 size: 96762 MB
node 5 free: 96075 MB
node 6 cpus: 36-41,84-89
node 6 size: 96719 MB
node 6 free: 96122 MB
node 7 cpus: 42-47,90-95
node 7 size: 96728 MB
node 7 free: 92745 MB
node distances:
node   0   1   2   3   4   5   6   7
  0: 10 12 12 12 32 32 32 32
  1: 12 10 12 12 32 32 32 32
  2: 12 12 10 12 32 32 32 32
  3: 12 12 12 10 32 32 32 32
  4: 32 32 32 32 10 12 12 12
  5: 32 32 32 32 12 10 12 12
  6: 32 32 32 32 12 12 10 12
  7: 32 32 32 32 12 12 12 10
```

```
-----  
9. /proc/meminfo  
MemTotal: 791842512 kB
```

```
-----  
10. who -r  
run-level 5 May 15 02:32
```

```
-----  
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.5)  
Default Target Status  
graphical degraded
```

```
-----  
12. Failed units, from systemctl list-units --state=failed  
UNIT          LOAD ACTIVE SUB DESCRIPTION  
* fwupd-refresh.service loaded failed failed Refresh fwupd metadata and update motd  
Legend: LOAD  -> Reflects whether the unit definition was properly loaded.  
        ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.  
        SUB   -> The low-level unit activation state, values depend on unit type.  
1 loaded units listed.
```

```
-----  
13. Services, from systemctl list-unit-files  
STATE          UNIT FILES  
enabled        ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init  
                cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager  
                grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd  
                networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb  
                sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved  
                systemd-timesyncd thermald ua-reboot-cmds ubuntu-adantage udisks2 ufw vgaauth
```

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## Platform Notes (Continued)

```
enabled-runtime    netplan-ovs-cleanupsystemd-fsck-rootsystemd-remount-fs
disabled          console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
                  systemd-boot-check-no-failures systemd-confext systemd-network-generator
                  systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
                  systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                  systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                  systemd-time-wait-sync upower
indirect          systemd-sysupdate systemd-sysupdate-reboot uidd
masked           cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common
```

---

14. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/vmlinuz-6.8.0-59-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro
```

---

15. cpupower frequency-info

```
analyzing CPU 9:
current policy: frequency should be within 1.50 GHz and 3.20 GHz.
The governor "schedutil" may decide which speed to use
within this range.

boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 3200MHz
```

---

16. sysctl

```
kernel.numa_balancing      1
kernel.randomize_va_space  0
vm.compaction_proactiveness 20
vm.dirty_background_bytes  0
vm.dirty_background_ratio  10
vm.dirty_bytes              0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio              8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold       500
vm.min_unmapped_ratio     1
vm.nr_hugepages            0
vm.nr_hugepages_mempolicy  0
vm.nr_overcommit_hugepages 0
vm.swappiness               1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor  10
vm.zone_reclaim_mode       1
```

---

17. /sys/kernel/mm/transparent\_hugepage

```
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

---

18. /sys/kernel/mm/transparent\_hugepage/khugepaged

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## Platform Notes (Continued)

```
alloc_sleep_millisecs    60000
defrag                  1
max_ptes_none           511
max_ptes_shared          256
max_ptes_swap            64
pages_to_scan            4096
scan_sleep_millisecs    10000
```

-----  
19. OS release  
From /etc/\*-release /etc/\*-version  
os-release Ubuntu 24.04.2 LTS

-----  
20. Disk information  
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5\_A1.3-2  
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 70G 3.3G 67G 5% /mnt/ramdisk

-----  
21. /sys/devices/virtual/dmi/id  
Vendor: Dell Inc.
Product: PowerEdge R7725
Product Family: PowerEdge
Serial: 1234567

-----  
22. dmidecode  
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
23x 802C0000802C MTC20F2085S1RC64BD2 32 GB 2 rank 6400
1x 80CE000080CE M321R4GA3EB2-CCPKC 32 GB 2 rank 6400

-----  
23. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Dell Inc.
BIOS Version: 1.1.3
BIOS Date: 02/25/2025
BIOS Revision: 1.1

## Compiler Version Notes

```
=====
C           | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====
```

```
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====
```

```
=====
C++          | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

PowerEdge R7725 (AMD EPYC 9255 24-Core Processor)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_fp\_base = 1020

SPECrate®2017\_fp\_peak = 1040

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Apr-2025

## Compiler Version Notes (Continued)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++, C, Fortran | 507.cactusBSSN\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin



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CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_fp\_base = 1020

SPECrate®2017\_fp\_peak = 1040

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Apr-2025

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapi -DSPEC\_LP64  
526.blender\_r: -funsigned-char -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
-march=znver5 -fveclib=AMDLIB -ffast-math -fno-PIE -no-pie -flio  
-fstruct-layout=7 -mllvm -unroll-threshold=50

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SPECrate®2017\_fp\_base = 1020

SPECrate®2017\_fp\_peak = 1040

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Apr-2025

## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flfto -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc  
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flfto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flfto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
```

(Continued on next page)



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Tested by: Dell Inc.

SPECrate®2017\_fp\_base = 1020

SPECrate®2017\_fp\_peak = 1040

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Apr-2025

## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdaloc -lflang  
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdaloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

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SPECrate®2017\_fp\_base = 1020

SPECrate®2017\_fp\_peak = 1040

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Apr-2025

## Peak Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl
```

538.imagick\_r: Same as 519.lbm\_r

```
544.nab_r: -m64 -fsto -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl
```

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SPECrate®2017\_fp\_base = 1020

SPECrate®2017\_fp\_peak = 1040

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Apr-2025

## Peak Optimization Flags (Continued)

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl
```

```
510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math  
-mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl
```

Fortran benchmarks:

```
503.bwaves_r: basepeak = yes
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -fvector-transform  
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

```
554.roms_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm  
-lamdalloc -ldl -lflang
```

Benchmarks using both Fortran and C:

```
521.wrf_r: basepeak = yes
```

```
527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays
```

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Dell Inc.

PowerEdge R7725 (AMD EPYC 9255 24-Core Processor)

CPU2017 License: 6573

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SPECrate®2017\_fp\_base = 1020

SPECrate®2017\_fp\_peak = 1040

Test Date: May-2025

Hardware Availability: Mar-2025

Software Availability: Apr-2025

## Peak Optimization Flags (Continued)

527.cam4\_r (continued):

```
-mllvm -reduce-array-computations=3 -zopt -Mrecursive  
-funroll-loops -mllvm -lslr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc  
-ldl -lflang
```

Benchmarks using both C and C++:

511.povray\_r: basepeak = yes

526.blender\_r: -m64 -std=c++14

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt  
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument



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Tested by: Dell Inc.

Software Availability: Apr-2025

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>

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