



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECSpeed®2017_fp_base = 231

SPECSpeed®2017_fp_peak = Not Run

CPU2017 License: 9017

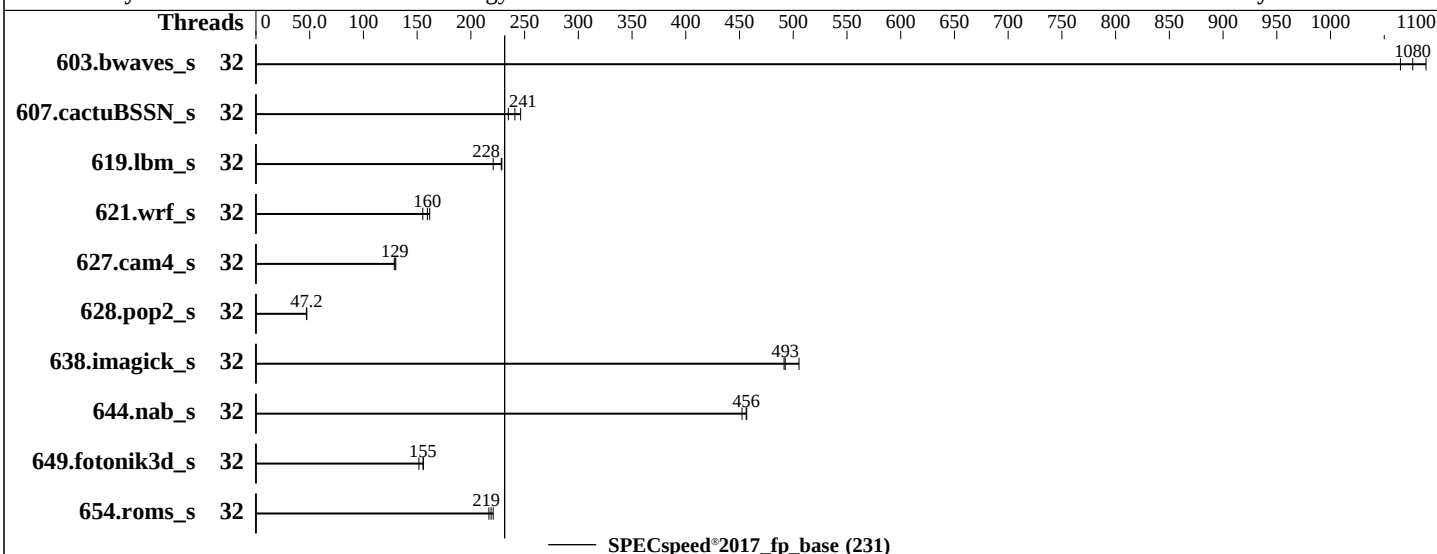
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025



Hardware

CPU Name: Intel Xeon 6714P
Max MHz: 4300
Nominal: 4000
Enabled: 32 cores, 4 chips, 2 threads/core
Orderable: 2,4 chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 48 MB I+D on chip per chip
Other: None
Memory: 2 TB (32 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 1 x 1.92 TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7
Kernel 6.4.0-150700.51-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: Lenovo BIOS Version RVE103X 1.10 released Jul-2025
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	<u>54.8</u>	<u>1080</u>	55.4	1070	54.2	1090							
607.cactuBSSN_s	32	67.7	246	71.0	235	<u>69.2</u>	<u>241</u>							
619.lbm_s	32	23.7	221	<u>22.9</u>	<u>228</u>	22.9	229							
621.wrf_s	32	85.2	155	<u>82.9</u>	<u>160</u>	81.8	162							
627.cam4_s	32	68.1	130	68.9	129	<u>68.5</u>	<u>129</u>							
628.pop2_s	32	251	47.3	253	47.0	<u>252</u>	<u>47.2</u>							
638.imagick_s	32	28.5	505	29.4	491	<u>29.3</u>	<u>493</u>							
644.nab_s	32	<u>38.3</u>	<u>456</u>	38.3	457	38.6	452							
649.fotonik3d_s	32	<u>58.6</u>	<u>155</u>	58.5	156	60.1	152							
654.roms_s	32	71.3	221	72.6	217	<u>72.0</u>	<u>219</u>							

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2024.1/lib/intel64:/home/cpu2017-1.1.9-ic2024.1/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:

Workload Profile set to General Computing - Peak Frequency

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Platform Notes (Continued)

LLC Prefetch set to Enabled

Sysinfo program /home/cpu2017-1.1.9-ic2024.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Aug 21 03:24:15 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)
x86_64 x86_64 x86_64 GNU/Linux

2. w
03:24:15 up 3 min, 1 user, load average: 0.13, 0.17, 0.08
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 8255481
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Platform Notes (Continued)

POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 8255481
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECCpu_1.01.sh
sh Run543-compliant-ic2024.1-lin-sapphirerapids-speedfp-base-smt-on-20240308.sh
runcpu --nobuild --action validate --define default-platform-flags -c
ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=32 --tune base -o all --define smt-on
--define drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=32 --tune base --output_format all --define
smt-on --define drop_caches --nopower --runmode speed --tune base --size refspeed fpspeed --nopreenv
--note-preenv --logfile $SPEC/tmp/CPU2017.473/templogs/preenv.fpspeed.473.0.log --lognum 473.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2024.1
```

```
-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6714P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 173
stepping       : 1
microcode      : 0x10003d0
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores      : 8
siblings       : 16
4 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 2: core ids 0-7
physical id 3: core ids 0-7
physical id 0: apicids 0-15
physical id 1: apicids 128-143
physical id 2: apicids 256-271
physical id 3: apicids 384-399
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

From lscpu from util-linux 2.40.4:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	64

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_fp_base = 231

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Platform Notes (Continued)

On-line CPU(s) list: 0-63
Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) 6714P
CPU family: 6
Model: 173
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 4
Stepping: 1
BogoMIPS: 8000.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_l1d arch_capabilities

Virtualization: VT-x
L1d cache: 1.5 MiB (32 instances)
L1i cache: 2 MiB (32 instances)
L2 cache: 64 MiB (32 instances)
L3 cache: 192 MiB (4 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-7,32-39
NUMA node1 CPU(s): 8-15,40-47
NUMA node2 CPU(s): 16-23,48-55
NUMA node3 CPU(s): 24-31,56-63
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	64K	2M	16	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Platform Notes (Continued)

L3 48M 192M 16 Unified 3 49152 1 64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-7,32-39
node 0 size: 515689 MB
node 0 free: 514989 MB
node 1 cpus: 8-15,40-47
node 1 size: 516091 MB
node 1 free: 515261 MB
node 2 cpus: 16-23,48-55
node 2 size: 516091 MB
node 2 free: 515479 MB
node 3 cpus: 24-31,56-63
node 3 size: 516025 MB
node 3 free: 515414 MB
node distances:
node  0  1  2  3
 0:  10  21  21  21
 1:  21  10  21  21
 2:  21  21  10  21
 3:  21  21  21  10
```

9. /proc/meminfo

MemTotal: 2113431204 kB

10. who -r

run-level 3 Aug 21 03:21

11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvmf-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autostart-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewallld fsidd gpm grub2-once haveged ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2
indirect	systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default
root=UUID=50860560-ae42-405b-bae3-a2b14754d476
splash=silent
mitigations=auto

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Platform Notes (Continued)

quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 52:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

15. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

16. /sys/kernel/mm/transparent_hugepage
defrag always defer+madvise [madvise] never
enabled [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7

19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2024.1
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p3 xfs 1.8T 68G 1.7T 4% /

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Platform Notes (Continued)

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:      Lenovo
Product:     ThinkSystem SR860 V4
Product Family: ThinkSystem
Serial:      9876543210
-----
```

```
-----
21. dmidecode
Additional information from dmidecode 3.6 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  4x SK Hynix HMC94AHBRA275N 64 GB 2 rank 6400
  7x SK Hynix HMC94AHBRA277N 64 GB 2 rank 6400
  3x SK Hynix HMC94AHBRA281N 64 GB 2 rank 6400
  2x SK Hynix HMC94AHBRA283N 64 GB 2 rank 6400
  8x SK Hynix HMC94AHBRA480N 64 GB 2 rank 6400
  3x SK Hynix HMC94AHBRA481N 64 GB 2 rank 6400
  3x SK Hynix HMC94AHBRA486N 64 GB 2 rank 6400
  2x SK Hynix HMC94AHBRA487N 64 GB 2 rank 6400
-----
```

```
-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Lenovo
BIOS Version:     RVE103X-1.10
BIOS Date:        07/17/2025
BIOS Revision:    1.10
Firmware Revision: 1.40
-----
```

Compiler Version Notes

```
=====
C | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```
=====
C++, C, Fortran | 607.cactuBSSN_s(base)
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

```
=====
Fortran | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
-----
```

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Compiler Version Notes (Continued)

=====
Fortran, C | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308

Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308

Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64

607.cactuBSSN_s: -DSPEC_LP64

619.lbm_s: -DSPEC_LP64

621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG

628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

-assume byterecl

638.imagick_s: -DSPEC_LP64

644.nab_s: -DSPEC_LP64

649.fotonik3d_s: -DSPEC_LP64

654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapfirerapids -Ofast -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR860 V4
(4.00 GHz, Intel Xeon 6714P)

SPECspeed®2017_fp_base = 231

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2025

Hardware Availability: Nov-2025

Software Availability: Jun-2025

Base Optimization Flags (Continued)

C benchmarks (continued):

```
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int  
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte  
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Birchstream-F.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Birchstream-F.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-08-20 15:24:14-0400.

Report generated on 2025-09-23 16:55:30 by CPU2017 PDF formatter v6716.

Originally published on 2025-09-23.