



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

ZTE Corporation

ZTE R5300G5 Server System
(2.20 GHz, Intel Xeon Silver 4516Y+)

SPECrate®2017_int_base = 432

SPECrate®2017_int_peak = 447

CPU2017 License: 9061

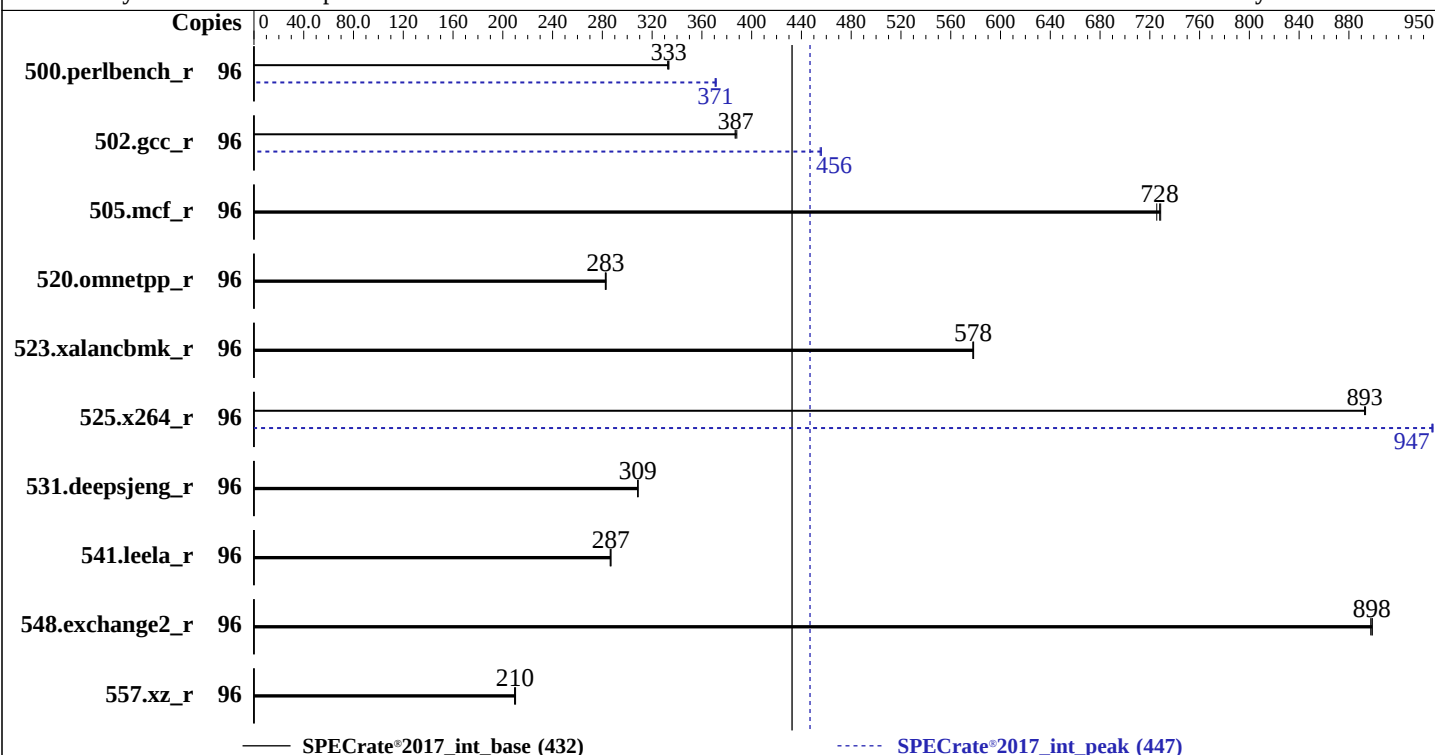
Test Sponsor: ZTE Corporation

Tested by: ZTE Corporation

Test Date: Aug-2025

Hardware Availability: Feb-2024

Software Availability: Mar-2024



Hardware

CPU Name: Intel Xeon Silver 4516Y+
Max MHz: 3700
Nominal: 2200
Enabled: 24 cores, 2 chips, 2 threads/core
Orderable: 2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 45 MB I+D on chip per chip
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)
Storage: 1 x 480 GB SATA SSD
Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.2 (Plow)
5.14.0-284.11.1.el9_2.x86_64
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Version 04.24.01.10 released Mar-2024
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	458	333	460	332	458	333	96	412	371	412	371	411	372
502.gcc_r	96	352	387	350	388	351	387	96	298	456	299	455	298	456
505.mcf_r	96	214	726	213	728	213	729	96	214	726	213	728	213	729
520.omnetpp_r	96	445	283	446	283	446	282	96	445	283	446	283	446	282
523.xalancbmk_r	96	175	578	175	578	175	578	96	175	578	175	578	175	578
525.x264_r	96	188	893	188	893	188	893	96	178	947	177	948	177	947
531.deepsjeng_r	96	356	309	357	309	357	308	96	356	309	357	309	357	308
541.leela_r	96	554	287	554	287	555	286	96	554	287	554	287	555	286
548.exchange2_r	96	280	899	280	898	280	898	96	280	899	280	898	280	898
557.xz_r	96	494	210	495	209	493	210	96	494	210	495	209	493	210

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

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General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Configuration:
ENERGY_PERF_BIAS_CFG mode = performance
LLC dead line alloc = Disabled
Patrol Scrub = Disabled
Sub NUMA(SNC) = Enable SNC2

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Fri Aug 15 13:33:38 2025

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
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6. /proc/cpuinfo
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9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-13.el9_2)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux localhost.localdomain 5.14.0-284.11.1.el9_2.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 12 10:45:03 EDT 2023 x86_64 x86_64 x86_64 GNU/Linux

2. w
13:33:38 up 1:28, 2 users, load average: 0.00, 0.57, 18.02
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root pts/0 12:07 10.00s 1.01s 0.00s -bash
root pts/1 12:07 26:39 0.02s 0.02s -bash

3. Username

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Platform Notes (Continued)

From environment variable \$USER: root

4. ulimit -a

```
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 4124641
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 4124641
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=48 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=48 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.016/temlogs/preenv.intrate.016.0.log --lognum 016.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : INTEL(R) XEON(R) SILVER 4516Y+
vendor_id       : GenuineIntel
cpu family      : 6
model           : 207
stepping        : 2
microcode       : 0x21000200
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores       : 24
siblings        : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0:  core ids 0-23
physical id 1:  core ids 0-23
physical id 0:  apicids 0-47
physical id 1:  apicids 128-175
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

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Platform Notes (Continued)

7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 96
On-line CPU(s) list:    0-95
Vendor ID:              GenuineIntel
BIOS Vendor ID:         Intel(R) Corporation
Model name:             INTEL(R) XEON(R) SILVER 4516Y+
BIOS Model name:        INTEL(R) XEON(R) SILVER 4516Y+
CPU family:             6
Model:                  207
Thread(s) per core:     2
Core(s) per socket:     24
Socket(s):              2
Stepping:               2
CPU max MHz:            3700.0000
CPU min MHz:            800.0000
BogoMIPS:               4400.00
Flags:                  fpu_vme_de_pse_tsc_msr_pae_mce_cx8_apic_sep_mtrr_pge_mca_cmov_pat_pse36
                        clflush_dts_acpi_mmx_fxsr_sse_sse2_ss_ht_tm_pbe_syscall_nx_pdpe1gb_rdtscp
                        lm_constant_tsc_art_arch_perfmon_pebs_bts_rep_good_nopl_xtopology
                        nonstop_tsc_cpuid_aperfperf_tsc_known_freq_pni_pclmulqdq_dtes64_monitor
                        ds_cpl_smx_est_tm2_ssse3_sdbg_fma_cx16_xtpr_pdc_m_pcid_dca_sse4_1_sse4_2
                        x2apic_movbe_popcnt_tsc_deadline_timer_aes_xsaves_avx_f16c_rdrand_lahf_lm
                        abm_3dnowprefetch_cpuid_fault_epb_cat_l3_cat_l2_cdp_l3_invpcid_single
                        cdp_l2_ssbd_mba_ibrs_ibpb_stibp_ibrs_enhanced_fsgsbase_tsc_adjust_bmi1
                        avx2_smep_bmi2_erms_invpcid_cqm_rdt_a_avx512f_avx512dq_rdseed_adx_smap
                        avx512ifma_clflushopt_clwb_intel_pt_avx512cd_sha_ni_avx512bw_avx512vl
                        xsavesopt_xsaves xgetbv1_xsaves_cqm_llc_cqm_occup_llc_cqm_mbm_total
                        cqm_mbm_local_avx_vnni_avx512_bf16_wbnoinvd_dtherm_ida_arat_pln_pts_hwp
                        hwp_act_window_hwp_epp_hwp_pkg_req_hfi_avx512vbmi_umip_pku_ospke_waitpkg
                        avx512_vbmi2_gfni_vaes_vpclmulqdq_avx512_vnni_avx512_bitalg_tme
                        avx512_vpopcntdq_la57_rdpid_bus_lock_detect_cldemote_movdiri_movdir64b
                        enqcmd_fsrn_md_clear_serialize_tsxldtrk_pconfig_arch_lbr_ibt_amx_bf16
                        avx512_fp16_amx_tile_amx_int8_flush_l1d_arch_capabilities
L1d cache:             2.3 MiB (48 instances)
L1i cache:             1.5 MiB (48 instances)
L2 cache:              96 MiB (48 instances)
L3 cache:              90 MiB (2 instances)
NUMA node(s):          4
NUMA node0 CPU(s):     0-11,48-59
NUMA node1 CPU(s):     12-23,60-71
NUMA node2 CPU(s):     24-35,72-83
NUMA node3 CPU(s):     36-47,84-95
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:      Not affected
Vulnerability Mds:       Not affected
Vulnerability Meltdown:  Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:  Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW
                        sequence
```

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Platform Notes (Continued)

Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	2M	96M	16	Unified	2	2048	1	64
L3	45M	90M	15	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-11,48-59
node 0 size: 257118 MB
node 0 free: 249504 MB
node 1 cpus: 12-23,60-71
node 1 size: 258042 MB
node 1 free: 252245 MB
node 2 cpus: 24-35,72-83
node 2 size: 258003 MB
node 2 free: 252153 MB
node 3 cpus: 36-47,84-95
node 3 size: 258037 MB
node 3 free: 252215 MB
node distances:
node  0  1  2  3
 0: 10 12 21 21
 1: 12 10 21 21
 2: 21 21 10 12
 3: 21 21 12 10
```

9. /proc/meminfo

MemTotal: 1055951216 kB

10. who -r

run-level 3 Aug 15 12:05

11. Systemd service manager version: systemd 252 (252-13.el9_2)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond dbus-broker firewalld getty@ insights-client-boot irqbalance kdump lvm2-monitor mdmonitor microcode nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd systemd-boot-update systemd-network-generator udisks2
enabled-runtime	systemd-remount-fs
disabled	blk-availability chrony-wait console-getty cpupower debug-shell dnf-system-upgrade kvm_stat man-db-restart-cache-update nftables rdisc rhcd rhsm rhsm-facts rpmdm-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect	sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate systemd-sysupdate-reboot

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Platform Notes (Continued)

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-284.11.1.el9_2.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
nohz_full=1-127

14. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 800 MHz and 3.70 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

15. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

16. /sys/kernel/mm/transparent_hugepage
defrag always defer defer+madvice [madvice] never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

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Platform Notes (Continued)

18. OS release
From /etc/*-release /etc/*-version
os-release Red Hat Enterprise Linux 9.2 (Plow)
redhat-release Red Hat Enterprise Linux release 9.2 (Plow)
system-release Red Hat Enterprise Linux release 9.2 (Plow)

19. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 372G 97G 275G 27% /home

20. /sys/devices/virtual/dmi/id
Vendor: ZTE
Product: R5300 G5
Product Family: Server
Serial: 219440501031

21. dmidecode
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
16x Samsung M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 4400

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends Inc.
BIOS Version: 04.24.01.10
BIOS Date: 03/13/2024
BIOS Revision: 4.24

Compiler Version Notes

=====

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 502.gcc_r(peak)

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Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====
Fortran | 548.exchange2_r(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64

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Base Portability Flags (Continued)

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

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SPEC CPU®2017 Integer Rate Result

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ZTE Corporation

ZTE R5300G5 Server System
(2.20 GHz, Intel Xeon Silver 4516Y+)

SPECrate®2017_int_base = 432

SPECrate®2017_int_peak = 447

CPU2017 License: 9061

Test Sponsor: ZTE Corporation

Tested by: ZTE Corporation

Test Date: Aug-2025

Hardware Availability: Feb-2024

Software Availability: Mar-2024

Peak Portability Flags (Continued)

525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallocc
502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
505.mcf_r: basepeak = yes
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallocc
557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

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Peak Optimization Flags (Continued)

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/ZTE-Platform-Settings-EMR-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/ZTE-Platform-Settings-EMR-V1.1.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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