



# SPEC CPU®2017 Floating Point Rate Result

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## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)  
( Tyrone Camarero MDA200A3N-212)  
(2.55 GHz, AMD EPYC 9684X)

SPECrate®2017\_fp\_base = 720

SPECrate®2017\_fp\_peak = 665

CPU2017 License: 6042

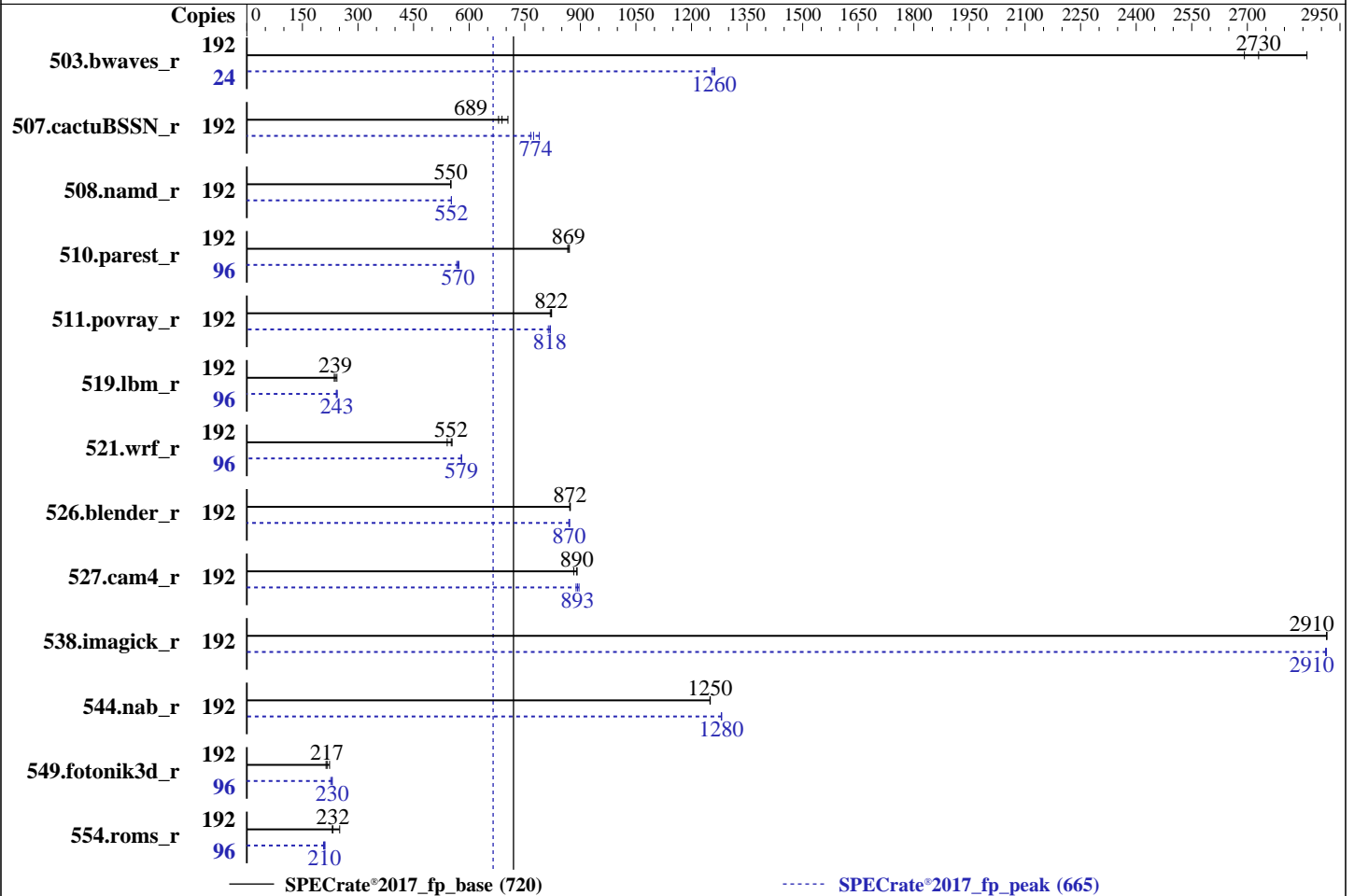
Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025



### Hardware

CPU Name: AMD EPYC 9684X  
 Max MHz: 3700  
 Nominal: 2550  
 Enabled: 96 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 1152 MB I+D on chip per chip, 96 MB shared / 8 cores  
 Other: None  
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)  
 Storage: 1 x 1 TB NVMe  
 Other: CPU Cooling: Air

### Software

OS: Ubuntu 24.04 LTS  
 kernel version 6.14.0-29-generic  
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: No  
 Firmware: Version ES312AMS.116 released Sep-2025  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: OS and BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	192	673	2860	<b><u>705</u></b>	<b><u>2730</u></b>	715	2690	24	<b><u>191</u></b>	<b><u>1260</u></b>	192	1260	191	1260
507.cactuBSSN_r	192	345	705	<b><u>353</u></b>	<b><u>689</u></b>	358	679	192	308	789	317	766	<b><u>314</u></b>	<b><u>774</u></b>
508.namd_r	192	331	550	331	550	<b><u>331</u></b>	<b><u>550</u></b>	192	<b><u>330</u></b>	<b><u>552</u></b>	330	552	330	552
510.parest_r	192	<b><u>578</u></b>	<b><u>869</u></b>	580	866	577	871	96	439	572	443	567	<b><u>441</u></b>	<b><u>570</u></b>
511.povray_r	192	<b><u>545</u></b>	<b><u>822</u></b>	545	822	547	819	192	<b><u>548</u></b>	<b><u>818</u></b>	547	819	551	814
519.lbm_r	192	832	243	<b><u>847</u></b>	<b><u>239</u></b>	858	236	96	419	241	415	244	<b><u>417</u></b>	<b><u>243</u></b>
521.wrf_r	192	<b><u>780</u></b>	<b><u>552</u></b>	776	554	795	541	96	371	579	<b><u>371</u></b>	<b><u>579</u></b>	371	580
526.blender_r	192	<b><u>335</u></b>	<b><u>872</u></b>	335	873	336	871	192	336	871	336	869	<b><u>336</u></b>	<b><u>870</u></b>
527.cam4_r	192	<b><u>377</u></b>	<b><u>890</u></b>	381	882	377	891	192	375	897	<b><u>376</u></b>	<b><u>893</u></b>	378	889
538.imagick_r	192	<b><u>164</u></b>	<b><u>2910</u></b>	164	2910	164	2910	192	<b><u>164</u></b>	<b><u>2910</u></b>	164	2910	164	2910
544.nab_r	192	258	1250	259	1250	<b><u>258</u></b>	<b><u>1250</u></b>	192	<b><u>252</u></b>	<b><u>1280</u></b>	252	1280	252	1280
549.fotonik3d_r	192	3351	223	3499	214	<b><u>3453</u></b>	<b><u>217</u></b>	96	<b><u>1625</u></b>	<b><u>230</u></b>	1652	227	1624	230
554.roms_r	192	<b><u>1312</u></b>	<b><u>232</u></b>	1323	231	1217	251	96	<b><u>727</u></b>	<b><u>210</u></b>	737	207	723	211

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



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## Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOCONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Settings:

cTDP: 400

Determinism Slider set to Power

ACPI SRAT L3 Cache as NUMA Domain: enabled

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on amd-benchmark Wed Sep 17 01:04:51 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-lubuntu8.8)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage

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## Platform Notes (Continued)

17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
18. OS release  
19. Disk information  
20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS

1. uname -a  
Linux amd-benchmark 6.14.0-29-generic #29~24.04.1-Ubuntu SMP PREEMPT\_DYNAMIC Thu Aug 14 16:52:50 UTC 2  
x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
01:04:51 up 6:36, 1 user, load average: 92.60, 165.42, 182.16  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
amd tty1 - 18:31 6:32m 0.81s 0.76s sudo su

3. Username  
From environment variable \$USER: root  
From the command 'logname': amd

4. ulimit -a  
time(seconds) unlimited  
file(blocks) unlimited  
data(kbytes) unlimited  
stack(kbytes) unlimited  
coredump(blocks) 0  
memory(kbytes) unlimited  
locked memory(kbytes) 2097152  
process 6190307  
nofiles 1024  
vmemory(kbytes) unlimited  
locks unlimited  
rtprio 0

5. sysinfo process ancestry  
/sbin/init splash  
/bin/login -p --  
-bash  
sudo su  
sudo su  
su  
bash  
python3 ./run\_amd\_rate\_aocc500\_znver5\_A1.py  
/bin/bash ./amd\_rate\_aocc500\_znver5\_A1.sh  
runcpu --config amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 fprate  
runcpu --configfile amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 --nopower  
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile  
\$SPEC/tmp/CPU2017.002/tempslogs/preenv.fprate.002.0.log --lognum 002.0 --from\_runcpu 2  
specperl \$SPEC/bin/sysinfo  
\$SPEC = /home/cpu2017

6. /proc/cpuinfo  
model name : AMD EPYC 9684X 96-Core Processor

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## Platform Notes (Continued)

```

vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping      : 2
microcode     : 0xa101248
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass rsrso
TLB size      : 3584 4K pages
cpu cores     : 96
siblings      : 192
1 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-95
physical id 0: apicids 0-191

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 192
On-line CPU(s) list:   0-191
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        Advanced Micro Devices, Inc.
Model name:             AMD EPYC 9684X 96-Core Processor
BIOS Model name:       AMD EPYC 9684X 96-Core Processor
                       2.5GHz
                       Unknown CPU @
BIOS CPU family:       107
CPU family:             25
Model:                  17
Thread(s) per core:    2
Core(s) per socket:    96
Socket(s):              1
Stepping:               2
Frequency boost:        enabled
CPU(s) scaling MHz:    102%
CPU max MHz:            2550.0000
CPU min MHz:            1500.0000
BogoMIPS:               5092.44
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                       pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
                       pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl xtopology
                       nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq
                       monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes
                       xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
                       abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce
                       topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
                       cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
                       ibrs_enhanced vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid
                       cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                       clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
                       xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                       user_shstk avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
                       amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale
                       vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic
                       v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku

```

(Continued on next page)



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## Platform Notes (Continued)

ospke avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg  
avx512\_vpopcntdq la57 rdpid overflow\_recov succor smca fsrm  
flush\_lld debug\_swap

### Virtualization:

L1d cache: 3 MiB (96 instances)  
L1i cache: 3 MiB (96 instances)  
L2 cache: 96 MiB (96 instances)  
L3 cache: 1.1 GiB (12 instances)

NUMA node(s): 2

NUMA node0 CPU(s): 0-191

Vulnerability Gather data sampling: Not affected

Vulnerability Ghostwrite: Not affected

Vulnerability Indirect target selection: Not affected

Vulnerability Itlb multihit: Not affected

Vulnerability Lltf: Not affected

Vulnerability MDS: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Reg file data sampling: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec rstack overflow: Mitigation; Safe RET

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; PBRSE-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

### From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	3M	8	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	1M	96M	8	Unified	2	2048	1	64
L3	96M	1.1G	16	Unified	3	98304	1	64

### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-191

node 0 size: 773768 MB

node 0 free: 769729 MB

node 1 cpus:

node 1 size: 774045 MB

node 1 free: 772385 MB

node distances:

node 0 1

0: 10 32

1: 32 10

### 9. /proc/meminfo

MemTotal: 1584961664 kB

### 10. who -r

run-level 3 Sep 16 18:29

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## Platform Notes (Continued)

11. Systemd service manager version: systemd 255 (255.4-lubuntu8.8)

```
Default Target Status
multi-user      running
```

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon anacron apparmor apport avahi-daemon bluetooth cloud-config cloud-final cloud-init cloud-init-local collectd console-setup cron cups cups-browsed dmesg e2scrub_reap getty@ gnome-remote-desktop gpu-manager grub-common grub-initrd-fallback kerneloops keyboard-setup networkd-dispatcher openvpn power-profiles-daemon rsyslog secureboot-db setvtrgb snapd ssl-cert sssd switcheroo-control sysstat systemd-oomd systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw unattended-upgrades wpa_supplicant
enabled-runtime	netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled	brltty console-getty debug-shell ipmievdf nftables openvpn-client@ openvpn-server@ openvpn@ rsync rtkit-daemon serial-getty@ speech-dispatcherd ssh systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-networkd systemd-networkd-wait-online systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext systemd-time-wait-sync upower wpa_supplicant-nl80211@ wpa_supplicant-wired@ wpa_supplicant@
generated	openipmi speech-dispatcher
indirect	saned@ spice-vdagentd sssd-autofs sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate systemd-sysupdate-reboot uuidd
masked	alsa-utils cryptdisks cryptdisks-early hwclock saned sudo x11-common

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/vmlinuz-6.14.0-29-generic
root=UUID=4c4111a1-2027-4689-8cc8-4d6d365932ca
ro
quiet
splash
vt.handoff=7
```

14. cpupower frequency-info

```
analyzing CPU 160:
  current policy: frequency should be within 1.50 GHz and 2.55 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes
    Boost States: 0
    Total States: 3
    Pstate-P0: 2550MHz
```

15. sysctl

```
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
```

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## Platform Notes (Continued)

```

vm.dirty_ratio                8
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds  43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio        1
vm.nr_hugepages               0
vm.nr_hugepages_mempolicy    0
vm.nr_overcommit_hugepages   0
vm.swappiness                  1
vm.watermark_boost_factor    15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode         1

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer defer+madvise madvise never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS

```

```

-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p5 ext4 701G 7.8G 657G 2% /home

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor:          Tyrone Systems
Product:         Tyrone Camarero MDA200A3N-212
Product Family: Camarero

```

```

-----
21. dmidecode
Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  24x Samsung M321R8GA0BB0-CQKZJ 64 GB 2 rank 4800

```

```

-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)

```

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## Platform Notes (Continued)

BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: ES312AMS.116  
BIOS Date: 09/01/2025  
BIOS Revision: 5.27

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

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# SPEC CPU®2017 Floating Point Rate Result

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**Tyrone Systems**  
(Test Sponsor: Netweb Technologies India Ltd)  
( Tyrone Camarero MDA200A3N-212)  
(2.55 GHz, AMD EPYC 9684X)

**SPECrate®2017\_fp\_base = 720**  
**SPECrate®2017\_fp\_peak = 665**

**CPU2017 License:** 6042  
**Test Sponsor:** Netweb Technologies India Ltd  
**Tested by:** Tyrone Systems

**Test Date:** Sep-2025  
**Hardware Availability:** Oct-2024  
**Software Availability:** Aug-2025

## Compiler Version Notes (Continued)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

-----  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

Benchmarks using both Fortran and C:  
flang clang

Benchmarks using both C and C++:  
clang++ clang

Benchmarks using Fortran, C, and C++:  
clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)  
( Tyrone Camarero MDA200A3N-212)  
(2.55 GHz, AMD EPYC 9684X)

SPECrate®2017\_fp\_base = 720

SPECrate®2017\_fp\_peak = 665

**CPU2017 License:** 6042

**Test Sponsor:** Netweb Technologies India Ltd

**Tested by:** Tyrone Systems

**Test Date:** Sep-2025

**Hardware Availability:** Oct-2024

**Software Availability:** Aug-2025

## Base Portability Flags (Continued)

```
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

### C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

### C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

### Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl
```

### Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
```

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# SPEC CPU®2017 Floating Point Rate Result

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## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)  
( Tyrone Camarero MDA200A3N-212)  
(2.55 GHz, AMD EPYC 9684X)

SPECrate®2017\_fp\_base = 720

SPECrate®2017\_fp\_peak = 665

**CPU2017 License:** 6042

**Test Sponsor:** Netweb Technologies India Ltd

**Tested by:** Tyrone Systems

**Test Date:** Sep-2025

**Hardware Availability:** Oct-2024

**Software Availability:** Aug-2025

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamplibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamplibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamplibm -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

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# SPEC CPU®2017 Floating Point Rate Result

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**Tyrone Systems**  
(Test Sponsor: Netweb Technologies India Ltd)  
( Tyrone Camarero MDA200A3N-212)  
(2.55 GHz, AMD EPYC 9684X)

SPECrate®2017\_fp\_base = 720  
SPECrate®2017\_fp\_peak = 665

**CPU2017 License:** 6042  
**Test Sponsor:** Netweb Technologies India Ltd  
**Tested by:** Tyrone Systems

**Test Date:** Sep-2025  
**Hardware Availability:** Oct-2024  
**Software Availability:** Aug-2025

## Base Other Flags (Continued)

Benchmarks using both Fortran and C:  
-Wno-unused-command-line-argument

Benchmarks using both C and C++:  
-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:  
-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

Benchmarks using both Fortran and C:  
flang clang

Benchmarks using both C and C++:  
clang++ clang

Benchmarks using Fortran, C, and C++:  
clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6  
-Wl, -mllvm -Wl, -reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
```

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## Tyrone Systems

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**CPU2017 License:** 6042

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**Tested by:** Tyrone Systems

**Test Date:** Sep-2025

**Hardware Availability:** Oct-2024

**Software Availability:** Aug-2025

## Peak Optimization Flags (Continued)

519.lbm\_r (continued):

```
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

538.imagick\_r: Same as 519.lbm\_r

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

```
510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

Fortran benchmarks:

```
503.bwaves_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -ldl -lflang
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)  
( Tyrone Camarero MDA200A3N-212)  
(2.55 GHz, AMD EPYC 9684X)

SPECrate®2017\_fp\_base = 720

SPECrate®2017\_fp\_peak = 665

**CPU2017 License:** 6042

**Test Sponsor:** Netweb Technologies India Ltd

**Tested by:** Tyrone Systems

**Test Date:** Sep-2025

**Hardware Availability:** Oct-2024

**Software Availability:** Aug-2025

## Peak Optimization Flags (Continued)

549.fotonik3d\_r (continued):

```
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

554.roms\_r: Same as 503.bwaves\_r

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

```
527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -freemap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

Benchmarks using both C and C++:

```
511.povray_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm
-lamdalloc -ldl
```

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# SPEC CPU®2017 Floating Point Rate Result

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## Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)  
(Tyrone Camarero MDA200A3N-212)  
(2.55 GHz, AMD EPYC 9684X)

SPECrate®2017\_fp\_base = 720

SPECrate®2017\_fp\_peak = 665

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

## Peak Optimization Flags (Continued)

```
526.blender_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-faggressive-loop-transform -fvector-transform -fscalar-transform
-Mrecursive -fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

## Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```





# SPEC CPU®2017 Floating Point Rate Result

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**Tyrone Systems**  
(Test Sponsor: Netweb Technologies India Ltd)  
( Tyrone Camarero MDA200A3N-212)  
(2.55 GHz, AMD EPYC 9684X)

**SPECrate®2017\_fp\_base = 720**

**SPECrate®2017\_fp\_peak = 665**

**CPU2017 License:** 6042

**Test Sponsor:** Netweb Technologies India Ltd

**Tested by:** Tyrone Systems

**Test Date:** Sep-2025

**Hardware Availability:** Oct-2024

**Software Availability:** Aug-2025

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Turin-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Turin-revD.xml>

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