



SPEChpc™ 2021 Tiny Result

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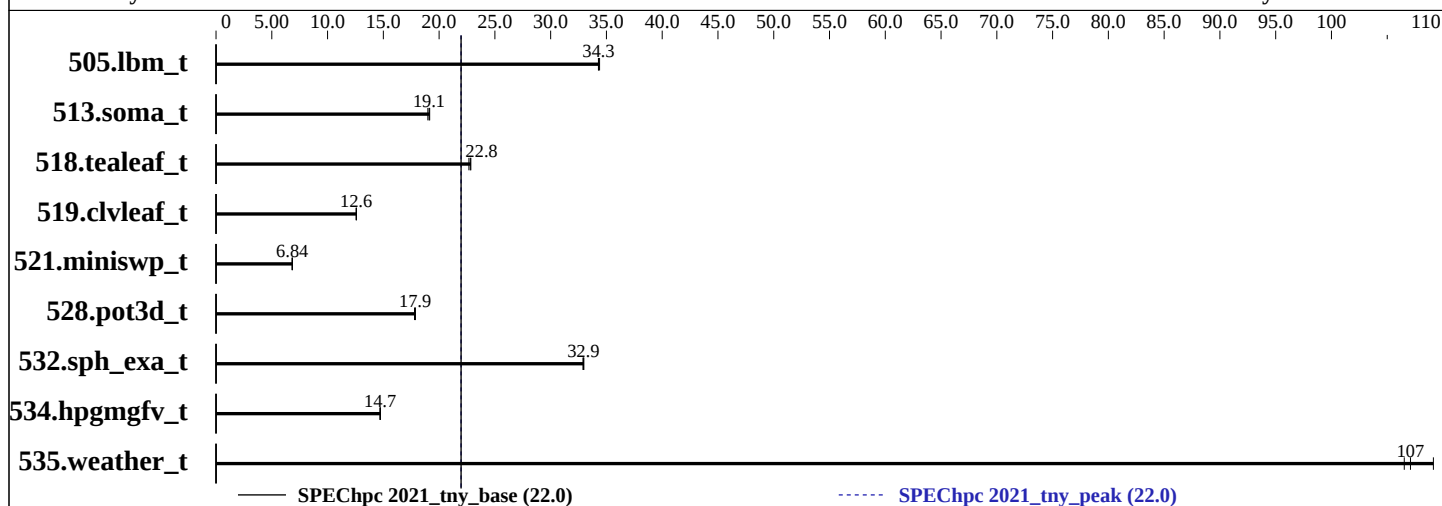
Lenovo
SR860 V4(Intel Xeon 6788P)

SPEChpc 2021_tny_base = 22.0

SPEChpc 2021_tny_peak = 22.0

hpc2021 License: 9017
Test Sponsor: Lenovo
Tested by: Lenovo

Test Date: Sep-2025
Hardware Availability: Sep-2025
Software Availability: Jun-2025



Results Table

Benchmark	Base										Peak							
	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
505.lbm_t	MPI	344	1	65.6	34.3	65.4	34.4	65.6	34.3	MPI	344	1	65.6	34.3	65.4	34.4	65.6	34.3
513.soma_t	MPI	344	1	195	19.0	194	19.1	193	19.2	MPI	344	1	195	19.0	194	19.1	193	19.2
518.tealeaf_t	MPI	344	1	72.8	22.7	72.4	22.8	72.2	22.8	MPI	344	1	72.8	22.7	72.4	22.8	72.2	22.8
519.civleaf_t	MPI	344	1	131	12.6	131	12.6	131	12.6	MPI	344	1	131	12.6	131	12.6	131	12.6
521.miniswp_t	MPI	344	1	235	6.82	234	6.85	234	6.84	MPI	344	1	235	6.82	234	6.85	234	6.84
528.pot3d_t	MPI	344	1	119	17.8	119	17.9	119	17.9	MPI	344	1	119	17.8	119	17.9	119	17.9
532.sph_exa_t	MPI	344	1	59.3	32.9	59.2	32.9	59.1	33.0	MPI	344	1	59.3	32.9	59.2	32.9	59.1	33.0
534.hpgmgfv_t	MPI	344	1	79.9	14.7	79.7	14.7	79.8	14.7	MPI	344	1	79.9	14.7	79.7	14.7	79.8	14.7
535.weather_t	MPI	344	1	30.1	107	30.3	107	29.5	109	MPI	344	1	30.1	107	30.3	107	29.5	109

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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Hardware Summary

Type of System: Homogenous Cluster
Compute Node: SR860 V4(Intel Xeon 6788P)
Interconnect: None
Compute Nodes Used: 1
Total Chips: 4
Total Cores: 344
Total Threads: 688
Total Memory: 1 TB
Total Accelerators: 0
Max. Peak Threads: 1

Software Summary

Compiler: Intel oneAPI Compiler 2025.2.1
MPI Library: Intel MPI Library 2021.16 for Linux OS
Other MPI Info: None
Other Software: None
Base Parallel Model: MPI
Base Ranks Run: 344
Base Threads Run: 1
Peak Parallel Models: MPI
Minimum Peak Ranks: 344
Maximum Peak Ranks: 344
Max. Peak Threads: 1
Min. Peak Threads: 1

Node Description: SR860 V4(Intel Xeon 6788P)

Hardware

Number of nodes: 1
Uses of the node: Compute
Vendor: Intel
Model: SR860 V4(Intel Xeon 6788P)
CPU Name: Intel Xeon 6788P
CPU(s) orderable: 4 chips
Chips enabled: 4
Cores enabled: 344
Cores per chip: 86
Threads per core: 2
CPU Characteristics: Turbo Boost Technology up to 3.8 GHz
CPU MHz: 2000
Primary Cache: 64 KB I + 48 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per core
L3 Cache: 336 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-6400B-R)
Disk Subsystem: 1 x 1.92 TB NVMe SSD
Other Hardware: None
Accel Count: None
Accel Model: None
Accel Vendor: None
Accel Type: None
Accel Connection: None
Accel ECC enabled: None
Accel Description: None
Adapter: None
Number of Adapters: 0
Slot Type: None
Data Rate: None
Ports Used: 0
Interconnect Type: None

Software

Accelerator Driver: None
Adapter: None
Adapter Driver: None
Adapter Firmware: None
Operating System: Ubuntu 24.02.2 LTS
6.8.0-79-generic
Local File System: ext4
Shared File System: None
System State: Run level 5
Other Software: None



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Interconnect Description: None

Hardware

Vendor: None
Model: None
Switch Model: None
Number of Switches: 0
Number of Ports: 0
Data Rate: None
Firmware: None
Topology: None
Primary Use: None

Software

: --

Submit Notes

The config file option 'submit' was used.

Compiler Version Notes

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CXXC 532.sph_exa_t(base)

Intel(R) oneAPI DPC++/C++ Compiler 2025.2.1 (2025.2.0.20250806)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/intel/oneapi/compiler/2025.2/bin/compiler
Configuration file: /opt/intel/oneapi/compiler/2025.2/bin/compiler/./icpx.cfg

=====

CC 505.lbm_t(base) 513.soma_t(base) 518.tealeaf_t(base) 521.miniswp_t(base)
534.hpgmgfv_t(base)

Intel(R) oneAPI DPC++/C++ Compiler 2025.2.1 (2025.2.0.20250806)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/intel/oneapi/compiler/2025.2/bin/compiler
Configuration file: /opt/intel/oneapi/compiler/2025.2/bin/compiler/./icx.cfg

=====

FC 519.clvleaf_t(base) 528.pot3d_t(base) 535.weather_t(base)

ifx (IFX) 2025.2.1 20250806
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Base Compiler Invocation

C benchmarks:

mpiicc -cc=icx

C++ benchmarks:

mpiicpc -cxx=icpx

Fortran benchmarks:

mpiifort -fc=ifx

Base Portability Flags

505.lbm_t: -lstdc++ -std=c++14
513.soma_t: -lstdc++ -std=c++14
518.tealeaf_t: -lstdc++ -std=c++14
521.miniswp_t: -lstdc++ -std=c++14
534.hpgmgfv_t: -lstdc++ -std=c++14

Base Optimization Flags

C benchmarks:

-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops

C++ benchmarks:

-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops

Fortran benchmarks:

-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops -nostandard-realloc-lhs -align array64byte

Base Other Flags

C benchmarks (except as noted below):

-Ispecmpitime -Wno-incompatible-function-pointer-types

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Base Other Flags (Continued)

521.miniswp_t: -Ispecmpitime/ -Wno-incompatible-function-pointer-types

534.hpgmgfv_t: -Ispecmpitime -Wno-incompatible-function-pointer-types

C++ benchmarks:
-Ispecmpitime

Fortran benchmarks:

519.clvleaf_t: -Ispecmpitime

Peak Optimization Flags

C benchmarks:

505.lbm_t: basepeak = yes

513.soma_t: basepeak = yes

518.tealeaf_t: basepeak = yes

521.miniswp_t: basepeak = yes

534.hpgmgfv_t: basepeak = yes

C++ benchmarks:

532.sph_exa_t: basepeak = yes

Fortran benchmarks:

519.clvleaf_t: basepeak = yes

528.pot3d_t: basepeak = yes

535.weather_t: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2024-12-11.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2024-12-11.xml



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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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