



SPEC® MPIM2007 Result

Copyright 2006-2010 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR650
(Intel Xeon Platinum 8180 CPU, 2.50 GHz)

SPECmpiM_peak2007 = Not Run

SPECmpiM_base2007 = 18.1

MPI2007 license: 28

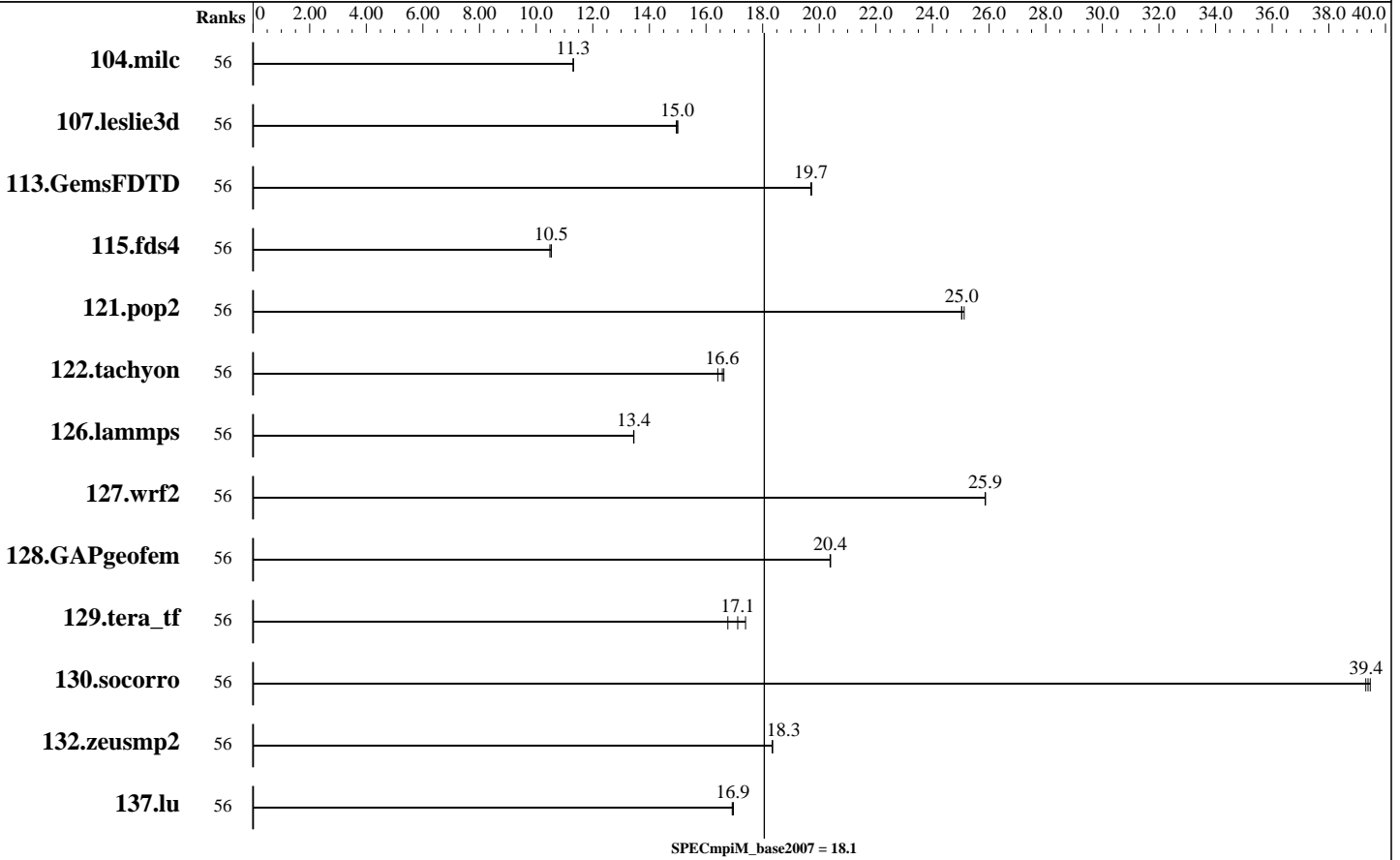
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Aug-2018

Hardware Availability: Aug-2017

Software Availability: Jul-2018



Results Table

Benchmark	Base							Peak						
	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
104.milc	56	<u>138</u>	<u>11.3</u>	138	11.3	138	11.3							
107.leslie3d	56	<u>348</u>	<u>15.0</u>	349	14.9	348	15.0							
113.GemsFDTD	56	320	19.7	320	19.7	<u>320</u>	<u>19.7</u>							
115.fds4	56	<u>185</u>	<u>10.5</u>	186	10.5	185	10.5							
121.pop2	56	<u>165</u>	<u>25.0</u>	164	25.1	165	25.0							
122.tachyon	56	170	16.4	<u>169</u>	<u>16.6</u>	168	16.6							
126.lammgs	56	217	13.4	217	13.5	<u>217</u>	<u>13.4</u>							
127.wrf2	56	<u>301</u>	<u>25.9</u>	301	25.9	301	25.9							
128.GAPgeofem	56	101	20.4	<u>101</u>	<u>20.4</u>	101	20.4							
129.tera_tf	56	<u>162</u>	<u>17.1</u>	159	17.4	165	16.8							

Table continues on next page. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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Results Table (Continued)

Benchmark	Base							Peak						
	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
130.socorro	56	<u>96.9</u>	<u>39.4</u>	96.7	39.5	97.1	39.3							
132.zeusmp2	56	<u>169</u>	<u>18.3</u>	169	18.3	169	18.3							
137.lu	56	<u>217</u>	<u>16.9</u>	217	16.9	217	17.0							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Hardware Summary

Type of System: Homogeneous
 Compute Node: ThinkSystem SR650
 Interconnect: N/A
 Total Compute Nodes: 1
 Total Chips: 2
 Total Cores: 56
 Total Threads: 56
 Total Memory: 768 GB
 Base Ranks Run: 56
 Minimum Peak Ranks: --
 Maximum Peak Ranks: --

Software Summary

C Compiler: Intel C++ Compiler 17.0 Update 7 for Linux
 Version 17.0.7 Build 20180403
 C++ Compiler: Intel C++ Compiler 17.0 Update 7 for Linux
 Version 17.0.7 Build 20180403
 Fortran Compiler: Intel Fortran Compiler 17.0 Update 7 for Linux
 Version 17.0.7 Build 20180403
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 MPI Library: Intel MPI Library for Linux* OS
 Version 2018 Update 3 Build 20180411
 Other MPI Info: None
 Pre-processors: No
 Other Software: None

Node Description: ThinkSystem SR650

Hardware

Number of nodes: 1
 Uses of the node: compute
 Vendor: Lenovo Global Technology
 Model: SR650
 CPU Name: Intel Xeon Platinum 8180
 CPU(s) orderable: 1-2 chips
 Chips enabled: 2
 Cores enabled: 56
 Cores per chip: 28
 Threads per core: 1
 CPU Characteristics: None
 CPU MHz: 2500
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 39424 KB I+D on chip per chip
 shared / 28 cores
 Other Cache: None
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V)
 Disk Subsystem: 800 GB 12 Gbps SAS 2.5" SSD (JBOD)
 Other Hardware: ThinkSystem RAID 930-8i 2GB Flash
 Adapter: N/A
 Number of Adapters: 0
 Slot Type: N/A
 Data Rate: N/A

Software

Adapter: N/A
 Adapter Driver: N/A
 Adapter Firmware: N/A
 Operating System: Red Hat Enterprise Linux Server release 7.3,
 Kernel 3.10.0-514.48.5.el7.x86_64
 Local File System: xfs
 Shared File System: None
 System State: None
 Other Software: None

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Node Description: ThinkSystem SR650

Ports Used: 0
Interconnect Type: None

Interconnect Description: N/A

Hardware

Vendor: N/A
Model: N/A
Switch Model: N/A
Number of Switches: 0
Number of Ports: 0
Data Rate: N/A
Firmware: N/A
Topology: N/A
Primary Use: N/A

Software

Submit Notes

The config file option 'submit' was used.

General Notes

MPI startup command:
mpiexec command was used to start MPI jobs.

RAM configuration:
Compute nodes have 2 x 32 GB RDIMM on each memory channel.

Add "intel_idle.max_cstate=0 intel_pstate=disable" into grub

BIOS settings:
Operating Mode : Maximum Performance Mode
Intel Hyper-Threading Technology (SMT): Disabled
SNC (Sub-NUMA Cluster): Enable

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



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Base Compiler Invocation

C benchmarks:
mpiicc

C++ benchmarks:

126.lammps: mpiicpc

Fortran benchmarks:

mpiifort

Benchmarks using both Fortran and C:

mpiicc mpiifort

Base Portability Flags

121.pop2: -DSPEC_MPI_CASE_FLAG
126.lammps: -DMPICH_IGNORE_CXX_SEEK
127.wrf2: -DSPEC_MPI_CASE_FLAG -DSPEC_MPI_LINUX
130.socorro: -assume nostd_intent_in

Base Optimization Flags

C benchmarks:
-O3 -ipo -xCORE-AVX512 -no-prec-div

C++ benchmarks:

126.lammps: -O3 -ipo -xCORE-AVX512 -no-prec-div

Fortran benchmarks:

-O3 -ipo -xCORE-AVX512 -no-prec-div

Benchmarks using both Fortran and C:

-O3 -ipo -xCORE-AVX512 -no-prec-div

The flags files that were used to format this result can be browsed at

http://www.spec.org/mpi2007/flags/Lenovo-SPECmpiM_Platform_Flags.20180912.html
http://www.spec.org/mpi2007/flags/EM64T_Intel121_flags.20170711.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/mpi2007/flags/Lenovo-SPECmpiM_Platform_Flags.20180912.xml
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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