CFP2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

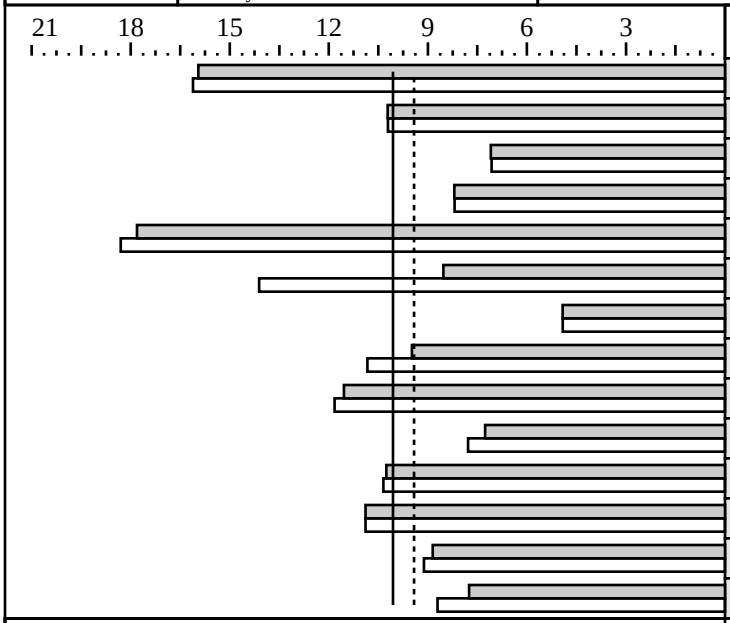
Advanced Micro Devices

Tyan Thunder K7 Motherboard, AMD Athlon (TM) MP 1800+

SPECfp_rate2000 = 10.1

SPECfp_rate_base2000 = 9.42

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: Oct-2001 | Hardware Avail: Oct-2001 | Software Avail: Sep-2001



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	233	16.0	2	230	16.1
171.swim	2	704	10.2	2	704	10.2
172.mgrid	2	589	7.09	2	591	7.07
173.applu	2	594	8.20	2	595	8.19
177.mesa	2	182	17.8	2	177	18.3
178.galgel	2	789	8.53	2	477	14.1
179.art	2	1227	4.92	2	1227	4.92
183.equake	2	318	9.48	2	278	10.8
187.facerec	2	382	11.5	2	373	11.8
188.ammp	2	702	7.27	2	656	7.78
189.lucas	2	452	10.3	2	448	10.3
191.fma3d	2	448	10.9	2	447	10.9
200.sixtrack	2	288	8.85	2	280	9.12
301.apsi	2	778	7.75	2	693	8.71

Hardware

CPU:	AMD Athlon (TM) MP 1800+
CPU MHz:	1533
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	256KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x256MB PC2100 DDR SDRAM CL2 Registered
Disk Subsystem:	Seagate ST330620A
Other Hardware:	None

Software

Operating System:	Windows 2000 SP1
Compiler:	Compaq Visual Fortran 6.6 Intel C/C++ 5.0.1 build 010525Z Intel Fortran 5.0.1 build 010525Z Microsoft Visual Studio 6.0 SP5 (libraries) MicroQuill Smartheap Library 5.0
File System:	FAT32
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
icl and ifl are the Intel C/C++ and Fortran compilers
f90 is the Compaq Fortran compiler
shlW32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com
Portability:
178.galgel: -FI -Fe$@ -link -stack:32000000
Baseline: C       icl -QxK -Qipo +FDO shlW32M.lib
Baseline: Fortran ifl -O3 -QxK -Qipo +FDO
Peak tuning:
168.wupwise:  ifl -O3 -QxK -Qwp_ipo +FDO
171.swim:     ifl -O3 -QxK -Qwp_ipo +FDO
172.mgrid:    ifl -O3 -QxK -Qwp_ipo +FDO
173.applu:    ifl -O3 -QxK -Qwp_ipo -Qscalar_rep- -Qauto +FDO
177.mesa:     icl -O3 -QxK -Qwp_ipo -Oa -Qunroll0 +FDO shlw32M.lib
178.galgel:   f90 -Optimize:5 -fast
179.art:      icl -O3 -QxK -QaxW -Qwp_ipo -Oa +FDO shlW32M.lib
183.equake:   icl -O3 -QxK -Qwp_ipo -Qrcd -Oa +FDO shlW32M.lib
```

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
 187.facerec:  f90 -Optimize:5 -fast
 188.ammp:     icl -O3 -QxK -Qwp_ipo -Oa +FDO
 189.lucas:    ifl -O3 -QxK -Qwp_ipo +FDO shlW32M.lib
 191.fma3d:    ifl -O3 -QxK -Qwp_ipo +FDO
 200.sixtrack: ifl -QxK -QaxW -Qwp_ipo -Qprefetch +FDO
 301.apsi:     f90 -Optimize:5 -fast
 Library ordering for 189.lucas (to include SmartHeap correctly with default libs):
 LIBS=libIEPCF90.lib libintrins.lib libF90.lib
 libqwind.lib libm.lib shlW32M.lib LIBC.lib libirc.lib OLDNAMES.lib
 ONESTEP is used for all base and peak runs
The tested system can be assembled using a case such as the Chenming 601 AE-F-O,
a 460W power supply such as the Delta Electronics DPS465 AB A
The System bus runs at 266MHz
```