



CFP2000 Result

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Advanced Micro Devices

Tyan Thunder K7 Motherboard, AMD Athlon (TM) MP 1500+

SPECfp_rate2000 = 9.78

SPECfp_rate_base2000 = 9.17

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: Sep-2001 | Hardware Avail: Oct-2001 | Software Avail: Sep-2001

							Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio		
18	16	14	12	10	8	6	4	2	168.wupwise	2	248	15.0	2	249	14.9
									171.swim	2	686	10.5	2	686	10.5
									172.mgrid	2	574	7.27	2	574	7.27
									173.applu	2	600	8.12	2	589	8.27
									177.mesa	2	199	16.4	2	200	16.2
									178.galgel	2	804	8.37	2	478	14.1
									179.art	2	1192	5.06	2	1194	5.05
									183.equake	2	332	9.08	2	286	10.5
									187.facerec	2	398	11.1	2	394	11.2
									188.amp	2	714	7.15	2	663	7.70
									189.lucas	2	450	10.3	2	448	10.3
									191.fma3d	2	462	10.6	2	463	10.5
									200.sixtrack	2	328	7.78	2	318	8.02
									301.apsi	2	789	7.64	2	706	8.54

Hardware

CPU: AMD Athlon (TM) MP 1500+
CPU MHz: 1333
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip
CPU(s) orderable: 1, 2
Parallel: No
Primary Cache: 64KBI + 64KBD on chip
Secondary Cache: 256KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 2x256MB PC2100 DDR SDRAM CL2 Registered
Disk Subsystem: IBM DPTA-372020
Other Hardware: None

Software

Operating System: Windows 2000 SP2
Compiler: Compaq Visual Fortran 6.6
Intel C/C++ 5.0.1 build 010525Z
Intel Fortran 5.0.1 build 010525Z
Microsoft Visual Studio 6.0 SP5 (libraries)
MicroQuill Smartheap Library 5.0
File System: FAT32
System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
icl and ifl are the Intel C/C++ and Fortran compilers
f90 is the Compaq Fortran compiler
shlw32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com
Portability:
178.galgel: -FI -Fe\$@ -link -stack:32000000
Baseline: C icl -QxK -Qipo +FDO shlw32M.lib
Baseline: Fortran ifl -O3 -QxK -Qipo +FDO
Peak tuning:
168.wupwise: ifl -O3 -QxK -Qwp_ipo +FDO
171.swim: ifl -O3 -QxK -Qwp_ipo +FDO
172.mgrid: ifl -O3 -QxK -Qwp_ipo +FDO
173.applu: ifl -O3 -QxK -Qwp_ipo -Qscalar_rep- -Qauto +FDO
177.mesa: icl -O3 -QxK -Qwp_ipo -Oa -Qunroll10 +FDO shlw32M.lib
178.galgel: f90 -Optimize:5 -fast
179.art: icl -O3 -QxK -QaxW -Qwp_ipo -Oa +FDO shlw32M.lib
183.equake: icl -O3 -QxK -Qwp_ipo -Qrcd -Oa +FDO shlw32M.lib



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Notes/Tuning Information (Continued)

```
187.facerec: f90 -Optimize:5 -fast
188.ampp:    icl -O3 -QxK -Qwp_ipo -Oa +FDO
189.lucas:   ifl -O3 -QxK -Qwp_ipo +FDO shlw32M.lib
191.fma3d:   ifl -O3 -QxK -Qwp_ipo +FDO
200.sixtrack: ifl -QxK -QaxW -Qwp_ipo -Qprefetch +FDO
301.apsi:    f90 -Optimize:5 -fast
```

Library ordering for 189.lucas (to include SmartHeap correctly with default libs):

```
LIBS=libIEPCF90.lib libintrins.lib libF90.lib
libqwind.lib libm.lib shlw32M.lib LIBC.lib libirc.lib OLDNAMES.lib
```

ONESTEP is used for all base and peak runs

The tested system can be assembled using a case such as the Chenming 601 AE-F-O,
a 460W power supply such as the Delta Electronics DPS465 AB A

The System bus runs at 266MHz