



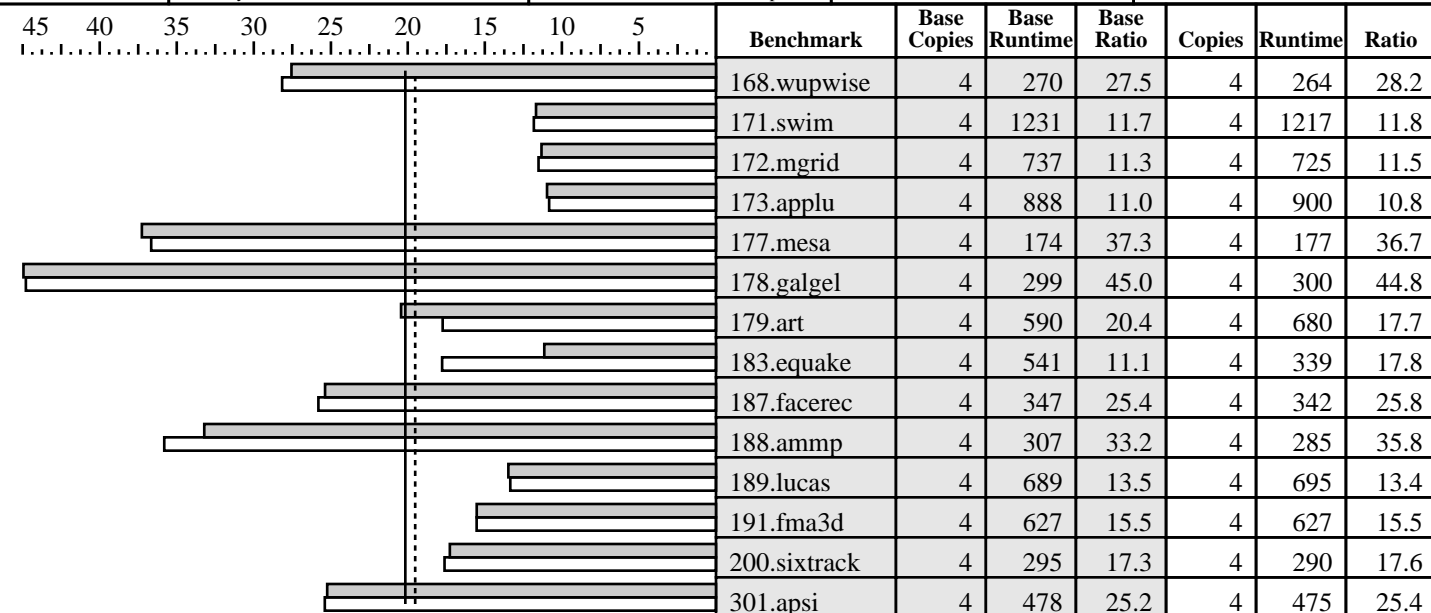
# CFP2000 Result

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**Dell**  
**PowerEdge 6650 (2.0 GHz Xeon MP)**

SPECfp\_rate2000 = 20.2  
SPECfp\_rate\_base2000 = 19.5

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Sep-2002 | Hardware Avail: Nov-2002 | Software Avail: Jun-2001



### Hardware

CPU: Intel Xeon (400 MHz system bus)  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 4 chips, 1 core/chip (Hyper-Threading Technology disabled)  
 CPU(s) orderable: 1, 2, 4  
 Parallel: No  
 Primary Cache: 12K(I) micro-ops + 8KB(D) on chip  
 Secondary Cache: 512KB(I+D) on chip  
 L3 Cache: 2048KB(I+D) on chip  
 Other Cache: None  
 Memory: 8 x 512MB DDR200  
 Disk Subsystem: 1 x 36GB SEAGATE ST336706LC  
 Other Hardware: None

### Software

Operating System: Windows 2000 Advanced Server (SP2)  
 Compiler: Intel C++ and Fortran Compiler 5.0.1 (020125Z)  
 Microsoft Visual C++ 6.0 (SP5)  
 MicroQuill SmartHeap Library 5.0  
 File System: NTFS  
 System State: Hyper-Threading Technology disabled in BIOS

## Notes/Tuning Information

```

PORTABILITY
  178.galgel: -FI /F32000000
FEEDBACK-DIRECTED OPTIMIZATION
  FDO: PASS1= /Qprof_gen PASS2= /Qprof_use
BASE TUNING
  C/F77/F90: /Qipo +FDO /QxW /O3
PEAK TUNING
  168.wupwise: /Qipo +FDO /QxW /Qprefetch
  171.swim: /Qipo +FDO /QxW /O3
  172.mgrid: /Qipo +FDO /QaxW /O3
  173.applu: /Qipo +FDO /QxW /O3 /Qscalar_rep- /Qauto
  177.mesa: -Qwp_ipo +FDO -QxW -Qunroll10
  178.galgel: /Qipo +FDO /QxW /O3
  179.art: -Qipo +FDO -QxW -O3 shlw32M.lib
  183.quake: -Qwp_ipo +FDO -QxW -Oa shlw32M.lib
  187.facerec: /Qipo +FDO /QxW /O3 shlw32M.lib
  188.amp: -Qipo +FDO -QaxW -Oa
  
```



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## Notes/Tuning Information (Continued)

```
189.lucas: /Qipo /QxW /Qunroll10
191.fma3d: /Qipo +FDO /QxW /O3
200.sixtrack: /Qipo +FDO /QxW
301.apsi: /Qipo /QxW /O3
EXTRA LIBRARIES
shlW32M.lib: MicroQuill SmartHeap Library 5.0
www.microquill.com
LIBRARY ORDERING
187.facerec: libIEPCF90.lib libintrins.lib libF90.lib libqwin.lib
libm.lib shlW32M.lib LIBC.lib libirc.lib OLDNAMES.lib
```