



CINT2000 Result

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Hewlett-Packard Company
HP Integrity Server rx2600 (1500 MHz, Itanium 2)

SPECint_rate2000 = 15.3
SPECint_rate_base2000 = 15.3

SPEC license #: 3 | Tested by: HP Richardson | Test date: Jul-2003 | Hardware Avail: Sep-2003 | Software Avail: Sep-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	144	11.3	1	144	11.3
175.vpr	1	125	13.0	1	125	13.0
176.gcc	1	70.8	18.0	1	70.8	18.0
181.mcf	1	78.1	26.7	1	78.1	26.7
186.crafty	1	81.9	14.2	1	81.9	14.2
197.parser	1	183	11.4	1	183	11.4
252.eon	1	84.0	17.9	1	84.0	17.9
253.perlbnk	1	149	14.0	1	149	14.0
254.gap	1	102	12.5	1	102	12.5
255.vortex	1	97.8	22.5	1	97.8	22.5
256.bzip2	1	123	14.1	1	123	14.1
300.twolf	1	234	14.8	1	234	14.8

Hardware

CPU: Intel Itanium 2
CPU MHz: 1500
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1-2
Parallel: no
Primary Cache: 16KBI + 16KBD (on chip) per CPU
Secondary Cache: 256KB (on chip) per CPU
L3 Cache: 6.0MB (on chip) per CPU
Other Cache: N/A
Memory: 12GB (12 * 1GB DIMMs)
Disk Subsystem: 1x36GB 10k RPM SCSI disk
Other Hardware: N/A

Software

Operating System: HP-UX11i-TCOE B.11.23
Compiler: HP C/ANSI C Compiler B.11.23
HP aC++ Compiler B.11.23
File System: vxfs
System State: Multi-user

Notes/Tuning Information

Portability Flags

```
176.gcc : -DHOST_WORDS_BIG_ENDIAN
181.mcf : -DWANT_STDC_PROTO
186.crafty : -DHP
252.eon : -I. -DFMAX_IS_DOUBLE -DNDEBUG
          -DSPEC_CPU2000_LP64 (because of 64-bit compile)
253.perlbnk : -DSPEC_CPU2000_HP
254.gap : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
          -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO
```

Base Flags

```
all : +Oprofile=collect:all/+Oprofile=use
C : +Ofaster -exec +Odatalayout
C++ : +Ofaster +inline_level 2 -minshared +DD64
```

Peak Flags

same as baseline (basepeak=true set globally)

Kernel Tunables:



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Notes/Tuning Information (Continued)

```
maxdsiz          0x7b03a000
maxdsiz_64bit    0x0400000000
maxssiz          0x10000000
maxssiz_64bit    0x40000000
vps_ceiling      16384
vps_pagesize     4096
swapmem_on       0
dbc_max_pct      20
dbc_min_pct      20
```

Note:

EFI shell utility 'cpuconfig' used
to deconfigure processors if necessary