



# CINT2000 Result

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## SGI

### SGI Altix 3000 (1300MHz, Itanium 2)

SPECint\_rate2000 = --

SPECint\_rate\_base2000 = 705

SPEC license #: 4 | Tested by: SGI | Test date: Dec-2003 | Hardware Avail: Jun-2003 | Software Avail: Dec-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	64	209	496			
175.vpr	64	171	609			
176.gcc	64	110	741			
181.mcf	64	182	735			
186.crafty	64	96.7	768			
197.parser	64	236	565			
252.eon	64	99.6	969			
253.perlbnk	64	185	722			
254.gap	64	159	513			
255.vortex	64	124	1138			
256.bzip2	64	187	596			
300.twolf	64	256	871			

#### Hardware

CPU: Intel Itanium 2  
CPU MHz: 1300  
FPU: Integrated  
CPU(s) enabled: 64 cores, 64 chips, 1 core/chip  
CPU(s) orderable: 4-64  
Parallel: No  
Primary Cache: 16KBI + 16KBD (on chip) per CPU  
Secondary Cache: 256KB (on chip) per CPU  
L3 Cache: 3.0MB (on chip) per CPU  
Other Cache: N/A  
Memory: 128 GB (16\*512MB PC2700 DIMMS per 4cpu module)  
Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)  
Other Hardware: None

#### Software

Operating System: SGI ProPack(TM) v2.3  
Compiler: Intel(R) C++ Compiler for Linux 8.0 (Build 20031017)  
MicroQuill SmartHeap Library 7.01 (www.microquill.com)  
File System: xfs  
System State: Single-user

### Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

#### Baseline optimization flags:

C programs: -fast -auto\_ilp32 +FDO  
C++ programs: -fast -ansi\_alias +FDO  
Extra Libraries: libsmartheap64.a

#### Portability Flags:

176.gcc: -DSPEC\_CPU2000\_LP64 -Dalloca=\_builtin\_alloca -D\_LIBC  
186.crafty: -DLINUX\_i386  
252.eon: -DSPEC\_CPU2000\_LP64 -DHAS\_ERRLIST -DFMAX\_IS\_DOUBLE  
253.perlbnk: -DSPEC\_CPU2000\_LP64 -DSPEC\_CPU2000\_NEED\_BOOL  
-DSPEC\_CPU2000\_LINUX\_IA64 -DSPEC\_CPU2000\_GLIBC22  
254.gap: -DSPEC\_CPU2000\_LP64 -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_IS\_USG  
-DSYS\_HAS\_IOCTL\_PROTO -DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_SIGNAL\_PROTO  
255.vortex: -DSPEC\_CPU2000\_LP64

Processes were bound to CPUs using dplace.