



# CINT2000 Result

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Hewlett-Packard Company  
ProLiant DL380 G3 (3.2GHz, Intel Xeon)

SPECint\_rate2000 = 17.8  
SPECint\_rate\_base2000 = 17.2

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Apr-2004 Hardware Avail: Feb-2004 Software Avail: Mar-2004

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	106	15.3	1	106	15.3
175.vpr	1	127	12.7	1	127	12.8
176.gcc	1	63.6	20.1	1	59.7	21.4
181.mcf	1	133	15.7	1	131	15.9
186.crafty	1	78.7	14.7	1	77.7	14.9
197.parser	1	132	15.8	1	132	15.8
252.eon	1	82.1	18.4	1	68.2	22.1
253.perlbnk	1	125	16.7	1	115	18.2
254.gap	1	66.4	19.2	1	66.3	19.2
255.vortex	1	77.0	28.6	1	78.7	28.0
256.bzip2	1	121	14.4	1	118	14.8
300.twolf	1	178	19.5	1	174	20.1

### Hardware

CPU: Intel Xeon (3.2GHz, 533MHz bus)  
CPU MHz: 3200  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip (HT Technology disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12K micro-ops I + 8KBD on chip  
Secondary Cache: 512KB(I+D) on chip  
L3 Cache: 2048KB(I+D) on chip  
Other Cache: N/A  
Memory: 4x512MB PC2100  
Disk Subsystem: 1x36.4GB 15K Ultra320 SCSI  
Other Hardware:

### Software

Operating System: Windows Server 2003 Standard (Build 3790)  
Compiler: Intel C++ Compiler 8.0 Build 20031211Z  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -QxN -Qipo -O3 +FDO ONESTEP=yes
Base tuning for C++ programs: -Qipo -GX -GR +FDO ONESTEP=yes
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -QxN -Qipo -Oa -O3 +FDO ONESTEP=yes
175.vpr: -QxN -Qipo -O3 +FDO ONESTEP=yes
176.gcc: -QxN -Qipo -Oi- -O3 +FDO ONESTEP=yes
181.mcf: -QxN -Qipo -O3 +FDO shlw32M.lib ONESTEP=yes
186.crafty: -QxN -Qipo -Oa -O3 +FDO shlw32M.lib ONESTEP=yes
197.parser: -QxN -Qipo -O3 +FDO ONESTEP=yes
252.eon: -QxN -Qipo -O3 +FDO ONESTEP=yes
253.perlbnk: -Qipo -Oa -O3 +FDO shlw32M.lib ONESTEP=yes
254.gap: -QxN -Qipo -O3 +FDO ONESTEP=yes
255.vortex: -QxN -Qipo -O3 +FDO shlw32M.lib ONESTEP=yes
256.bzip2: -Qunroll11 -Oa -Qipo +FDO ONESTEP=yes
```



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## Notes/Tuning Information (Continued)

300.twolf: -QxN -Qipo -O3 +FDO shlw32M.lib ONESTEP=yes  
BIOS Configuration Notes  
HT Technology disabled