



CFP2000 Result

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Hewlett-Packard Company
ProLiant ML370 G4 (3.4GHz, Intel Xeon)

SPECfp2000 = 1490
SPECfp_base2000 = 1470

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2004 Hardware Avail: Aug-2004 Software Avail: Aug-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
168.wupwise	1600	70.2	2278	70.2	2278	[Bar chart showing ratio 2278]			
171.swim	3100	138	2247	138	2247	[Bar chart showing ratio 2247]			
172.mgrid	1800	138	1307	138	1306	[Bar chart showing ratio 1306]			
173.applu	2100	150	1404	145	1450	[Bar chart showing ratio 1450]			
177.mesa	1400	93.7	1494	93.9	1491	[Bar chart showing ratio 1491]			
178.galgel	2900	128	2260	128	2260	[Bar chart showing ratio 2260]			
179.art	2600	209	1244	213	1221	[Bar chart showing ratio 1221]			
183.quake	1300	76.5	1700	64.9	2004	[Bar chart showing ratio 2004]			
187.facerec	1900	109	1743	109	1743	[Bar chart showing ratio 1743]			
188.amp	2200	224	983	224	983	[Bar chart showing ratio 983]			
189.lucas	2000	99.4	2013	98.1	2038	[Bar chart showing ratio 2038]			
191.fma3d	2100	150	1399	150	1399	[Bar chart showing ratio 1399]			
200.sixtrack	1100	180	610	180	610	[Bar chart showing ratio 610]			
301.apsi	2600	233	1118	233	1118	[Bar chart showing ratio 1118]			

Hardware

CPU: Intel Xeon (3.4GHz, 1MB L2, 800MHz bus)
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip (HT Technology disabled)
CPU(s) orderable: 1,2
Parallel: --
Primary Cache: 16K micro-ops I + 16KBD on chip
Secondary Cache: 1024KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 8x512MB DDR2-400
Disk Subsystem: 1x18.2GB 15K Ultra320 SCSI
Other Hardware:

Software

Operating System: Windows Server 2003 Standard (Build 3790)
Compiler: Intel C++ Compiler 8.0 Build 20031211Z
Intel Fortran Compiler 8.0 build 20040125Z
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for All Programs: -QxP -fast +FDO*

Portability flags:

178.galgel: -FI /F32000000

Peak tuning:

168.wupwise: -QxP -fast -Qansi_alias +FDO*
171.swim: -QxP -fast -f77rtl +FDO ONESTEP=yes
172.mgrid: -QxP -fast -Qansi_alias +FDO*
173.applu: -QxP -fast -Qscalar_rep- -Qauto -f77rtl +FDO* ONESTEP=yes
177.mesa: -QxP -fast +FDO* ONESTEP=yes
178.galgel: -QxP -fast +FDO*
179.art: -fast -Oa -Qunroll4 -Zp4 ONESTEP=yes
183.quake: -QxP -fast -Oa -Qrcd +FDO* shlw32M.lib
187.facerec: -QaxP -fast -Qunroll1 -Qscalar_rep- -fpp +FDO*
188.amp: basepeak=1



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Notes/Tuning Information (Continued)

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189.lucas:      -QaxP -fast
191.fma3d:      basepeak=1
200.sixtrack:   -QxP  -fast
301.apsi:       -QxP  -fast
                -f77rtl +FDO*
BIOS Configuration Notes
HT Technology disabled
```