



# CFP2000 Result

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Hewlett-Packard Company  
ProLiant DL360 G4 (3.6GHz, Intel Xeon)

SPECfp\_rate2000 = 22.8  
SPECfp\_rate\_base2000 = 22.3

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2004 Hardware Avail: Nov-2004 Software Avail: Aug-2004

					Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio				
45	40	35	30	25	20	15	10	5	168.wupwise	2	92.1	40.3	2	92.1	40.3
									171.swim	2	350	20.6	2	350	20.6
									172.mgrid	2	219	19.1	2	219	19.1
									173.applu	2	285	17.1	2	285	17.1
									177.mesa	2	90.6	35.9	2	90.6	35.9
									178.galgel	2	214	31.5	2	215	31.4
									179.art	2	435	13.9	2	420	14.4
									183.equake	2	141	21.4	2	107	28.2
									187.facerec	2	148	29.8	2	150	29.4
									188.amp	2	285	17.9	2	285	17.9
									189.lucas	2	212	21.9	2	211	22.0
									191.fma3d	2	218	22.3	2	218	22.3
									200.sixtrack	2	173	14.8	2	173	14.8
									301.apsi	2	277	21.8	2	277	21.8

### Hardware

CPU: Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (HT Technology disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 16K micro-ops I + 16KBD on chip  
Secondary Cache: 1024KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 2x1024MB PC2700 DDR-333  
Disk Subsystem: 1x230GB SATA  
Other Hardware:

### Software

Operating System: Windows Server 2003 Standard (Build 3790)  
Compiler: Intel C++ Compiler 8.0 Build 20031211Z  
Intel Fortran Compiler 8.0 build 20040125Z  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use  
+FDO\*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for All Programs: -QxP -fast -Qansi\_alias +FDO\*

Portability flags:

178.galgel: -FI /F32000000

Peak tuning:

168.wupwise: -QxP -fast -Qansi\_alias +FDO\*  
171.swim: -QxP -fast -f77rtl +FDO ONESTEP=yes  
172.mgrid: -QxP -fast -Qansi\_alias +FDO\*  
173.applu: -QxP -fast -Qscalar\_rep- -Qauto -f77rtl +FDO ONESTEP=yes  
177.mesa: -QxP -fast +FDO ONESTEP=yes  
178.galgel: -QxP -fast +FDO ONESTEP=yes  
179.art: -fast -Oa -Qunroll4 -Zp4 ONESTEP=yes  
183.equake: -QxP -fast -Oa -Qrcd +FDO ONESTEP=yes shlw32M.lib  
187.facerec: -QaxN -fast -Qunroll11 -Qscalar\_rep- -fpp +FDO ONESTEP=yes  
188.amp: basepeak=1



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## Notes/Tuning Information (Continued)

```
189.lucas:      -QxN-fast
191.fma3d:      basepeak=1
200.sixtrack:   -QxP -fast
301.apsi:       -QxP -fast          -f77rtl      +FDO
BIOS Configuration Notes
HT Technology disabled
Adjacent Sector Prefetch disabled
```