



CINT2000 Result

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Hewlett-Packard Company
ProLiant BL20p G3 (3.6GHz, Intel Xeon)

SPECint_rate2000 = 33.7
SPECint_rate_base2000 = 32.8

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Nov-2004 Hardware Avail: Aug-2004 Software Avail: Aug-2004

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip | 2 | 118 | 27.5 | 2 | 119 | 27.4 |
| 175.vpr | 2 | 165 | 19.7 | 2 | 153 | 21.2 |
| 176.gcc | 2 | 61.5 | 41.5 | 2 | 61.5 | 41.5 |
| 181.mcf | 2 | 218 | 19.1 | 2 | 217 | 19.3 |
| 186.crafty | 2 | 74.9 | 31.0 | 2 | 74.9 | 31.0 |
| 197.parser | 2 | 137 | 30.6 | 2 | 136 | 30.7 |
| 252.eon | 2 | 73.5 | 41.0 | 2 | 60.5 | 49.9 |
| 253.perlbnk | 2 | 97.7 | 42.8 | 2 | 96.3 | 43.4 |
| 254.gap | 2 | 65.5 | 39.0 | 2 | 64.3 | 39.7 |
| 255.vortex | 2 | 73.1 | 60.3 | 2 | 73.2 | 60.2 |
| 256.bzip2 | 2 | 151 | 23.0 | 2 | 151 | 23.0 |
| 300.twolf | 2 | 172 | 40.4 | 2 | 172 | 40.4 |

Hardware

CPU: Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (HT Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12K micro-ops I + 16KBD on chip
Secondary Cache: 1024KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4x2048MB PC3200 CL3.0
Disk Subsystem: 1x72.8GB 15K U320
Other Hardware:

Software

Operating System: Windows Server 2003 Standard (Build 3790)
Compiler: Intel C++ Compiler 8.1 Build 20040802Z
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -QxP -fast +FDO shlw32M.lib
Base tuning for C++ programs: -QxP -fast -GX -GR
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -QxP -fast -Oa +FDO ONESTEP=yes
175.vpr: -QxP -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -QxP -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -QxP -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -QxP -fast +FDO ONESTEP=yes
252.eon: -QxP -O2 -Qipo +FDO ONESTEP=yes
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -QxP -fast +FDO ONESTEP=yes
255.vortex: -QxP -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: -fast -Oa -Qunroll1 +FDO ONESTEP=yes
```



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Notes/Tuning Information (Continued)

300.twolf: -QxP -fast +FDO shlw32M.lib
BIOS Configuration Notes
HT Technology disabled