



CFP2000 Result

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Acer Incorporated

Acer Altos R710 (3.8 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECfp_rate2000 = 32.8

SPECfp_rate_base2000 = 32.6

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Nov-2005 | Hardware Avail: Oct-2005 | Software Avail: Apr-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	70.7	52.5	2	70.5	52.7
171.swim	2	285	25.3	2	284	25.3
172.mgrid	2	179	23.4	2	178	23.4
173.applu	2	217	22.4	2	217	22.4
177.mesa	2	84.9	38.3	2	84.9	38.3
178.galgel	2	88.3	76.2	2	88.3	76.2
179.art	2	81.9	73.7	2	81.9	73.7
183.quake	2	84.8	35.5	2	84.8	35.6
187.facerec	2	115	38.3	2	115	38.3
188.amp	2	197	25.9	2	182	28.1
189.lucas	2	191	24.4	2	190	24.4
191.fma3d	2	187	26.0	2	187	26.0
200.sixtrack	2	146	17.5	2	146	17.5
301.apsi	2	227	26.5	2	227	26.6

Hardware

CPU: Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)
 CPU MHz: 3800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology Disable)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
 Secondary Cache: 2048KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 6 x 512MB ECC DDR2-400 SDRAM
 Disk Subsystem: 1 x 18GB 10000RPM U320 SCSI HDD
 Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
 Compiler: Intel C++ 9.0 Build 20050430Z for IA32 and Fortran Compiler 9.0 Build 20050722Z for IA32
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
 shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for Fortran Programs : -fast -Qansi_alias +FDO
Base for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
192.fma3d: Approved windowsdp src.alt used
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
```



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Notes/Tuning Information (Continued)

189.lucas: -fast -Qprefetch- +FDO

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>