



CFP2000 Result

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Acer Incorporated

Acer Altos R510Mk2 (3.6 GHz Intel Xeon processor, 2MB L2)

SPECfp2000 = 1846

SPECfp_base2000 = 1843

SPEC license #: 97 Tested by: Acer Incorporated Test date: Jan-2006 Hardware Avail: Jan-2006 Software Avail: Nov-2005

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio | |
|--------------|----------------|--------------|------------|---------|-------|--|
| 168.wupwise | 1600 | 61.1 | 2618 | 61.1 | 2618 | |
| 171.swim | 3100 | 139 | 2234 | 139 | 2235 | |
| 172.mgrid | 1800 | 120 | 1506 | 120 | 1506 | |
| 173.applu | 2100 | 133 | 1574 | 133 | 1574 | |
| 177.mesa | 1400 | 89.4 | 1566 | 89.4 | 1566 | |
| 178.galgel | 2900 | 80.1 | 3621 | 80.1 | 3621 | |
| 179.art | 2600 | 61.0 | 4261 | 61.0 | 4261 | |
| 183.quake | 1300 | 48.8 | 2665 | 48.8 | 2662 | |
| 187.facerec | 1900 | 99.4 | 1912 | 99.4 | 1912 | |
| 188.amp | 2200 | 190 | 1159 | 186 | 1181 | |
| 189.lucas | 2000 | 102 | 1959 | 102 | 1960 | |
| 191.fma3d | 2100 | 143 | 1473 | 143 | 1473 | |
| 200.sixtrack | 1100 | 153 | 718 | 153 | 717 | |
| 301.apsi | 2600 | 204 | 1277 | 204 | 1276 | |

Hardware

CPU: Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 6 x 512MB ECC DDR2-400 SDRAM
Disk Subsystem: 1 x 160GB 7200RPM SATA HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ 9.0 Build 20051130Z for IA32 and Fortran Compiler 9.0 Build 20051130Z for IA32
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
shlw32M.lib is Microquill SmartHeap Library Version 7.4
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
```



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Notes/Tuning Information (Continued)

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>