



CFP2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Acer Incorporated
Acer Altos R720 (3.73 GHz Intel Xeon Processor 5080)

SPECfp_rate2000 = 63.4
SPECfp_rate_base2000 = 63.4

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2006 | Hardware Avail: Jun-2006 | Software Avail: Dec-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	79.7	93.2	4	79.7	93.2
171.swim	4	293	49.1	4	293	49.1
172.mgrid	4	188	44.5	4	188	44.5
173.applu	4	206	47.2	4	206	47.2
177.mesa	4	73.3	88.6	4	73.3	88.6
178.galgel	4	96.7	139	4	96.7	139
179.art	4	70.2	172	4	70.2	172
183.equake	4	135	44.6	4	135	44.6
187.facerec	4	130	67.9	4	130	67.9
188.amp	4	171	59.5	4	171	59.5
189.lucas	4	199	46.7	4	199	46.7
191.fma3d	4	190	51.3	4	190	51.3
200.sixtrack	4	148	34.5	4	148	34.5
301.apsi	4	215	56.2	4	215	56.2

Hardware

CPU: Intel Xeon Processor 5080 (3.73 GHz, 2x2MB L2, 1066MHz system bus)
CPU MHz: 3733
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache: 2048KB(I+D) on chip, per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8 x 2048MB ECC FB-DIMM 667
Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
Other Hardware:

Software

Operating System: Red Hat Enterprise Linux 4 Advanced Server Update 3 EM64T (Kernel 2.6.9-34.EL.smp)
Compiler: Intel C++ Compiler 9.0 Build 20051201 for EM64T
Intel Fortran Compiler 9.0 Build 20051201 for EM64T
File System: ext3
System State: Runlevel 3

Notes/Tuning Information

GENERAL

ONESTEP=yes for all benchmarks
+FDO : PASS1= -prof_gen PASS2= -prof_use

PORTABILITY FLAGS

-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

BASE TUNING

Baseline optimizations for C and Fortran: -fast +FDO

PEAK TUNING

basepeak=yes set for all benchmarks
Enable hardware prefetching and adjacent cache line prefetch BIOS setting