



CFP2000 Result

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Hewlett-Packard Company

ProLiant ML350 G5 (3.73GHz, Intel Xeon processor 5080)

SPECfp_rate2000 = 60.1

SPECfp_rate_base2000 = 56.8

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	80.3	92.4	4	80.3	92.4
171.swim	4	381	37.8	4	378	38.0
172.mgrid	4	238	35.0	4	234	35.6
173.applu	4	295	33.0	4	199	49.1
177.mesa	4	74.6	87.0	4	69.8	93.0
178.galgel	4	105	128	4	105	128
179.art	4	69.9	173	4	69.9	173
183.quake	4	151	40.0	4	118	51.3
187.facerec	4	137	64.5	4	128	69.1
188.amp	4	170	60.1	4	170	60.1
189.lucas	4	256	36.2	4	256	36.2
191.fma3d	4	212	45.9	4	212	45.9
200.sixtrack	4	151	33.9	4	151	33.9
301.apsi	4	228	52.9	4	230	52.5

Hardware

CPU: Intel Xeon processor 5080 (3.73GHz, 2x2MB L2, 1066MHz bus)
CPU MHz: 3730
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 12KB micro-ops I + 16KBD (on chip) per core
Secondary Cache: 2048KB(I+D) (on chip) per core
L3 Cache: N/A
Other Cache: N/A
Memory: 4x2048MB PC2-5300F
Disk Subsystem: 1x36GB 10K SAS
Other Hardware:

Software

Operating System: RedHat Enterprise Linux 4.0 Advanced Server for AMD/EM64T, Update 3 Kernel 2.6.9-34.ELsmp
Compiler: Intel C++ Compiler for EM64T-based applications, (Version 9.1 Build 20060323)
Intel Fortran Compiler for EM64T-based applications, (Version 9.1 Build 20060323)
PathScale EKOPATH(TM) Compiler Suite, Release 2.4
File System: ext2
System State: Multi-user run level 3

Notes/Tuning Information

```
+FDO: PASS1= -prof_gen PASS2=-prof_use (Intel Compiler)
+FDO: PASS1= -fb_create fbdata PASS2=-fb_opt fbdata (PathScale Compiler)
ifort is the Intel Fortran compiler, icc is the Intel C++ compiler; and
pathf95 is PathScale Fortran compiler, pathcc is the PathScale C compiler.
Base tuning for C programs: icc -fast -auto_ilp32 +FDO
Base tuning for FORTRAN programs: ifort -fast +FDO
Portability:
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI
Peak tuning:
168.wupwise: basepeak=1
171.swim: pathf95 -Ofast -LNO:fusion=2:simd=0 -WOPT:val=0 -march=em64t
172.mgrid: pathf95 -Ofast -CG:load_exe=0 -LNO:blocking=off:prefetch Ahead=5
-OPT:ro=3:unroll_size=256 -WOPT:mem_opnds=on -march=em64t
173.applu: pathf95 -O3 -ipa -CG:load_exe=0
-LNO:fission=1:fusion=2:blocking=off:full_unroll_size=9000
-OPT:IEEE_a=3:ro=3 -TENV:X=3 -march=em64t
```



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Notes/Tuning Information (Continued)

```

177.mesa: pathcc -O2 -ipa -OPT:Ofast -fno-math-errno -CG:local_fwd_sched=on
          -GRA:optimize_boundary=on -march=em64t +FDO
178.galgel: basepeak=1
179.art: basepeak=1
183.equake: icc -fast +FDO ONESTEP=yes -rcd -auto-ilp32
187.facerec: pathf95 -Ofast -IPA:plimit=1500 -LNO:fusion=2
            -OPT:IEEE_NaN_Inf=off:ro=3:unroll_size=0 -march=em64t +FDO
188.ammmp: basepeak=1
189.lucas: ifort -fast ONESTEP=yes
191.fma3d: basepeak=1

```

Power Regulator set to Static High Performance Mode
Hyper-Threading Technology disabled
BIOS Configuration Notes