



CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX201 (Intel Xeon 5030 processor, 2.66GHz)

SPECint_rate2000 = 30.6

SPECint_rate_base2000 = 30.6

SPEC license #: 9001 | Tested by: Itautec | Test date: Nov-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.zip | 4 | 243 | 26.7 | 4 | 243 | 26.7 |
| 175.vpr | 4 | 333 | 19.5 | 4 | 333 | 19.5 |
| 176.gcc | 4 | 121 | 42.1 | 4 | 121 | 42.1 |
| 181.mcf | 4 | 468 | 17.8 | 4 | 468 | 17.8 |
| 186.crafty | 4 | 203 | 22.9 | 4 | 203 | 22.9 |
| 197.parser | 4 | 275 | 30.4 | 4 | 275 | 30.4 |
| 252.eon | 4 | 135 | 44.6 | 4 | 135 | 44.6 |
| 253.perlbnk | 4 | 253 | 33.0 | 4 | 253 | 33.0 |
| 254.gap | 4 | 151 | 33.9 | 4 | 151 | 33.9 |
| 255.vortex | 4 | 174 | 50.8 | 4 | 174 | 50.8 |
| 256.bzip2 | 4 | 273 | 25.5 | 4 | 273 | 25.5 |
| 300.twolf | 4 | 364 | 38.2 | 4 | 364 | 38.2 |

Hardware

CPU: Intel Xeon 5030 processor (2.66GHz, 2x2MB L2, 667MHz system bus)
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable: 1
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 2MB (I+D) on chip, per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8GB (8x1GB DDR2-RAM PC2-4200 FB-DIMM CL4 ECC)
Disk Subsystem: 1 x 120GB Serial ATA 7200 RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler 9.1
Build 20060519Z (for 32-bit applications),
Microsoft Visual Studio .NET 7.0.9466 (for libraries),
MicroQuill SmartHeap for SMP Library 8.0
File System: NTFS
System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning flags

for C programs: -fast +FDO sh1SMPMt.lib
for C++ programs: -fast -Qcxx_features +FDO

EXTRA LIBRARIES

MicroQuill SmartHeap for SMP Library 8.0 available from
<http://www.microquill.com/>