



CINT2000 Result

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IBM Corporation
IBM System X 3950 (3.0 GHz Xeon 7120N)

SPECint2000 = --
SPECint_base2000 = 1462

SPEC license #: 11 | Tested by: IBM Corporation | Test date: Dec-2006 | Hardware Avail: Oct-2006 | Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	137	1020			
175.vpr	1400	142	986			
176.gcc	1100	65.0	1693			
181.mcf	1800	108	1666			
186.crafty	1000	88.3	1133			
197.parser	1800	152	1187			
252.eon	1300	63.6	2044			
253.perlbmk	1800	100	1796			
254.gap	1100	70.8	1553			
255.vortex	1900	76.4	2487			
256.bzip2	1500	129	1160			
300.twolf	3000	197	1526			

Hardware

CPU: Intel Xeon processor 7120N (3.0 GHz, 667 MHz bus)
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology enabled)
 CPU(s) orderable: 1, 2, 4 chips
 Parallel: No
 Primary Cache: 12 K I micro-ops + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 16 MB on chip per chip
 Other Cache: N/A
 Memory: 16 x 2048 MB ECC PC2-3200
 Disk Subsystem: 73GB SAS 10K RPM
 Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler 9.1 for 32-bit applications
 Build 20060323Z Package ID: W_CC_P_9.1.020
 Microsoft Visual Studio 2005 (for libraries)
 SmartHeap Library Version 8.0 from <http://www.microquill.com/>
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M-80.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M-80.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon: -DHAS_ERRLIST
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
```

Bios Settings

Hardware Prefetch enabled
 Memory Array set to High-Performance Memory Array