



# CINT2000 Result

Copyright ©1999-2007, Standard Performance Evaluation Corporation

**Hewlett-Packard Company**  
ProLiant ML350 G5 (1.86 GHz, Intel Xeon processor E5320)

SPECint2000 = **1972**  
SPECint\_base2000 = **1966**

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Feb-2007 Hardware Avail: Nov-2006 Software Avail: Nov-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	121	1156	121	1155	
175.vpr	1400	97.0	1444	95.0	1474	
176.gcc	1100	49.5	2220	49.5	2220	
181.mcf	1800	56.2	3205	55.9	3220	
186.crafty	1000	58.7	1703	58.8	1701	
197.parser	1800	116	1550	116	1553	
252.eon	1300	53.6	2426	53.4	2435	
253.perlbmk	1800	79.7	2259	79.4	2267	
254.gap	1100	58.9	1868	58.9	1868	
255.vortex	1900	57.5	3303	57.5	3303	
256.bzip2	1500	104	1446	104	1446	
300.twolf	3000	138	2182	138	2182	

### Hardware

CPU: Intel Xeon processor E5320 (1.86 GHz, 2x4 MB L2 shared, 1066 MHz bus)  
CPU MHz: 1860  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 8 MB I+D on chip per chip (4 MB shared per 2 cores)  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8 GB (4x2 GB PC2-5300F CL5)  
Disk Subsystem: 4x36 GB 10 K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise X64 Edition (x86\_64) SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20061103Z)  
Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)  
MicroQuill Smartheap Library 8.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx\_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbmk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbmk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



# CINT2000 Result

Copyright ©1999-2007, Standard Performance Evaluation Corporation

**Hewlett-Packard Company**  
ProLiant ML350 G5 (1.86 GHz, Intel Xeon processor E5320)

SPECint2000 = 1972  
SPECint\_base2000 = 1966

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Feb-2007 | Hardware Avail: Nov-2006 | Software Avail: Nov-2006

## Notes/Tuning Information (Continued)

```
256.bzip2:    basepeak=1
300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch disabled
Other Configuration Notes
  The start /b /wait /affinity command is used to bind CPU(s) to processes.
```