



CINT2000 Result

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Hewlett-Packard Company
ProLiant BL460c (2.66GHz, Intel Xeon processor 5150)

SPECint_rate2000 = 54.1
SPECint_rate_base2000 = 54.1

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Oct-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

90 80 70 60 50 40 30 20 10										Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
										164.gzip	2	90.2	36.0	2	90.2	36.0
										175.vpr	2	84.7	38.4	2	84.7	38.4
										176.gcc	2	46.3	55.1	2	46.3	55.1
										181.mcf	2	86.8	48.1	2	86.8	48.1
										186.crafty	2	32.4	71.5	2	32.4	71.5
										197.parser	2	107	38.9	2	107	38.9
										252.eon	2	36.3	83.0	2	36.3	83.0
										253.perlbnk	2	61.7	67.7	2	61.7	67.7
										254.gap	2	53.5	47.7	2	53.5	47.7
										255.vortex	2	52.8	83.5	2	52.8	83.5
										256.bzip2	2	80.9	43.0	2	80.9	43.0
										300.twolf	2	108	64.7	2	108	64.7

Hardware

CPU: Intel Xeon processor 5150 (2.66 GHz, 4 MB L2 shared, 1333 MHz bus)
CPU MHz: 2660
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (8x1 GB PC2-5300F)
Disk Subsystem: 2x72 GB 10 K SAS
Other Hardware:

Software

Operating System: RedHat Enterprise Linux 4.0 Advanced Server for AMD64/EM64T, Update 3 Kernel 2.6.9-34.ELsmp
Compiler: Intel C++ Compiler for EM64T-based applications, (Version 9.1 Build 20060323)
Intel Fortran Compiler for EM64T-based applications, (Version 9.1 Build 20060323)
File System: ext2
System State: Multi-user run level 3

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen PASS2=-prof_use
Base tuning for C programs: -fast -auto_ilp32 +FDO ONESTEP=yes
Base tuning for C++ programs: -fast
Portability flags:
-DSPEC_CPU2000_LP64 applied to all benchmarks
186.crafty: -DLINUX_i386
252.eon: -DHAS_ERRLIST
253.perlbnk: -DSPEC_CPU2000_LINUX_I386 -DSPEC_CPU2000_NEED_BOOL
254.gap: -DSYS_IS_USG
Peak tuning:
basepeak=1 for all modules
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch disabled
```