SPEC® CFP2006 Result
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Itautec

$SPECfp^{®}_rate2006 = $ 51.4

Servidor Itautec MX221 (Intel Xeon E5345)

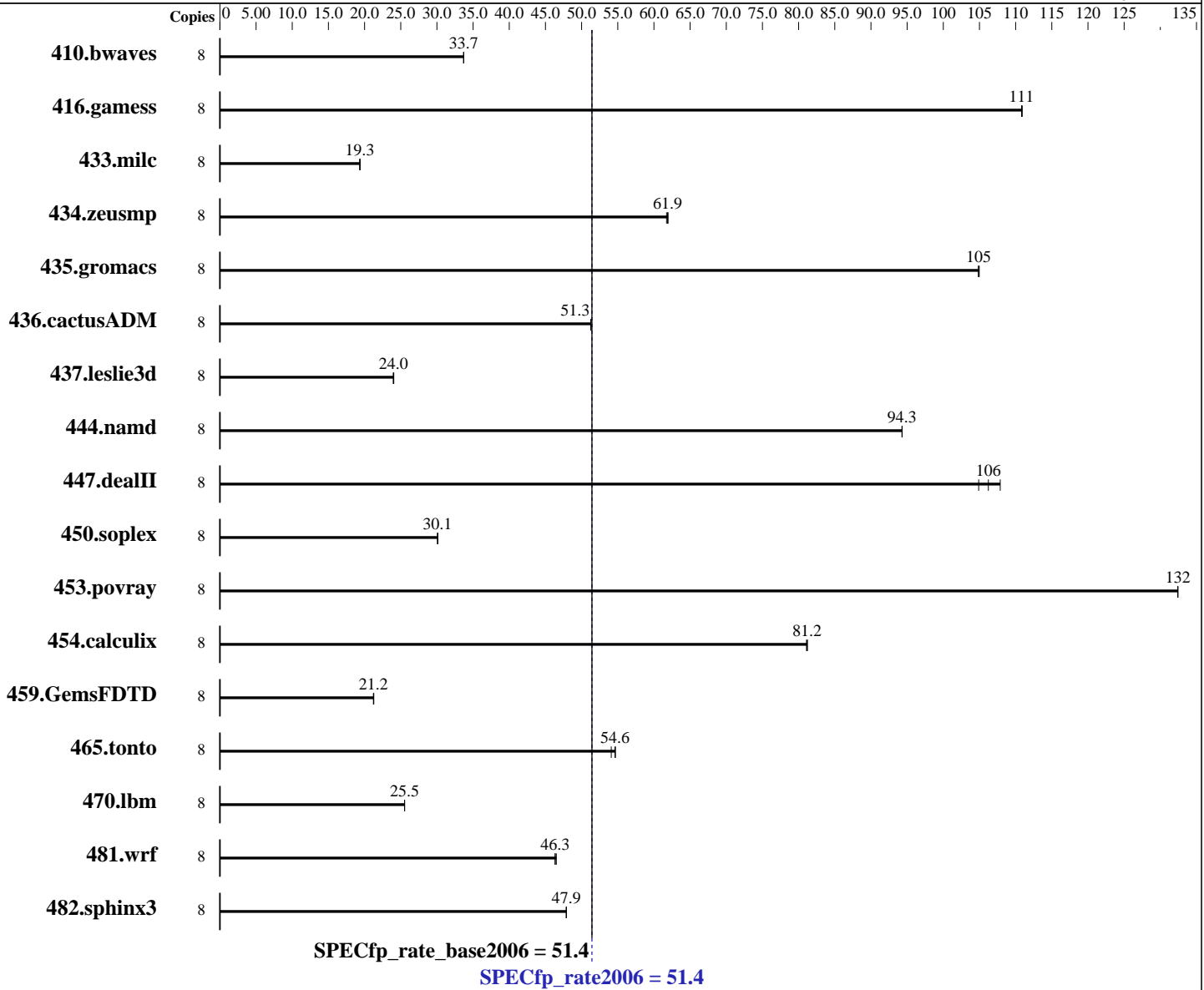$SPECfp_rate_base2006 = $ 51.4

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006

Copies		Value
410.bwaves	8	33.7
416.gamess	8	111
433.milc	8	19.3
434.zeusmp	8	61.9
435.gromacs	8	105
436.cactusADM	8	51.3
437.leslie3d	8	24.0
444.namd	8	94.3
447.dealII	8	106
450.soplex	8	30.1
453.povray	8	132
454.calculix	8	81.2
459.GemsFDTD	8	21.2
465.tonto	8	54.6
470.lbm	8	25.5
481.wrf	8	46.3
482.sphinx3	8	47.9

SPECfp_rate_base2006 = 51.4
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Hardware
CPU Name: Intel Xeon E5345
CPU Characteristics: 1333MHz system bus
CPU MHz: 2330
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1-2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software
Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.025 Build no 20060519Z
Intel Fortran Compiler for IA32 version 9.1
Package ID W_FC_C_9.1.025 Build no 20060519Z
Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel: No
File System: NTFS

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L3 Cache:	None	System State:	Default
Other Cache:	None	Base Pointers:	32-bit
Memory:	16 GB (8x2GB DDR2-RAM PC2-5300F CAS 5-5-5)	Peak Pointers:	32-bit
Disk Subsystem:	73 GB SCSI, 10000RPM	Other Software:	Microquill SmartHeap Library v.8.0 for SMP
Other Hardware:	None		

Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
410.bwaves	8	**_3226_**	**_33.7_**	3227	33.7	3226	33.7	8	**_3226_**	**_33.7_**	3227	33.7	3226	33.7
416.gamess	8	1413	111	1412	111	**_1413_**	**_111_**	8	1413	111	1412	111	**_1413_**	**_111_**
433.milc	8	3790	19.4	3800	19.3	**_3795_**	**_19.3_**	8	3790	19.4	3800	19.3	**_3795_**	**_19.3_**
434.zeusmp	8	1175	62.0	**_1176_**	**_61.9_**	1179	61.7	8	1175	62.0	**_1176_**	**_61.9_**	1179	61.7
435.gromacs	8	**_545_**	**_105_**	544	105	545	105	8	**_545_**	**_105_**	544	105	545	105
436.cactusADM	8	1864	51.3	**_1864_**	**_51.3_**	1864	51.3	8	1864	51.3	**_1864_**	**_51.3_**	1864	51.3
437.leslie3d	8	3138	24.0	**_3133_**	**_24.0_**	3128	24.0	8	3138	24.0	**_3133_**	**_24.0_**	3128	24.0
444.namd	8	**_680_**	**_94.3_**	680	94.3	680	94.3	8	**_680_**	**_94.3_**	680	94.3	680	94.3
447.dealII	8	**_862_**	**_106_**	848	108	873	105	8	**_862_**	**_106_**	848	108	873	105
450.soplex	8	**_2217_**	**_30.1_**	2214	30.1	2218	30.1	8	**_2217_**	**_30.1_**	2214	30.1	2218	30.1
453.povray	8	321	132	**_321_**	**_132_**	321	132	8	321	132	**_321_**	**_132_**	321	132
454.calculix	8	813	81.2	**_813_**	**_81.2_**	814	81.1	8	813	81.2	**_813_**	**_81.2_**	814	81.1
459.GemsFDTD	8	3989	21.3	4000	21.2	**_3995_**	**_21.2_**	8	3989	21.3	4000	21.2	**_3995_**	**_21.2_**
465.tonto	8	1440	54.7	**_1441_**	**_54.6_**	1455	54.1	8	1440	54.7	**_1441_**	**_54.6_**	1455	54.1
470.lbm	8	4305	25.5	**_4305_**	**_25.5_**	4306	25.5	8	4305	25.5	**_4305_**	**_25.5_**	4306	25.5
481.wrf	8	**_1928_**	**_46.3_**	1928	46.3	1921	46.5	8	**_1928_**	**_46.3_**	1928	46.3	1921	46.5
482.sphinx3	8	3256	47.9	3255	47.9	**_3256_**	**_47.9_**	8	3256	47.9	3255	47.9	**_3256_**	**_47.9_**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

    The Servidor Itautec MX221 and the Servidor Itautec MX201
    are electronically equivalent.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
     444.namd: -TP
    447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
               -DBOOST_NO_INTRINSIC_WCHAR_T
     453.povray: -DSPEC_CPU_WINDOWS_ICL
    454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
        481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:
```
-fast /F950000000 shlSMPMt.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F950000000 shlSMPMt.lib
          -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
-fast /F950000000          -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
-fast /F950000000          -link /FORCE:MULTIPLE
```

Peak Optimization Flags

C benchmarks:

> 433.milc: `basepeak = yes`

> 470.lbm: `basepeak = yes`

> 482.sphinx3: `basepeak = yes`

C++ benchmarks:

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Software Availability: May-2006

Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:32:06 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 7 March 2007.