



SPEC[®] CFP2006 Result

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Itautec

SPECfp[®]_rate2006 = 38.2

Servidor Itautec LX211 (Intel Xeon E5320)

SPECfp_rate_base2006 = 38.2

CPU2006 license: 9001

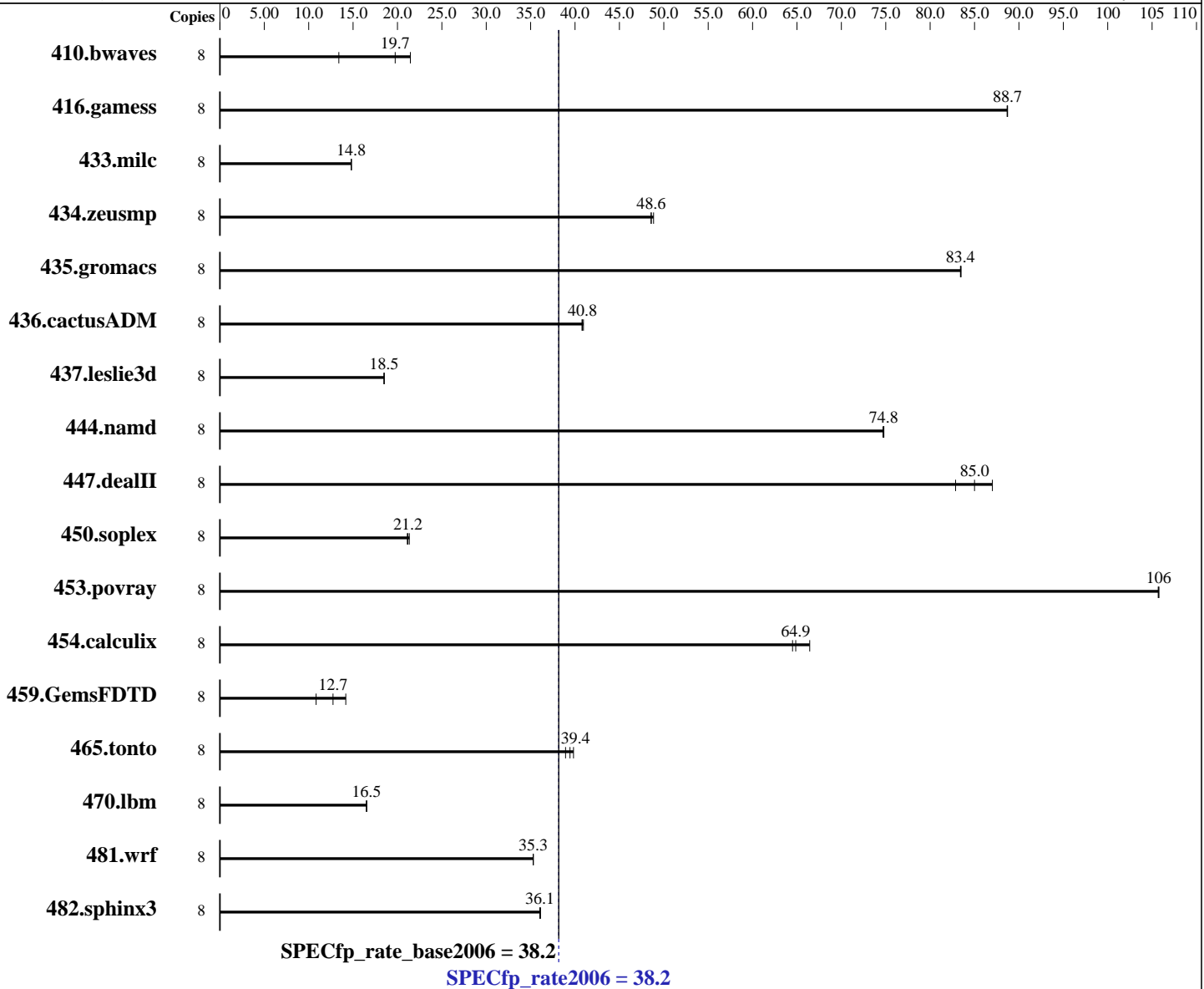
Test sponsor: Itautec

Tested by: Itautec

Test date: Feb-2007

Hardware Availability: Feb-2007

Software Availability: May-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.025 Build no 20060519Z
 Intel Fortran Compiler for IA32 version 9.1
 Package ID W_FC_C_9.1.025 Build no 20060519Z
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS

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L3 Cache: None
Other Cache: None
Memory: 6 GB (6x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 120 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	8115	13.4	5068	21.5	5507	19.7	8	8115	13.4	5068	21.5	5507	19.7
416.gamess	8	1767	88.7	1766	88.7	1766	88.7	8	1767	88.7	1766	88.7	1766	88.7
433.milc	8	4962	14.8	4961	14.8	4963	14.8	8	4962	14.8	4961	14.8	4963	14.8
434.zeusmp	8	1490	48.8	1499	48.6	1498	48.6	8	1490	48.8	1499	48.6	1498	48.6
435.gromacs	8	684	83.5	685	83.4	685	83.4	8	684	83.5	685	83.4	685	83.4
436.cactusADM	8	2344	40.8	2341	40.8	2335	40.9	8	2344	40.8	2341	40.8	2335	40.9
437.leslie3d	8	4066	18.5	4068	18.5	4064	18.5	8	4066	18.5	4068	18.5	4064	18.5
444.namd	8	859	74.7	858	74.8	858	74.8	8	859	74.7	858	74.8	858	74.8
447.dealII	8	1052	87.0	1104	82.9	1077	85.0	8	1052	87.0	1104	82.9	1077	85.0
450.soplex	8	3150	21.2	3127	21.3	3161	21.1	8	3150	21.2	3127	21.3	3161	21.1
453.povray	8	402	106	403	106	402	106	8	402	106	403	106	402	106
454.calculix	8	1018	64.9	1023	64.5	993	66.4	8	1018	64.9	1023	64.5	993	66.4
459.GemsFDTD	8	6662	12.7	5986	14.2	7835	10.8	8	6662	12.7	5986	14.2	7835	10.8
465.tonto	8	1996	39.4	2022	38.9	1977	39.8	8	1996	39.4	2022	38.9	1977	39.8
470.lbm	8	6656	16.5	6656	16.5	6651	16.5	8	6656	16.5	6656	16.5	6651	16.5
481.wrf	8	2532	35.3	2532	35.3	2533	35.3	8	2532	35.3	2532	35.3	2533	35.3
482.sphinx3	8	4326	36.0	4324	36.1	4318	36.1	8	4326	36.0	4324	36.1	4318	36.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc7.1 -Qc99 ifort



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Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlSMPMt.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: basepeak = yes
447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: basepeak = yes

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-ic91-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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