



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp®_rate2006 = 26.0

ProLiant DL380 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 25.6

CPU2006 license: 3

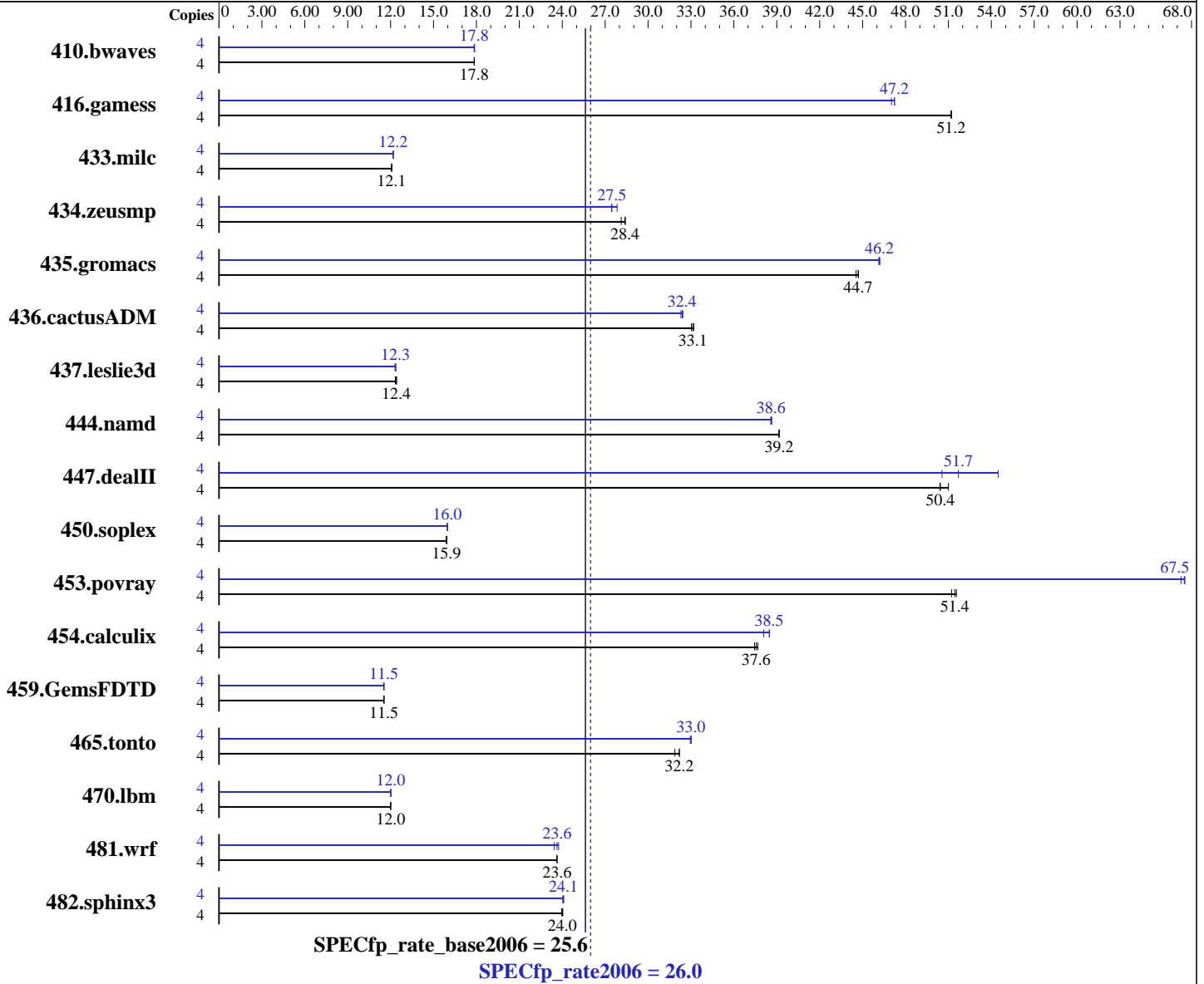
Test date: Mar-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64)
 kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_cc_c_9.1.045
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_fc_c_9.1.040
 Auto Parallel: No

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 26.0

ProLiant DL380 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 25.6

CPU2006 license: 3

Test date: Mar-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 4x36 GB 10 K SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	3046	17.8	3046	17.8	3045	17.9	4	3046	17.8	3041	17.9	3046	17.8		
416.gamess	4	1530	51.2	1529	51.2	1530	51.2	4	1666	47.0	1658	47.2	1658	47.2		
433.milc	4	3042	12.1	3040	12.1	3040	12.1	4	3013	12.2	3012	12.2	3016	12.2		
434.zeusmp	4	1281	28.4	1282	28.4	1294	28.1	4	1326	27.4	1325	27.5	1308	27.8		
435.gromacs	4	639	44.7	641	44.5	639	44.7	4	618	46.2	619	46.2	619	46.1		
436.cactusADM	4	1440	33.2	1443	33.1	1447	33.0	4	1476	32.4	1480	32.3	1474	32.4		
437.leslie3d	4	3030	12.4	3029	12.4	3049	12.3	4	3054	12.3	3040	12.4	3049	12.3		
444.namd	4	819	39.2	819	39.2	819	39.2	4	830	38.7	831	38.6	831	38.6		
447.dealII	4	897	51.0	907	50.4	908	50.4	4	885	51.7	905	50.6	840	54.5		
450.soplex	4	2098	15.9	2101	15.9	2093	15.9	4	2087	16.0	2090	16.0	2090	16.0		
453.povray	4	413	51.6	415	51.2	414	51.4	4	316	67.3	315	67.5	315	67.5		
454.calculix	4	876	37.7	881	37.4	879	37.6	4	857	38.5	866	38.1	858	38.5		
459.GemsFDTD	4	3679	11.5	3682	11.5	3678	11.5	4	3685	11.5	3679	11.5	3677	11.5		
465.tonto	4	1223	32.2	1223	32.2	1235	31.9	4	1192	33.0	1195	33.0	1192	33.0		
470.lbm	4	4579	12.0	4579	12.0	4578	12.0	4	4577	12.0	4576	12.0	4576	12.0		
481.wrf	4	1892	23.6	1891	23.6	1890	23.6	4	1890	23.6	1906	23.4	1881	23.7		
482.sphinx3	4	3243	24.0	3247	24.0	3253	24.0	4	3236	24.1	3244	24.0	3235	24.1		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
"ulimit -s unlimited" set

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 26.0

ProLiant DL380 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 25.6

CPU2006 license: 3

Test date: Mar-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 26.0

ProLiant DL380 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 25.6

CPU2006 license: 3

Test date: Mar-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:45:50 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 March 2007.