SPEC® CINT2006 Result
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Acer Incorporated

Acer Altos G540 (Intel Xeon 5120)

SPECint®_rate2006 =   25.5

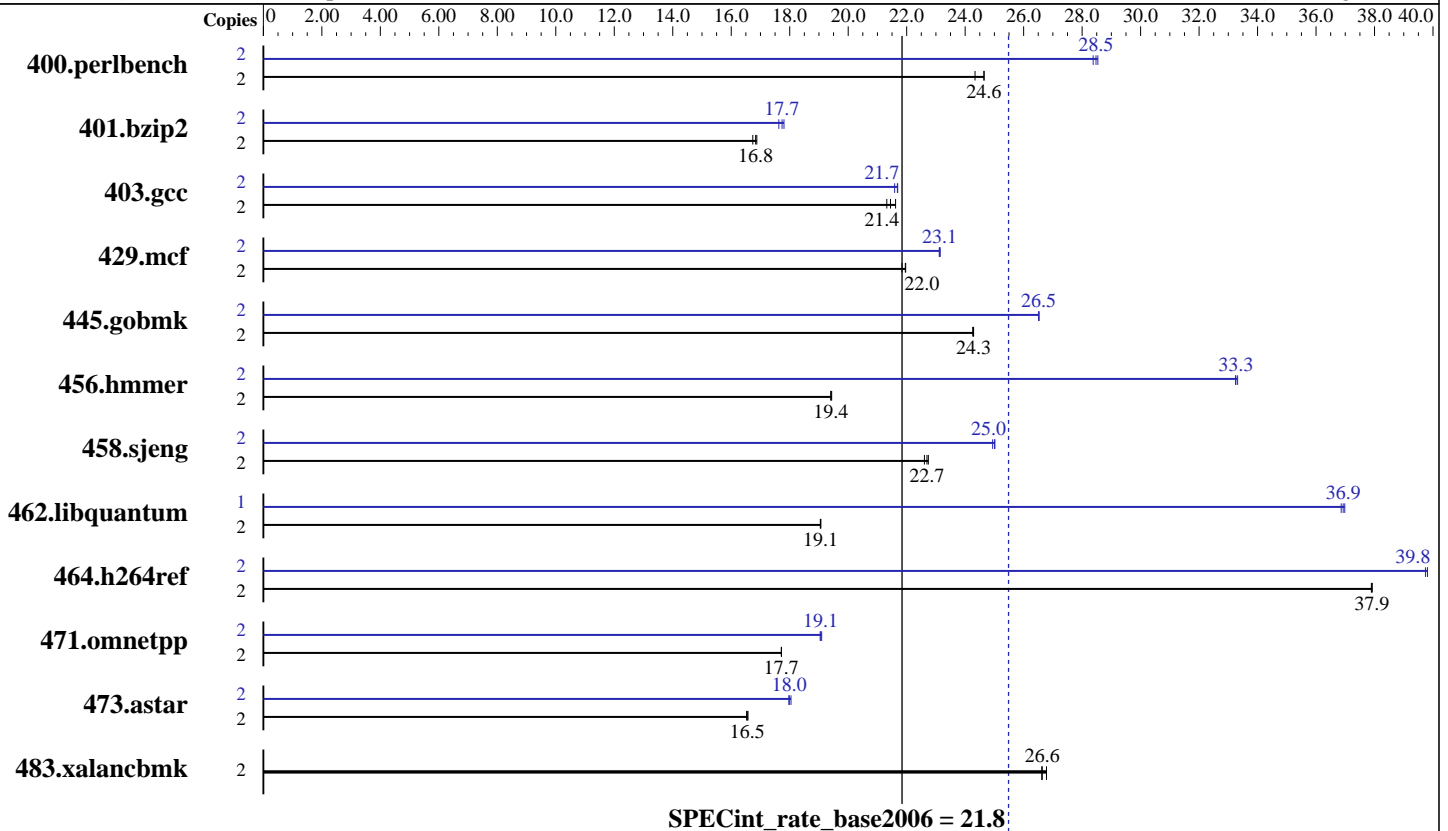SPECint_rate_base2006 = 21.8

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Sep-2007
Hardware Availability: Nov-2006
Software Availability: Aug-2007

Benchmark	Copies	Value
400.perlbench	2	28.5
400.perlbench	2	24.6
401.bzip2	2	17.7
401.bzip2	2	16.8
403.gcc	2	21.7
403.gcc	2	21.4
429.mcf	2	23.1
429.mcf	2	22.0
445.gobmk	2	26.5
445.gobmk	2	24.3
456.hmmer	2	33.3
456.hmmer	2	19.4
458.sjeng	2	25.0
458.sjeng	2	22.7
462.libquantum	1	36.9
462.libquantum	2	19.1
464.h264ref	2	39.8
464.h264ref	2	37.9
471.omnetpp	2	19.1
471.omnetpp	2	17.7
473.astar	2	18.0
473.astar	2	16.5
483.xalancbmk	2	26.6

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Hardware

CPU Name: Intel Xeon 5120
CPU Characteristics: 1066MHz system bus
CPU MHz: 1860
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73 GB 10000RPM SAS HDD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1
Build 20070725
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	803	24.3	793	24.6	**793**	**24.6**	2	**686**	**28.5**	689	28.4	685	28.5
401.bzip2	2	1144	16.9	**1146**	**16.8**	1153	16.7	2	1084	17.8	**1088**	**17.7**	1095	17.6
403.gcc	2	745	21.6	755	21.3	**751**	**21.4**	2	746	21.6	**743**	**21.7**	742	21.7
429.mcf	2	**831**	**22.0**	830	22.0	835	21.8	2	**789**	**23.1**	789	23.1	788	23.1
445.gobmk	2	864	24.3	**864**	**24.3**	865	24.3	2	**791**	**26.5**	791	26.5	791	26.5
456.hmmer	2	962	19.4	**961**	**19.4**	960	19.4	2	561	33.2	**561**	**33.3**	560	33.3
458.sjeng	2	1070	22.6	1064	22.7	**1066**	**22.7**	2	967	25.0	**968**	**25.0**	970	24.9
462.libquantum	2	**2174**	**19.1**	2174	19.1	2176	19.0	1	**561**	**36.9**	562	36.9	560	37.0
464.h264ref	2	1167	37.9	1168	37.9	**1167**	**37.9**	2	1114	39.7	**1112**	**39.8**	1112	39.8
471.omnetpp	2	706	17.7	705	17.7	**706**	**17.7**	2	656	19.0	655	19.1	**656**	**19.1**
473.astar	2	850	16.5	847	16.6	**848**	**16.5**	2	781	18.0	778	18.0	**781**	**18.0**
483.xalancbmk	2	515	26.8	518	26.6	**518**	**26.6**	2	515	26.8	518	26.6	**518**	**26.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

    All results used taskset to bind processes to cores

Base Compiler Invocation

C benchmarks:
        icc

C++ benchmarks:
        icpc

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
        -fast -inline-calloc -opt-malloc-options=3

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
               -prefetch
```

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Peak Optimization Flags (Continued)

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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