SPEC® CINT2006 Result

Intel Corporation

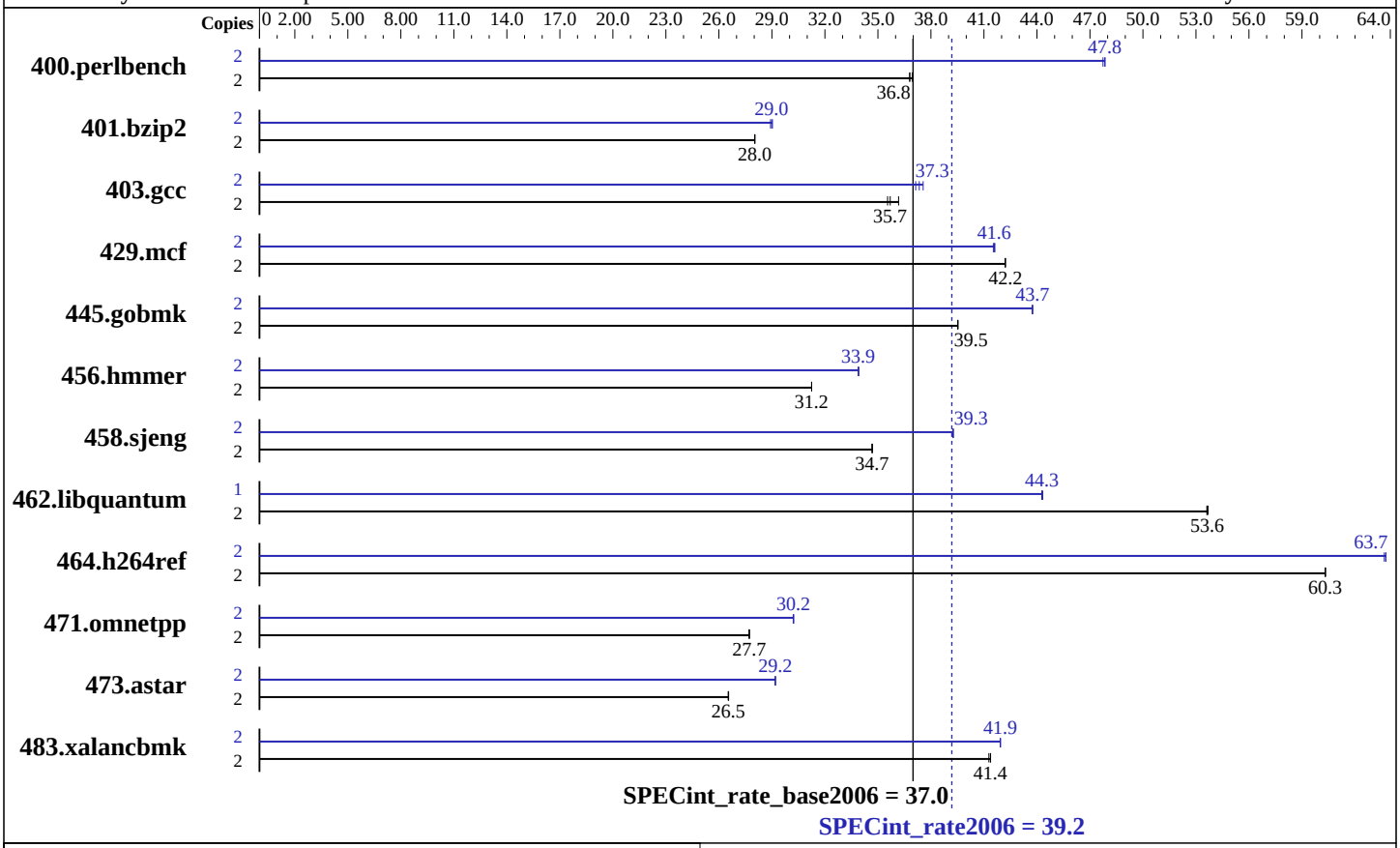
Asus P5E3 Deluxe (Intel Core 2 Duo E6850)

SPECint®_rate2006 = 39.2

SPECint_rate_base2006 = 37.0

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo E6850
CPU Characteristics:	3.00 GHz 1333 MHz FSB
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Corsair TWIN3X2048-1333C9DHX DDR3-1333 CL9)
Disk Subsystem:	Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista64 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	529	36.9	531	36.8	**531**	**36.8**	2	408	47.8	**408**	**47.8**	409	47.7
401.bzip2	2	688	28.0	**689**	**28.0**	689	28.0	2	**666**	**29.0**	667	28.9	665	29.0
403.gcc	2	445	36.2	453	35.5	**451**	**35.7**	2	429	37.6	434	37.1	**431**	**37.3**
429.mcf	2	432	42.2	**432**	**42.2**	432	42.2	2	**439**	**41.6**	438	41.6	439	41.5
445.gobmk	2	**531**	**39.5**	531	39.5	531	39.5	2	479	43.8	480	43.7	**480**	**43.7**
456.hmmer	2	**597**	**31.2**	597	31.2	597	31.2	2	551	33.9	550	33.9	**551**	**33.9**
458.sjeng	2	698	34.7	**698**	**34.7**	698	34.7	2	617	39.3	**616**	**39.3**	616	39.3
462.libquantum	2	**773**	**53.6**	772	53.7	773	53.6	1	468	44.3	**468**	**44.3**	467	44.3
464.h264ref	2	**734**	**60.3**	734	60.3	734	60.3	2	694	63.7	**694**	**63.7**	695	63.7
471.omnetpp	2	451	27.7	451	27.7	**451**	**27.7**	2	413	30.2	414	30.2	**414**	**30.2**
473.astar	2	**529**	**26.5**	529	26.5	529	26.6	2	481	29.2	**481**	**29.2**	481	29.2
483.xalancbmk	2	334	41.3	334	41.4	**334**	**41.4**	2	**329**	**41.9**	329	41.9	329	41.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 11/2007:
http://www.asus.com/products.aspx?l1=3&l2=11&l3=572&l4=0&model=1872&modelmenu=1
The system bus runs at 1333 MHz
System was configured with Asus EN8800GTX discrete graphics card
Binaries were built on Windows Vista32
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
The start command with the /affinity switch was used to bind processes to cores
OMP_NUM_THREADS was set to number of logical processors as seen by the OS
KMP_AFFINITY was set to physical,0
submit was disabled for 462.libquantum at peak.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`

Continued on next page

Base Portability Flags (Continued)

464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`-fast -Qvec-guard-write /F512000000`

C++ benchmarks:
`-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc8 -Qc99`

C++ benchmarks:
`icl -Qvc8`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000

429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:36:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 November 2007.