SPEC® CINT2006 Result
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Hewlett-Packard Company

ProLiant BL460c
(3.16 GHz, Intel Xeon X5460)

SPECint®_rate2006 = Not Run
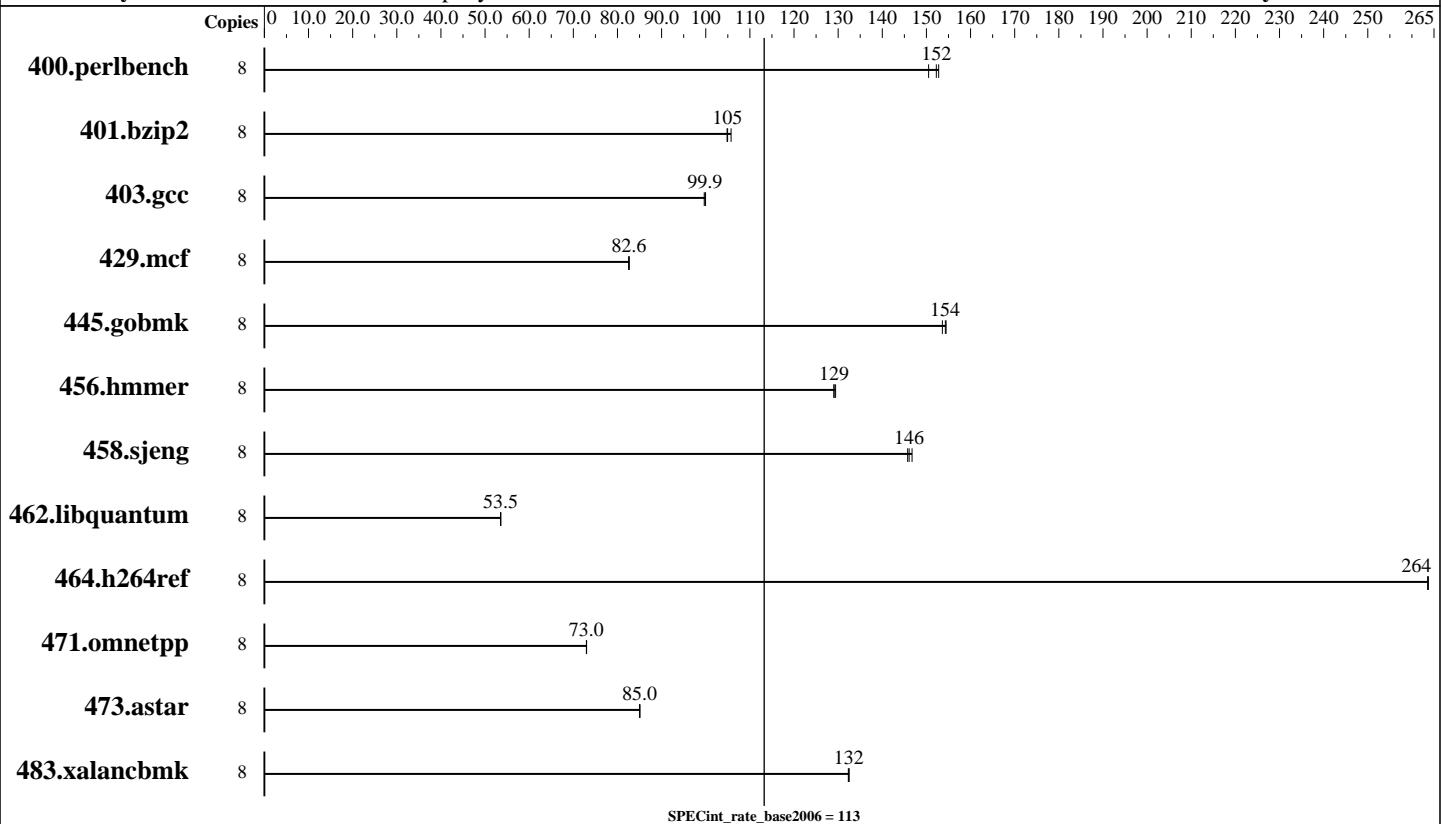
SPECint_rate_base2006 = 113

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Benchmark	Copies	Result
400.perlbench	8	152
401.bzip2	8	105
403.gcc	8	99.9
429.mcf	8	82.6
445.gobmk	8	154
456.hmmer	8	129
458.sjeng	8	146
462.libquantum	8	53.5
464.h264ref	8	264
471.omnetpp	8	73.0
473.astar	8	85.0
483.xalancbmk	8	132

SPECint_rate_base2006 = 113

Hardware

CPU Name: Intel Xeon X5460
CPU Characteristics: 3.16 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz: 3166
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x72 GB 15 K SAS
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: No
File System: ext2
System State: Multi-user run level 3
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: MicroQuill SmartHeap Library 8.1 binutils-2.17.50

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	512	153	519	150	**513**	**152**							
401.bzip2	8	**736**	**105**	736	105	730	106							
403.gcc	8	646	99.7	**645**	**99.9**	644	100							
429.mcf	8	883	82.7	884	82.6	**884**	**82.6**							
445.gobmk	8	**544**	**154**	543	154	546	154							
456.hmmer	8	577	129	578	129	**578**	**129**							
458.sjeng	8	664	146	660	147	**663**	**146**							
462.libquantum	8	**3096**	**53.5**	3096	53.5	3097	53.5							
464.h264ref	8	671	264	**671**	**264**	672	264							
471.omnetpp	8	685	73.0	685	73.0	**685**	**73.0**							
473.astar	8	661	85.0	660	85.1	**661**	**85.0**							
483.xalancbmk	8	**417**	**132**	417	132	417	132							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
      Power Regulator set to Static High Performance Mode
      Adjacent Sector Prefetch Disabled
      Hardware Prefetcher Disabled

Base Compiler Invocation

C benchmarks:
      icc

C++ benchmarks:
      icpc

Base Portability Flags

     400.perlbench: -DSPEC_CPU_LINUX_IA32
     462.libquantum: -DSPEC_CPU_LINUX
     483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

      403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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