



SPEC® CINT2006 Result

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Intel Corporation

Intel Desktop Board DQ35JO (Intel Core 2 Duo E8200)

SPECint®_rate2006 = 36.8

SPECint_rate_base2006 = 33.8

CPU2006 license: 13

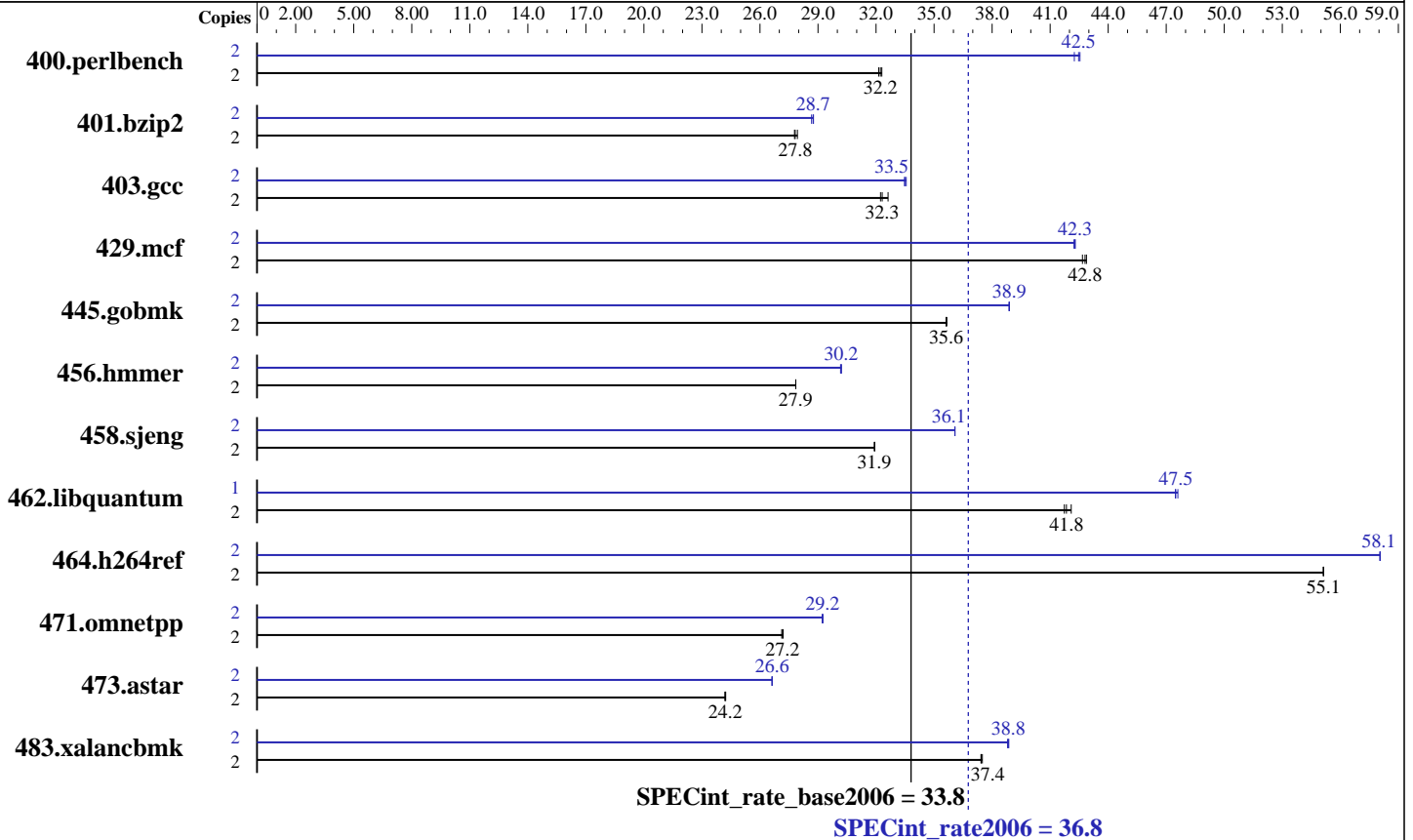
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E8200
 CPU Characteristics: 2.66 GHz, 1333 FSB
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Micron DDR2-800 CL5)
 Disk Subsystem: Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	605	32.3	608	32.1	606	32.2	2	460	42.5	462	42.3	459	42.5
401.bzip2	2	694	27.8	695	27.8	691	27.9	2	671	28.8	671	28.7	673	28.7
403.gcc	2	494	32.6	500	32.2	498	32.3	2	481	33.5	480	33.6	481	33.5
429.mcf	2	425	42.9	427	42.7	426	42.8	2	432	42.2	431	42.3	431	42.3
445.gobmk	2	589	35.6	589	35.6	588	35.7	2	539	38.9	539	38.9	540	38.9
456.hammer	2	670	27.9	670	27.9	670	27.8	2	619	30.2	618	30.2	618	30.2
458.sjeng	2	758	31.9	758	31.9	758	31.9	2	671	36.1	671	36.1	671	36.1
462.libquantum	2	990	41.8	985	42.1	993	41.7	1	436	47.5	435	47.6	436	47.5
464.h264ref	2	803	55.1	803	55.1	803	55.1	2	762	58.1	762	58.1	762	58.1
471.omnetpp	2	460	27.2	460	27.2	461	27.1	2	428	29.2	427	29.3	428	29.2
473.astar	2	580	24.2	580	24.2	580	24.2	2	527	26.6	527	26.6	527	26.6
483.xalancbmk	2	369	37.4	369	37.4	368	37.5	2	355	38.8	355	38.9	356	38.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 03/2008:

<http://www.intel.com/products/motherboard/DQ35JO/index.htm>

The system bus runs at 1333 MHz

System was configured with Asus EN8800GTX discrete graphics card

Binaries were built on Windows Vista Ultimate (32-bit)

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

OMP_NUM_THREADS was set to number of logical processors as seen by the OS

KMP_AFFINITY was set to physical,0

submit was disabled for 462.libquantum at peak.

Base Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

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Base Portability Flags (Continued)

483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-fast -Qvec-guard-write /F512000000

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000

429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000

462.libquantum: -fast -Qunroll14 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll12 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.xml>

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