



# SPEC® CFP2006 Result

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## IBM Corporation

SPECfp®\_rate2006 = 91.4

IBM System x iDataPlex dx360 (Intel Xeon E5472)

SPECfp\_rate\_base2006 = 83.9

CPU2006 license: 11

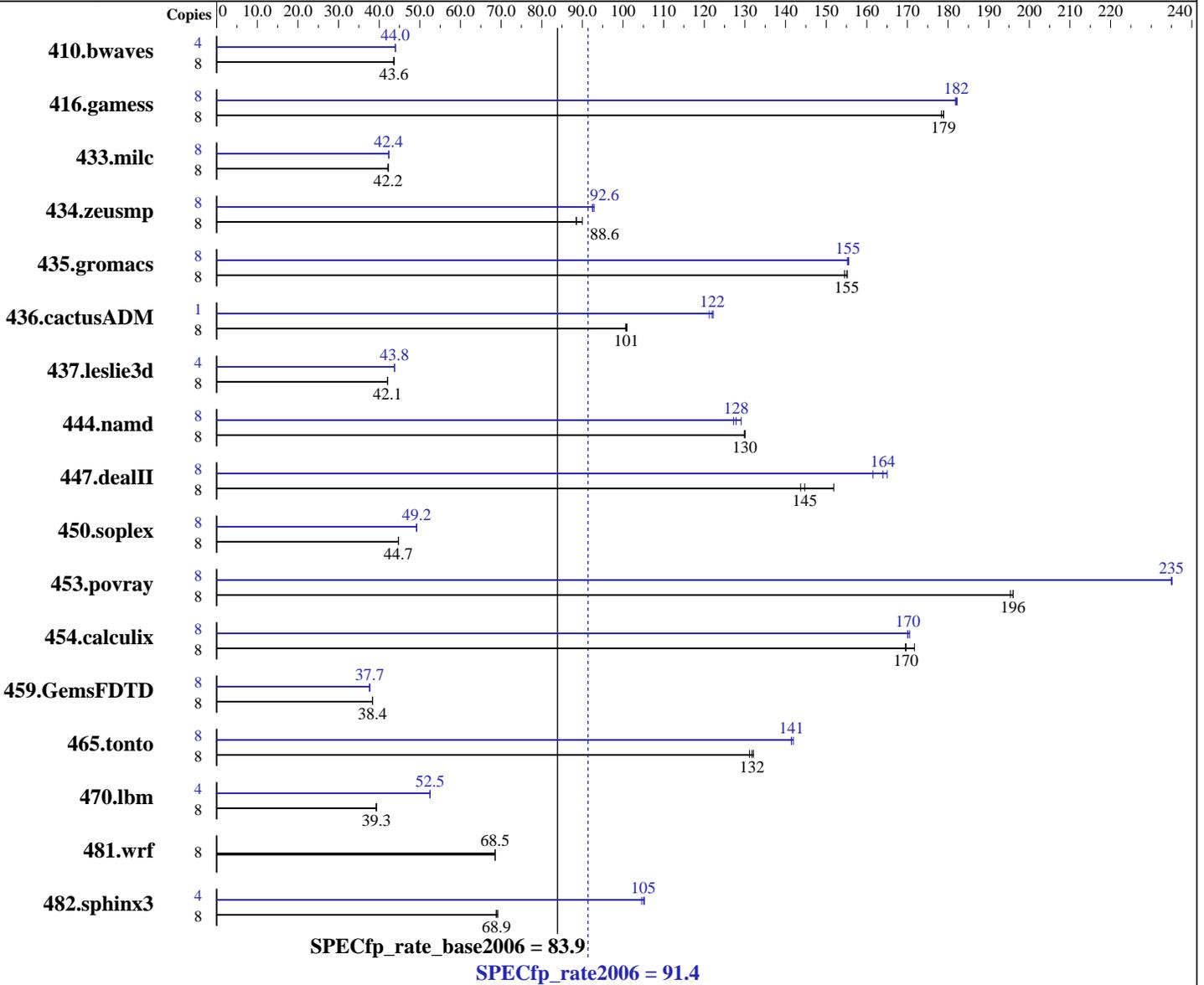
Test date: Oct-2008

Test sponsor: IBM Corporation

Hardware Availability: Nov-2008

Tested by: IBM Corporation

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon E5472  
 CPU Characteristics: 1600MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042, l\_fproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

Continued on next page



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L3 Cache: None  
Other Cache: None  
Memory: 16 GB (8 x 2 GB DDR2-6400F ECC)  
Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	2493	43.6	2491	43.7	<b><u>2492</u></b>	<b><u>43.6</u></b>	4	1236	44.0	<b><u>1236</u></b>	<b><u>44.0</u></b>	1236	44.0
416.gamess	8	876	179	878	178	<b><u>876</u></b>	<b><u>179</u></b>	8	859	182	<b><u>860</u></b>	<b><u>182</u></b>	862	182
433.milc	8	1739	42.2	<b><u>1739</u></b>	<b><u>42.2</u></b>	1740	42.2	8	1734	42.3	1733	42.4	<b><u>1734</u></b>	<b><u>42.4</u></b>
434.zeusmp	8	809	90.0	<b><u>822</u></b>	<b><u>88.6</u></b>	823	88.5	8	784	92.9	787	92.5	<b><u>786</u></b>	<b><u>92.6</u></b>
435.gromacs	8	370	155	368	155	<b><u>368</u></b>	<b><u>155</u></b>	8	367	156	368	155	<b><u>368</u></b>	<b><u>155</u></b>
436.cactusADM	8	950	101	947	101	<b><u>948</u></b>	<b><u>101</u></b>	1	97.8	122	98.6	121	<b><u>98.0</u></b>	<b><u>122</u></b>
437.leslie3d	8	1789	42.0	<b><u>1787</u></b>	<b><u>42.1</u></b>	1786	42.1	4	858	43.8	<b><u>859</u></b>	<b><u>43.8</u></b>	860	43.7
444.namd	8	<b><u>494</u></b>	<b><u>130</u></b>	494	130	493	130	8	<b><u>502</u></b>	<b><u>128</u></b>	497	129	504	127
447.dealII	8	<b><u>632</u></b>	<b><u>145</u></b>	637	144	603	152	8	567	162	555	165	<b><u>558</u></b>	<b><u>164</u></b>
450.soplex	8	1492	44.7	<b><u>1491</u></b>	<b><u>44.7</u></b>	1490	44.8	8	1357	49.2	1355	49.2	<b><u>1356</u></b>	<b><u>49.2</u></b>
453.povray	8	217	196	<b><u>217</u></b>	<b><u>196</u></b>	218	195	8	<b><u>181</u></b>	<b><u>235</u></b>	181	235	181	235
454.calculix	8	389	170	<b><u>389</u></b>	<b><u>170</u></b>	384	172	8	387	171	<b><u>388</u></b>	<b><u>170</u></b>	388	170
459.GemsFDTD	8	<b><u>2212</u></b>	<b><u>38.4</u></b>	2212	38.4	2213	38.4	8	2257	37.6	<b><u>2253</u></b>	<b><u>37.7</u></b>	2253	37.7
465.tonto	8	600	131	596	132	<b><u>597</u></b>	<b><u>132</u></b>	8	<b><u>557</u></b>	<b><u>141</u></b>	555	142	557	141
470.lbm	8	2798	39.3	<b><u>2798</u></b>	<b><u>39.3</u></b>	2798	39.3	4	1046	52.6	1047	52.5	<b><u>1047</u></b>	<b><u>52.5</u></b>
481.wrf	8	<b><u>1304</u></b>	<b><u>68.5</u></b>	1305	68.5	1303	68.6	8	<b><u>1304</u></b>	<b><u>68.5</u></b>	1305	68.5	1303	68.6
482.sphinx3	8	2266	68.8	2253	69.2	<b><u>2264</u></b>	<b><u>68.9</u></b>	4	<b><u>742</u></b>	<b><u>105</u></b>	741	105	745	105

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores except for 436.cactusADM peak

## General Notes

OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M  
Hardware Prefetch Enabled and Adjacent Sector Prefetch Disabled  
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run



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CPU2006 license: 11

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## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch  
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -fno-alias -auto-ilp32

447.dealIII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -Ob0 -ansi-alias  
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4 -auto

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -opt-prefetch -parallel  
-auto-ilp32

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## Peak Optimization Flags (Continued)

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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