



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®_rate2006 = 61.3

Dell Precision T7400 (Intel Xeon X5272, 3.40 GHz)

SPECfp_rate_base2006 = 60.0

CPU2006 license: 55

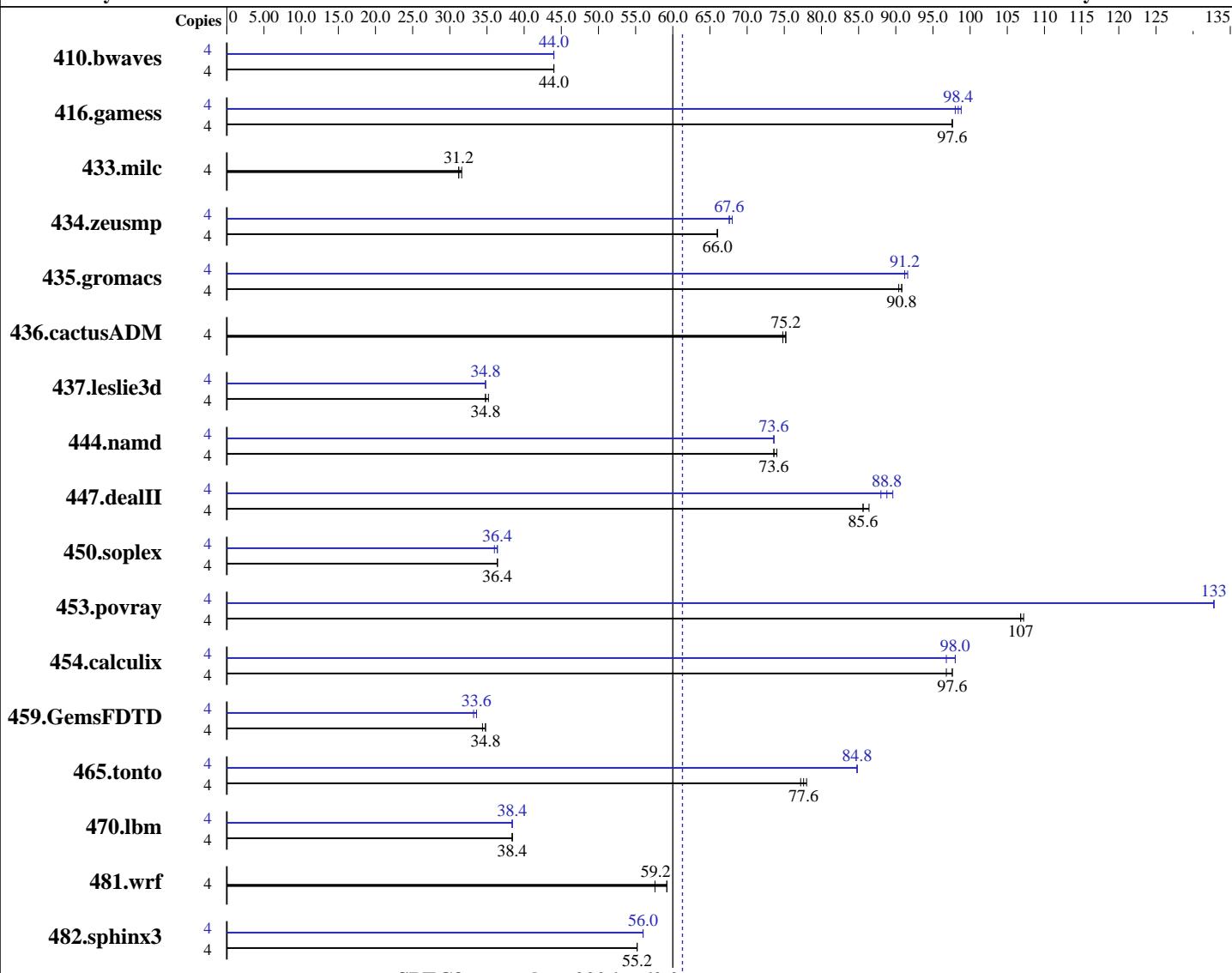
Test date: Dec-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



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Hardware

CPU Name: Intel Xeon X5272
CPU Characteristics: 1600 MHz Bus Speed
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip

Software

Operating System: Windows Vista Business SP1 (64-bit)
Compiler: Intel C++ Compiler for Intel 64, Version 11.0
Build 20080930 Package ID: w_cproc_p_11.0.061
Intel Visual Fortran Compiler for Intel 64,
Version 11.0
Build 20080930 Package ID: w_cprof_p_11.0.061
Microsoft Visual Studio 2008 SP1
Auto Parallel: No
File System: NTFS

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L3 Cache:	None	System State:	Default
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	16 GB (8x2 GB DDR2-800 FB-DIMM, CL5)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 80 GB SATA 7200 RPM	Other Software:	MicroQuill SmartHeap Library 8.1 for x64
Other Hardware:	None		

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1238	44.0	1240	44.0	1240	44.0	4	1238	44.0	1239	44.0	1239	44.0
416.gamess	4	803	97.6	803	97.6	803	97.6	4	799	98.0	795	98.4	793	98.8
433.milc	4	1168	31.6	1170	31.2	1174	31.2	4	1168	31.6	1170	31.2	1174	31.2
434.zeusmp	4	551	66.0	552	66.0	551	66.0	4	535	68.0	538	67.6	539	67.6
435.gromacs	4	316	90.4	315	90.8	315	90.8	4	312	91.6	313	91.2	313	91.2
436.cactusADM	4	636	75.2	636	75.2	638	74.8	4	636	75.2	636	75.2	638	74.8
437.leslie3d	4	1074	35.2	1076	34.8	1076	34.8	4	1077	34.8	1078	34.8	1078	34.8
444.namd	4	435	73.6	434	74.0	435	73.6	4	436	73.6	436	73.6	437	73.6
447.dealII	4	530	86.4	535	85.6	535	85.6	4	520	88.0	516	88.8	510	89.6
450.soplex	4	920	36.4	921	36.4	920	36.4	4	921	36.4	921	36.4	922	36.0
453.povray	4	199	107	199	107	199	107	4	160	133	160	133	160	133
454.calculix	4	339	97.6	339	97.6	340	96.8	4	337	98.0	342	96.8	337	98.0
459.GemsFDTD	4	1232	34.4	1213	34.8	1213	34.8	4	1273	33.2	1262	33.6	1261	33.6
465.tonto	4	509	77.2	506	77.6	505	78.0	4	464	84.8	465	84.8	463	84.8
470.lbm	4	1432	38.4	1433	38.4	1434	38.4	4	1438	38.4	1433	38.4	1433	38.4
481.wrf	4	757	59.2	776	57.6	756	59.2	4	757	59.2	776	57.6	756	59.2
482.sphinx3	4	1408	55.2	1409	55.2	1411	55.2	4	1390	56.0	1394	56.0	1393	56.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries were built on Windows Vista Ultimate (64-bit)

BIOS Settings

Adjacent Cache Line Prefetch set to ON

Base Compiler Invocation

C benchmarks:

 icl -Qvc9 -Qstd=c99

C++ benchmarks:

 icl -Qvc9

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_P64 /assume:underscore
416.gamess: -DSPEC_CPU_P64
  433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
  437.leslie3d: -DSPEC_CPU_P64
  444.namd: -DSPEC_CPU_P64 /TP
  447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
  450.soplex: -DSPEC_CPU_P64
  453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
  454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
  465.tonto: -DSPEC_CPU_P64
  470.lbm: -DSPEC_CPU_P64
  481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64
```

Base Optimization Flags

C benchmarks:

```
-QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000
```

C++ benchmarks:

```
-QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qcxx_features /F512000000 shlw64m.lib
-link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
-QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000
```

Benchmarks using both Fortran and C:

```
-QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000
```



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Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch  
/F512000000
```

```
482.sphinx3: -QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qunroll2  
/F512000000
```

C++ benchmarks:

```
444.namd: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)  
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa /F512000000  
shlw64m.lib -link /FORCE:MULTIPLE
```

```
447.dealII: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)  
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2  
-Qansi-alias -Qscalar-rep- /F512000000 shlw64m.lib  
-link /FORCE:MULTIPLE
```

```
450.soplex: -Qprof_gen(pass 1) -QxSSE4.1 -Qauto-ilp32  
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F512000000  
shlw64m.lib -link /FORCE:MULTIPLE
```

```
453.povray: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)  
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4  
-Qansi-alias /F512000000 shlw64m.lib  
-link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

```
410.bwaves: -QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div-
-Qopt-prefetch /F10000000000
416.gamess: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0
-Qansi-alias -Qscalar-rep- /F10000000000
434.zeusmp: -Qprof_gen(pass 1) -QxSSE4.1 -Qauto-ilp32
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F10000000000
437.leslie3d: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F10000000000
459.GemsFDTD: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0
-Qopt-prefetch /F10000000000
465.tonto: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto
/F10000000000
```

Benchmarks using both Fortran and C:

```
435.gromacs: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qauto-ilp32(pass 2)
-Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F10000000000
436.cactusADM: basepeak = yes
454.calculix: -QxSSE4.1 -Qauto-ilp32 -Qipo -O3 -Qprec-div- /F10000000000
481.wrf: basepeak = yes
```

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.xml>



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Tested with SPEC CPU2006 v1.1.

Report generated on Tue Jul 22 21:30:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 24 December 2008.