SPEC® CFP2006 Result
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Bull SAS

NovaScale R440 E2
(Intel Xeon E5502, 1.86 GHz)

$SPECfp^®\_rate2006 =$ 71.0
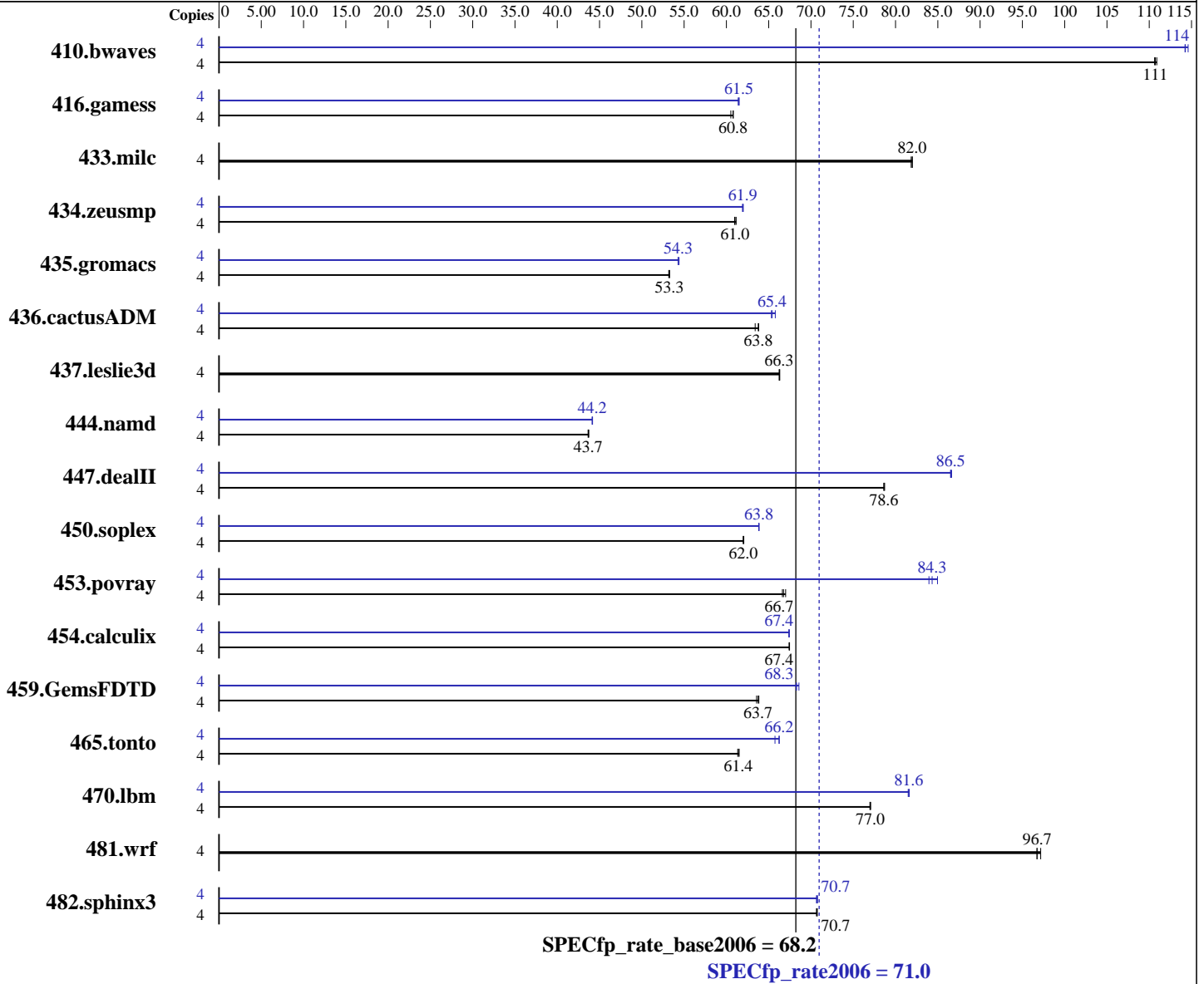
$SPECfp\_rate\_base2006 =$ 68.2

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: Mar-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

Copies		
410.bwaves	4	114
	4	111
416.gamess	4	61.5
	4	60.8
433.milc	4	82.0
434.zeusmp	4	61.9
	4	61.0
435.gromacs	4	54.3
	4	53.3
436.cactusADM	4	65.4
	4	63.8
437.leslie3d	4	66.3
444.namd	4	44.2
	4	43.7
447.dealII	4	86.5
	4	78.6
450.soplex	4	63.8
	4	62.0
453.povray	4	84.3
	4	66.7
454.calculix	4	67.4
	4	67.4
459.GemsFDTD	4	68.3
	4	63.7
465.tonto	4	66.2
	4	61.4
470.lbm	4	81.6
	4	77.0
481.wrf	4	96.7
482.sphinx3	4	70.7
	4	70.7

SPECfp_rate_base2006 = 68.2
SPECfp_rate2006 = 71.0

Hardware

CPU Name: Intel Xeon E5502
CPU Characteristics:
CPU MHz: 1867
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081, l_cprof_p_11.0.081
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)

Continued on next page

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L3 Cache:	4 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	48 GB (12 X 4 GB PC3-8500R running at 800 MHz)	Other Software:	Binutils 2.18.50.0.7.20080502
Disk Subsystem:	1x146.5 GB SAS, 15000 RPM		
Other Hardware:	None		

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	490	111	491	111	**491**	**111**	4	476	114	474	115	**476**	**114**
416.gamess	4	1288	60.8	1294	60.5	**1288**	**60.8**	4	1274	61.5	**1274**	**61.5**	1276	61.4
433.milc	4	**448**	**82.0**	449	81.8	448	82.0	4	**448**	**82.0**	449	81.8	448	82.0
434.zeusmp	4	597	61.0	**596**	**61.0**	595	61.2	4	588	61.9	587	62.0	**588**	**61.9**
435.gromacs	4	537	53.2	**536**	**53.3**	536	53.3	4	526	54.3	525	54.4	**526**	**54.3**
436.cactusADM	4	**750**	**63.8**	749	63.8	754	63.4	4	732	65.3	727	65.8	**731**	**65.4**
437.leslie3d	4	567	66.3	**567**	**66.3**	568	66.2	4	567	66.3	**567**	**66.3**	568	66.2
444.namd	4	735	43.6	734	43.7	**735**	**43.7**	4	726	44.2	**727**	**44.2**	727	44.1
447.dealII	4	**582**	**78.6**	582	78.6	581	78.7	4	**529**	**86.5**	529	86.5	528	86.6
450.soplex	4	**538**	**62.0**	538	62.1	538	62.0	4	**523**	**63.8**	522	63.9	523	63.8
453.povray	4	**319**	**66.7**	319	66.6	318	67.0	4	253	84.0	250	85.0	**252**	**84.3**
454.calculix	4	489	67.5	**489**	**67.4**	490	67.4	4	489	67.4	490	67.4	**490**	**67.4**
459.GemsFDTD	4	**666**	**63.7**	668	63.6	665	63.8	4	619	68.6	622	68.2	**621**	**68.3**
465.tonto	4	642	61.3	640	61.5	**641**	**61.4**	4	599	65.7	**594**	**66.2**	594	66.3
470.lbm	4	713	77.1	**714**	**77.0**	714	77.0	4	674	81.5	**674**	**81.6**	673	81.6
481.wrf	4	460	97.2	462	96.7	**462**	**96.7**	4	460	97.2	462	96.7	**462**	**96.7**
482.sphinx3	4	1104	70.6	1102	70.7	**1102**	**70.7**	4	**1103**	**70.7**	1104	70.6	1101	70.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
   The config file option 'submit' was used.
    numactl was used to bind copies to the cores
```

Operating System Notes

```
    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
    BIOS setting:
    NUMA configuration: Enabled
```

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General Notes

```
The NEC Express5800/R120a-1(Intel Xeon E5502),
the NEC Express5800/R120a-2(Intel Xeon E5502),
the Bull NovaScale R440 E2 (Intel Xeon E5502, 1.86 GHz) and
the Bull NovaScale R460 E2 (Intel Xeon E5502, 1.86 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/R120a-2(Intel Xeon E5502) model.
```

Base Compiler Invocation

C benchmarks:
    `icc`

C++ benchmarks:
    `icpc`

Fortran benchmarks:
    `ifort`

Benchmarks using both Fortran and C:
    `icc ifort`

Base Portability Flags

```
  410.bwaves: -DSPEC_CPU_LP64
  416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
   447.dealII: -DSPEC_CPU_LP64
   450.soplex: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
    `-xSSE4.2 -ipo -O3 -no-prec-div -static`

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

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Peak Optimization Flags

C benchmarks:

    433.milc: basepeak = yes

    470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
             -auto-ilp32

    482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

    444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -fno-alias -auto-ilp32

    447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll2 -ansi-alias -scalar-rep-

    450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -opt-malloc-options=3

    453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll4 -ansi-alias

Fortran benchmarks:

    410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll2 -Ob0 -ansi-alias -scalar-rep-

    434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

    437.leslie3d: basepeak = yes

    459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                  -unroll2 -Ob0 -opt-prefetch

    465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -unroll4 -auto

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch` `-auto-ilp32`

436.cactusADM: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2)
`-unroll2` `-opt-prefetch` `-auto-ilp32`

454.calculix: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revG.html
http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.20090710.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revG.xml
http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.20090710.xml

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