



SPEC® CINT2006 Result

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ASUSTeK Computer Inc.

SPECint®_rate2006 = 258

ASUS TS500-E6 (Z8NA-D6) server system (Intel Xeon X5570)

SPECint_rate_base2006 = 240

CPU2006 license: 9016

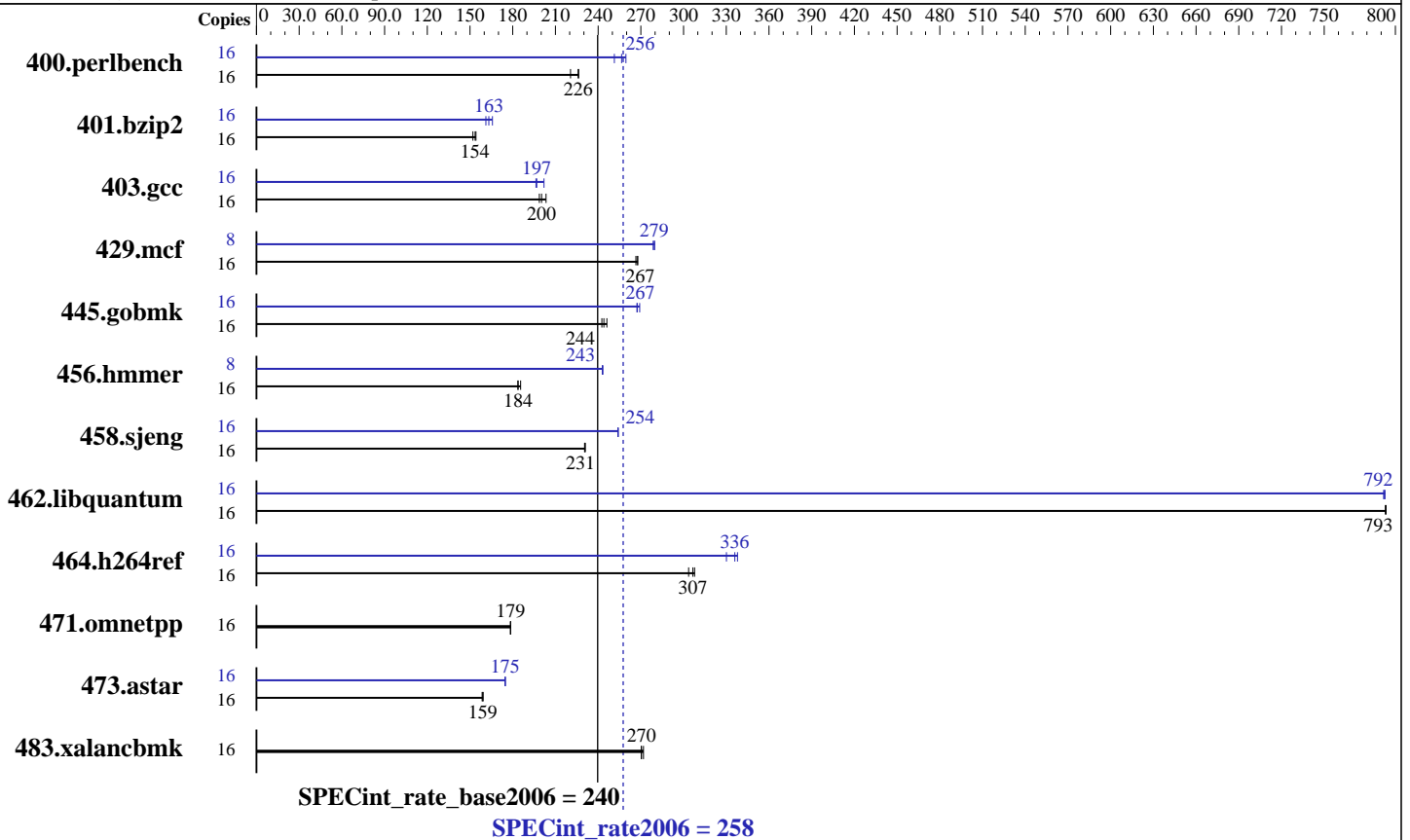
Test date: May-2009

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Mar-2009

Tested by: ASUSTeK Computer Inc.

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 X 4 GB PC3-10600R, CL=9)
 Disk Subsystem: HITACHI HDT725050VLA360 500GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Kernel linux 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	709	221	<u>692</u>	<u>226</u>	691	226	16	602	260	<u>609</u>	<u>256</u>	622	251
401.bzip2	16	1001	154	1016	152	<u>1005</u>	<u>154</u>	16	932	166	958	161	<u>946</u>	<u>163</u>
403.gcc	16	<u>643</u>	<u>200</u>	633	203	648	199	16	638	202	<u>654</u>	<u>197</u>	656	196
429.mcf	16	545	268	548	267	<u>546</u>	<u>267</u>	8	<u>261</u>	<u>279</u>	262	279	261	280
445.gobmk	16	<u>688</u>	<u>244</u>	682	246	692	243	16	624	269	628	267	<u>628</u>	<u>267</u>
456.hammer	16	813	184	<u>812</u>	<u>184</u>	805	186	8	307	243	<u>307</u>	<u>243</u>	307	243
458.sjeng	16	838	231	840	230	<u>839</u>	<u>231</u>	16	<u>762</u>	<u>254</u>	763	254	762	254
462.libquantum	16	418	793	418	793	<u>418</u>	<u>793</u>	16	419	792	<u>418</u>	<u>792</u>	418	793
464.h264ref	16	1166	304	1151	308	<u>1155</u>	<u>307</u>	16	<u>1054</u>	<u>336</u>	1048	338	1073	330
471.omnetpp	16	561	178	<u>560</u>	<u>179</u>	560	179	16	561	178	<u>560</u>	<u>179</u>	560	179
473.astar	16	706	159	<u>706</u>	<u>159</u>	709	159	16	<u>643</u>	<u>175</u>	643	175	643	175
483.xalancbmk	16	406	272	408	270	<u>408</u>	<u>270</u>	16	406	272	408	270	<u>408</u>	<u>270</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Component Notes

Tested system case compliance with Intel EEB 3.61 spec
SSI Server Power Supply 650W or higher
System was configured with ASPEED AST2050 VGA (on board VGA)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):
icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 01:37:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 June 2009.