SPEC® CINT2006 Result

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Intel Corporation

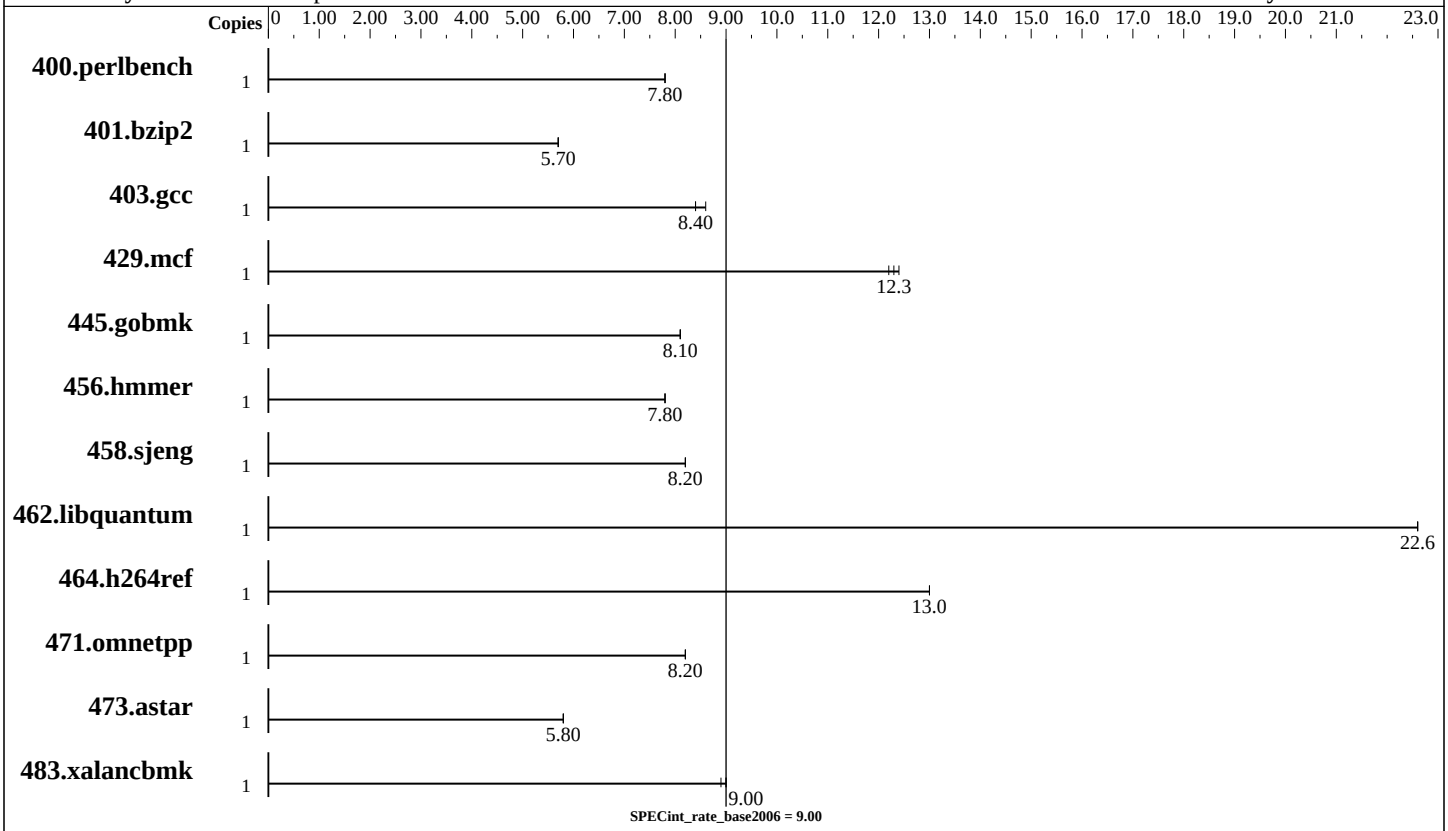
Acer Aspire 3410T (Intel Celeron M 723)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 9.00

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2009
Hardware Availability: May-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Celeron M 723
CPU Characteristics:	
CPU MHz:	1200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per chip
Secondary Cache:	1 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB DDR3-1066 CL7; bios sets it to DDR2-800 CL5)
Disk Subsystem:	ST9250315AS 250 GB SATA, 5400RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Home Premium w/ SP1 (32-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1	1252	7.80	**1253**	**7.80**	1253	7.80							
401.bzip2	1	**1693**	**5.70**	1690	5.70	1700	5.70							
403.gcc	1	958	8.40	**955**	**8.40**	937	8.60							
429.mcf	1	746	12.2	737	12.4	**740**	**12.3**							
445.gobmk	1	1288	8.10	**1289**	**8.10**	1290	8.10							
456.hmmer	1	1191	7.80	1192	7.80	**1191**	**7.80**							
458.sjeng	1	1467	8.20	**1468**	**8.20**	1470	8.20							
462.libquantum	1	**917**	**22.6**	917	22.6	917	22.6							
464.h264ref	1	**1701**	**13.0**	1702	13.0	1700	13.0							
471.omnetpp	1	758	8.20	758	8.20	**758**	**8.20**							
473.astar	1	**1217**	**5.80**	1217	5.80	1215	5.80							
483.xalancbmk	1	772	8.90	769	9.00	**770**	**9.00**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
The system bus runs at 800 MHz
Binaries were built on Windows Vista Ultimate (32-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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