



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®_rate2006 = 142

IBM BladeCenter LS42 (AMD Opteron 8435)

SPECfp_rate_base2006 = 128

CPU2006 license: 11

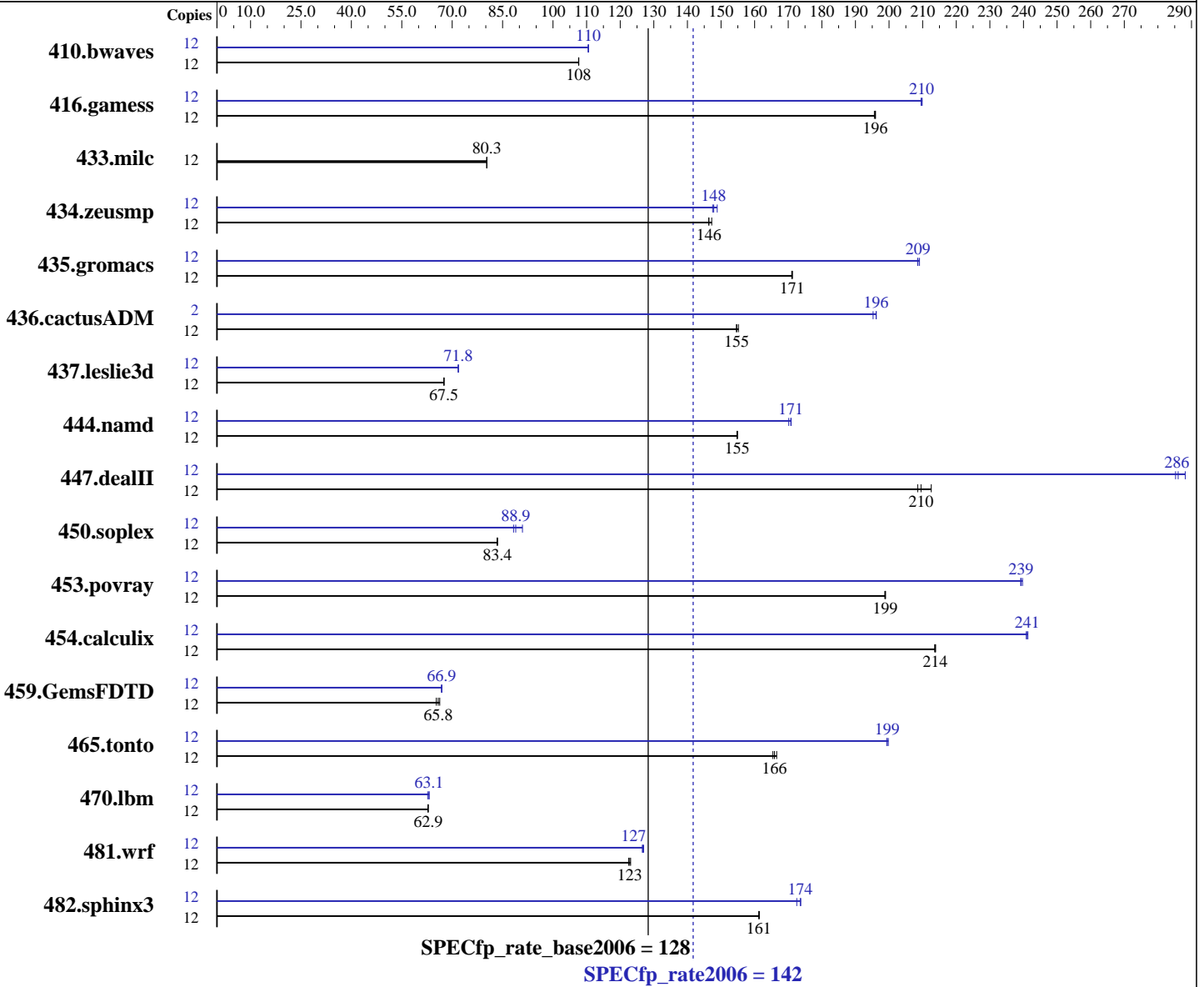
Test date: Jun-2009

Test sponsor: IBM Corporation

Hardware Availability: Sep-2009

Tested by: IBM Corporation

Software Availability: Apr-2009



Hardware

CPU Name: AMD Opteron 8435
 CPU Characteristics:
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5 on an x86_64
 Compiler: PGI Server Complete Version 8.0 x86 Open64 4.2.2 Compiler Suite (from AMD)
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (8 x 4 GB, PC2-6400 ECC)
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
 Other Hardware: None

Other Software: binutils 2.18

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1515	108	1516	108	1515	108	12	1475	111	1476	110	1476	110
416.gamess	12	1199	196	1199	196	1201	196	12	1121	210	1121	210	1120	210
433.milc	12	1372	80.3	1372	80.3	1372	80.3	12	1372	80.3	1372	80.3	1372	80.3
434.zeusmp	12	742	147	746	146	746	146	12	739	148	740	148	734	149
435.gromacs	12	501	171	500	171	500	171	12	410	209	411	209	411	209
436.cactusADM	12	928	155	927	155	924	155	2	122	196	122	195	122	196
437.leslie3d	12	1670	67.5	1670	67.5	1669	67.6	12	1571	71.8	1571	71.8	1570	71.8
444.namd	12	621	155	622	155	622	155	12	566	170	564	171	563	171
447.dealII	12	646	213	655	210	658	209	12	481	285	476	288	480	286
450.soplex	12	1200	83.4	1200	83.4	1199	83.5	12	1126	88.9	1134	88.3	1101	90.9
453.povray	12	321	199	321	199	321	199	12	267	239	267	239	266	240
454.calculix	12	463	214	463	214	464	214	12	410	241	411	241	411	241
459.GemsFDTD	12	1920	66.3	1934	65.8	1951	65.3	12	1904	66.9	1905	66.8	1902	66.9
465.tonto	12	709	167	712	166	714	165	12	591	200	592	199	592	199
470.lbm	12	2627	62.8	2622	62.9	2623	62.9	12	2613	63.1	2629	62.7	2611	63.1
481.wrf	12	1094	122	1091	123	1089	123	12	1058	127	1059	127	1056	127
482.sphinx3	12	1450	161	1449	161	1450	161	12	1347	174	1355	173	1346	174

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
 'ulimit -l 2097152' was used to set environment locked pages in memory limit
 Set vm/nr_hugepages=5400 in /etc/sysctl.conf
 mount -t hugetlbfs nodev /mnt/hugepages



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General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "450"

LD_LIBRARY_PATH = "/cpu2006/amd0905is-libs/64:/cpu2006/amd0905is-libs/32"

NCPUS = "6"

PGI_HUGE_PAGES = "450"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>.

Base Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks:

pgcpp

Fortran benchmarks:

pgf95

Benchmarks using both Fortran and C:

pgcc pgf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -Mnomain
 436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -Mnomain
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Bstatic_pgi

C++ benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed --zc_eh -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

Fortran benchmarks:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mvect=short -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

Benchmarks using both Fortran and C:

-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Mvect=short -Bstatic_pgi

Base Other Flags

C benchmarks:

-Mipa=jobs:4

C++ benchmarks:

-Mipa=jobs:4

Fortran benchmarks:

-Mipa=jobs:4

Benchmarks using both Fortran and C:

-Mipa=jobs:4

Peak Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks (except as noted below):

openCC

444.namd: pgcpp

Fortran benchmarks (except as noted below):

openf95

410.bwaves: pgf95

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Peak Compiler Invocation (Continued)

434.zeusmp: pgf95

437.leslie3d: pgf95

Benchmarks using both Fortran and C (except as noted below):

pgcc pgf95

435.gromacs: opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64
 436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -Mnomain
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -fastsse -Msmartalloc=huge -Mprefetch=t0 -Mloop32
-Mfprelaxed -Mipa=fast -Mipa=inline -tp shanghai-64
-Bstatic_pgi

482.sphinx3: -Mphi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mfprelaxed -Msmartalloc -tp shanghai-64 -Bstatic_pgi

C++ benchmarks:

444.namd: -Mphi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
-Mipa=inline(pass 2) -fastsse -Munroll=n:4 -Munroll=m:8
-Msmartalloc=huge -Mnodepchk -Mfprelaxed --zc_eh
-tp shanghai-64 -Bstatic_pgi

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Peak Optimization Flags (Continued)

447.deallI: -march=barcelona -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -Wf,-fno-exceptions -m32 -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m
-GRA:unspill=on -CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on
-HP:bdt=2m:heap=2m

Fortran benchmarks:

410.bwaves: -fastsse -Msmartalloc -Mprefetch=nta -Mfprelaxed
-Mipa=fast -Mipa=inline -tp shanghai-64 -Bstatic_pgi

416.gamess: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O2 -OPT:Ofast -OPT:ro=3
-OPT:unroll_size=256 -HP:bdt=2m:heap=2m

434.zeusmp: -fastsse -Mfprelaxed -Mprefetch=distance:8 -Mprefetch=t0
-Msmartalloc=huge -Msmartalloc=hugebss -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

437.leslie3d: -Mphi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=fuse -Msmartalloc=huge -Mprefetch=distance:8
-Mprefetch=t0 -Mfprelaxed -tp shanghai-64 -Bstatic_pgi

459.GemsFDTD: -march=barcelona -Ofast -LNO:fission=2 -LNO:simd=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -HP

465.tonto: -march=barcelona -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m

436.cactusADM: -fastsse -Mconcur -Msmartalloc=huge -Mfprelaxed -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

454.calculix: -Mphi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=short -Msmartalloc=huge -Mprefetch=t0 -Mpre

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454.calculix (continued):

-Mfprelaxed -tp shanghai-64 -Bstatic_pgi

481.wrf: -fastsse -Mvect=noaltcode -Msmartalloc=huge

-Mprefetch=distance:8 -Mfprelaxed -tp shanghai-64

-Bstatic_pgi

Peak Other Flags

C benchmarks:

-Mipa=jobs:4(pass 2)

C++ benchmarks:

444.namd: -Mipa=jobs:4(pass 2)

Fortran benchmarks:

410.bwaves: -Mipa=jobs:4

434.zeusmp: -Mipa=jobs:4

437.leslie3d: -Mipa=jobs:4(pass 2)

Benchmarks using both Fortran and C:

436.cactusADM: -Mipa=jobs:4

454.calculix: -Mipa=jobs:4(pass 2)

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090710.html

<http://www.spec.org/cpu2006/flags/x86-open64-4.2.2-flags-revA.20090710.html>

<http://www.spec.org/cpu2006/flags/amd-platform.20090728.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090710.xml

<http://www.spec.org/cpu2006/flags/x86-open64-4.2.2-flags-revA.20090710.xml>

<http://www.spec.org/cpu2006/flags/amd-platform.20090728.xml>



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