



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

### SPECint<sup>®</sup>\_rate2006 = 139

## CELSIUS M470, Intel Xeon W3580

### SPECint\_rate\_base2006 = 131

CPU2006 license: 19

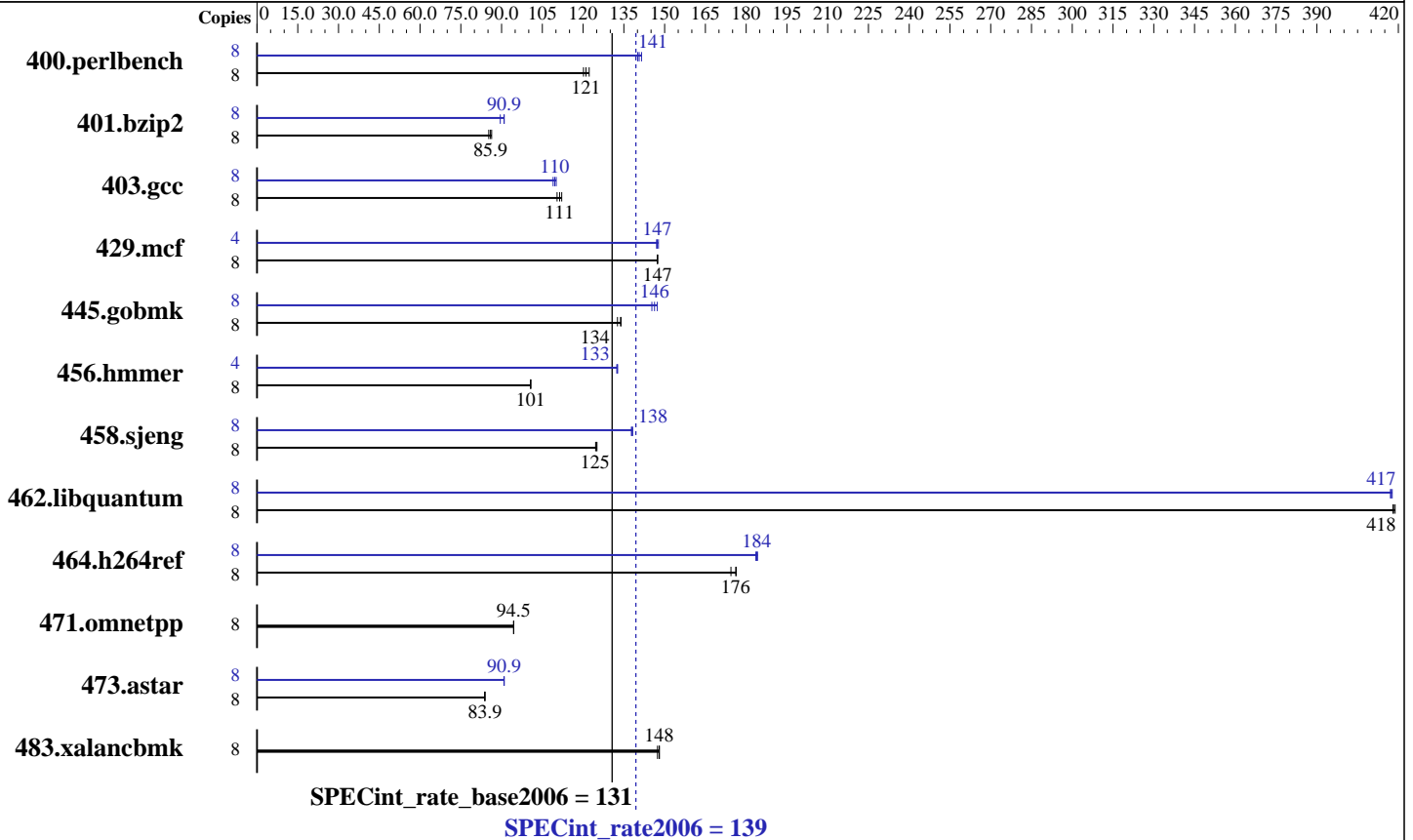
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2009

Hardware Availability: Aug-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon W3580  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.6 GHz  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 12 GB (6x2 GB PC3 10600E, 2 rank, CL9, ECC)  
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 10 (x86\_64) SP2, kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	639	122	651	120	<b>646</b>	<b>121</b>	8	<b>556</b>	<b>141</b>	558	140	552	142
401.bzip2	8	<b>899</b>	<b>85.9</b>	905	85.3	894	86.3	8	862	89.5	<b>849</b>	<b>90.9</b>	849	91.0
403.gcc	8	<b>578</b>	<b>111</b>	583	110	574	112	8	585	110	<b>588</b>	<b>110</b>	591	109
429.mcf	8	<b>495</b>	<b>147</b>	495	147	495	147	4	248	147	247	148	<b>248</b>	<b>147</b>
445.gobmk	8	626	134	<b>627</b>	<b>134</b>	633	133	8	570	147	578	145	<b>574</b>	<b>146</b>
456.hammer	8	<b>741</b>	<b>101</b>	741	101	741	101	4	282	132	<b>281</b>	<b>133</b>	281	133
458.sjeng	8	774	125	<b>775</b>	<b>125</b>	777	125	8	700	138	<b>702</b>	<b>138</b>	703	138
462.libquantum	8	397	418	<b>396</b>	<b>418</b>	396	419	8	397	417	<b>397</b>	<b>417</b>	397	418
464.h264ref	8	1004	176	<b>1005</b>	<b>176</b>	1015	174	8	961	184	<b>963</b>	<b>184</b>	964	184
471.omnetpp	8	<b>529</b>	<b>94.5</b>	530	94.4	529	94.5	8	<b>529</b>	<b>94.5</b>	530	94.4	529	94.5
473.astar	8	669	83.9	<b>670</b>	<b>83.9</b>	670	83.8	8	618	90.9	<b>618</b>	<b>90.9</b>	617	91.0
483.xalancbmk	8	<b>373</b>	<b>148</b>	373	148	375	147	8	<b>373</b>	<b>148</b>	373	148	375	147

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Memory speed set to "Max Performance" (Switch in "Advanced Memory Options")

## General Notes

For information about Fujitsu please visit:  
<http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090818.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090818.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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